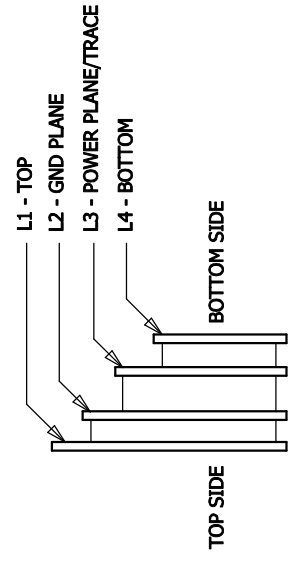


LINEAR TECHNOLOGY DATE: Feb 11, 2014
 DEMO CIRCUIT 1854A Rev3

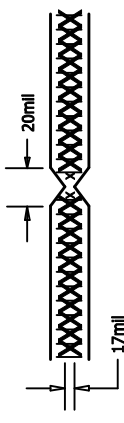
LAYER STRUCTURE



SIZE	QTY	SYM	PLATED	TOL
187	4	+	YES	+/-3mil
95	13	X	YES	+/-3mil
70	2	□	NO	+/-3mil
10	85	⊗	YES	+/-3mil

NOTES : UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
LEAD FREE ASSEMBLY COMPLIANT ISOLA FR-370HR OR EQUIVALENT.
2. MATERIAL:
2 OZ. COPPER FINISH ON EXTERNAL LAYERS.
1 OZ. COPPER FINISH ON INTERNAL LAYERS.
THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
FAMMABILITY SHOWN AND TOLERANCES +/-0.010
0.00 ARE PRIMARY DATUMS.
3. SIZE: DIMENSIONS SHOWN AND TOLERANCES +/-0.010
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, .001 INCH THICK MIN. ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-3mil
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING LPT BOTH SIDES COLOR GREEN.
GOLD IMMERSION BOTH SIDES.
6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
7. DO NOT ALTER ARTWORK TO ADD LOGO OR DATE CODE BUT MAY MODIFY PAD SIZE TO MEET END FINISH.
8. PCB's SHOULD BE RoHS COMPLIANT.
9. SCORING: FOR PANELIZED FABRICATION ONLY



LINEAR TECHNOLOGY 1850 MCCARTHY BLVD SUITE 700 PH: (409) 321-1800 WWW.LINEAR.COM LTC CONFIDENTIAL FOR CUSTOMER USE ONLY		TITLE: FABRICATION DRAWING DUAL OUTPUT BUCK AND INVERTING CONVERTER		IC NO. LT8471EFE DC1854A	REV 3
APPROVALS PCB DES. APP ENG.		SCALE = 1 : 1		FILENAME: 1854A-Rev2.PCB	