Using the LM3648EVM Evaluation Module

User's Guide



Literature Number: SNVU466
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LM3648EVM User's Guide

1 Introduction

The Texas Instruments LM3648EVM evaluation module (EVM) helps designers evaluate the operation and performance of the LM3648 High-Current LED driver. The device offers configurability via I²C-compatible interface. It can be enabled in Flash or Torch mode via the I²C interface or externally using the STROBE and TORCH/TEMP pins. The module utilizes two LEDs (D1 & D2) mounted on the EVM.

The EVM contains one Synchronous Boost LED Flash Driver (See Table 1).

Table 1. Device and Package Configurations

FLASH LED DRIVER	IC	PACKAGE
U1	LM3648	0.4 mm-pitch, 12-Bump DSBGA

2 Setup

This section describes the jumpers and connectors on the EVM as well as how to properly connect, set up, and use the LM3648EVM.

2.1 Input/Output Connector Description

Input / GND - These are the power input terminals for the driver. The terminal block provides a power (VIN) and ground (GND) connection to allow the user to attach the EVM to a cable harness.

EN (J12) - This is the jumper used to enable the LED driver (HWEN pin). The driver will be enabled when the HWEN pin is high (VIO) and disabled when it is low (GND).



Figure 1. Enable Jumper Settings

VIO (J16) - This pin provides power for the I²C lines (Clock & Data) and for the HWEN pin. It is recommended that this pin is connected to the VIN pin. If desired, it can be connected to the 3.3-V line provided by the USB interface connector. In this configuration, communication via the I²C interface may not be possible if the supply voltage to the LED driver is below approximately 3 V.

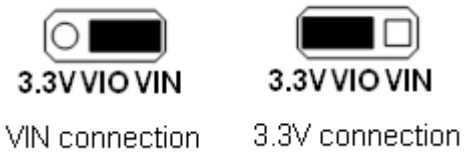


Figure 2. VIO Jumper Settings

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D1-CON (J14 pin3 and pin4) and D2-CON (J14 pin1 and pin2) are the jumpers used to connect the on-board flash LEDs to the LED output of the driver.

STROBE (J9) - This pin provides an external method for initiating a flash event. The STROBE pin is connected to ground via a 300-kΩ resistor internal to the LM3648. To externally drive this pin, either connect a control signal directly to the STROBE pin of the connector or place a jumper between connector pins STROBE and PWM0. Pin PWM0 can be configured as a time-adjustable voltage pulse via the General User Interface (GUI) software provided.



Figure 3. STROBE Jumper Settings

TORCH/TEMP (J21) - This pin provides an external method for initiating a torch event. The TORCH/TEMP pin is connected to ground via a 300-kΩ resistor internal to the LM3648 as well as an externally connected NTC thermistor. To externally drive this pin, either connect a control signal directly to the TORCH/TEMP pin of the connector or place a jumper between connector pins TORCH/TEMP and PWM1. Pin PWM1 can be configured as ON or OFF via the GUI software provided. Removing the jumper, and setting the TORCH/TEMP pin to TEMP mode, the TEMP function can be utilized via the externally connected NTC thermistor.

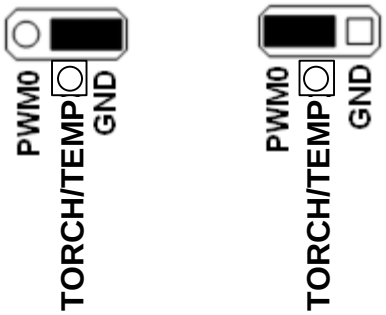


Figure 4. TORCH/TEMP Jumper Settings

TX (J11) - This pin is used to initiate a TX-interrupt event. The TX pin is connected to ground via a 300-kΩ resistor internal to the LM3648. To externally drive this pin, either connect a control signal directly to the TX pin of the connector or place a jumper between connector pins TX and PWM2. Pin PWM2 can be configured as to provide continuous voltage pulses via the GUI software provided.



Figure 5. TX Jumper Settings

SDA / SCL (J19) - These connections allow the user to externally control the I^2C lines. For independent control of the I^2C lines, **do not** connect the VIO jumper to either the 3.3 V or the VIN pin.

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OUT, LED1, TORCH/TEMP (J12, J17, J18) - These provide access to the regulated output of the driver, the outputs of the LED current source, and the TEMP pin. The user can measure VOUT with reference to GND, VLED with reference to GND, current source headroom directly between VOUT and VLED, and can monitor or control TORCH/TEMP input.

VINL/VIN (J7) - The user can monitor the Inductor Current and Input Current waveforms by omitting this jumper and using separate wires from the power supply to the VINL and VIN pins. This will remove the Input Capacitors from the Inductor and eliminate their filtering effect to the Inductor Current.

J13: LED Current measurements -The LM3648EVM provides a way to accurately measure the LED current through the LED on board. Resistor R1 (0.1 Ω) is placed between the cathode of LED1and Ground. The user can first measure the resistor values accurately, by applying a known current through connector D1 and Ground and measuring the voltage between D1S and GNDS. Then, during normal flash or torch operation, the voltage measured across the resistor divided by the resistor value will equal the current through the resistor (and the LED).

2.2 Setup

The input voltage range for the flash driver is 2.5 V to 5.5 V. The on-board LEDs or an LED module should be connected for proper operation.

2.3 Operation

For proper operation of the LM3648EVM, the jumpers should be properly configured. The recommended setting, using shorting blocks is:

VIO to VIN

EN to VIO

STROBE to PWM0 or external signal

TORCH/TEMP to PWM1 or external signal

TX to PWM2 or external signal

LED (J13) shorted

In this configuration, the device will power up when power is applied.

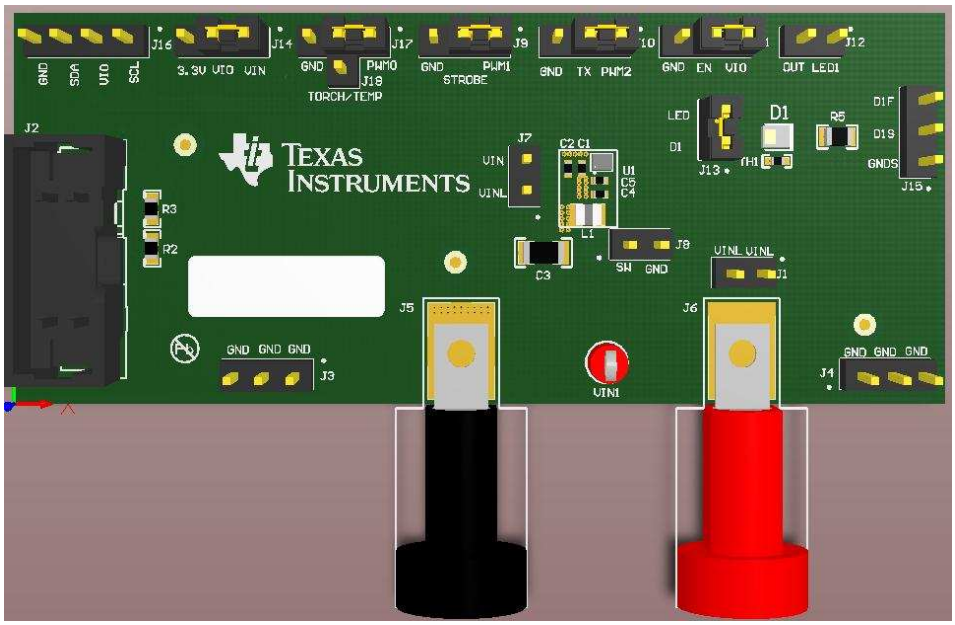


Figure 6. Jumper Configuration

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3 Board Layout

Figure 7, Figure 8, Figure 9 and Figure 10 show the board layout for the LM3648EVM. The EVM offers resistors, capacitors, and jumpers to enable the device and to configure it as desired.

The LM3648 will dissipate power, especially during high current and long duration flash events. Power will also be dissipated on the flash LEDs. The EVM layout is designed to minimize temperature rise during operation. It is recommended that in order to prevent overheating, repeated flash events in very short time intervals is avoided.

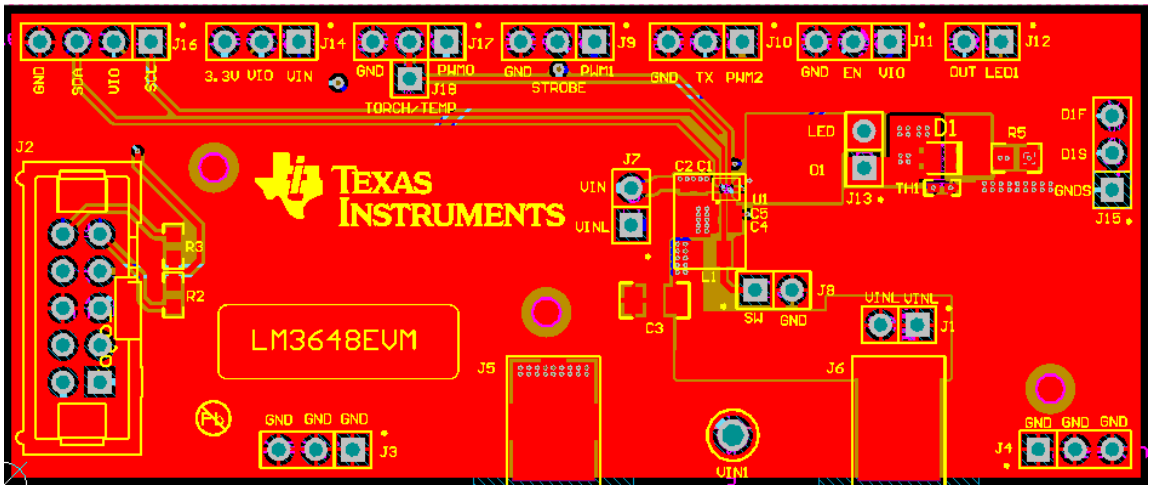


Figure 7. Top Assembly Layer

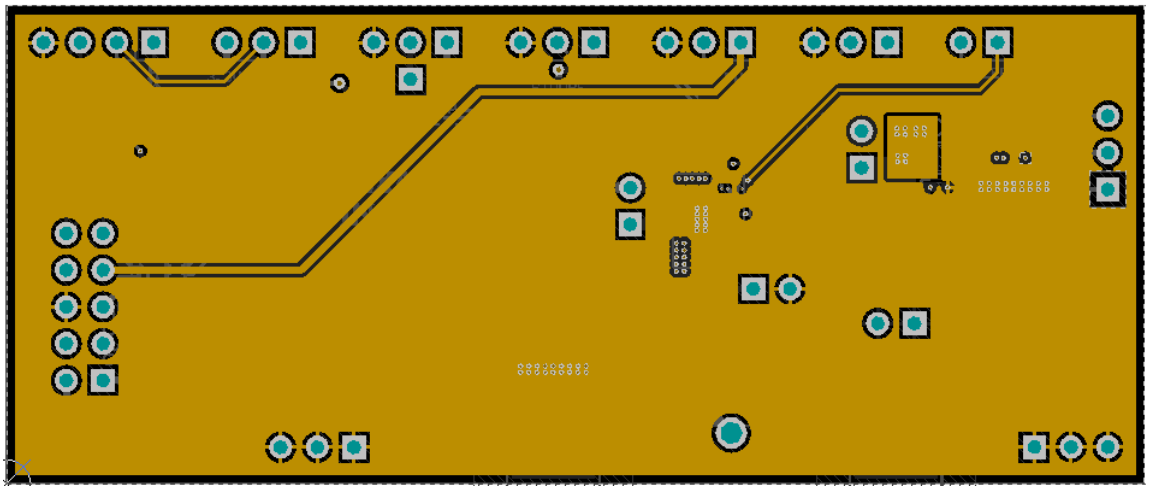


Figure 8. Middle Layer 1 Routing

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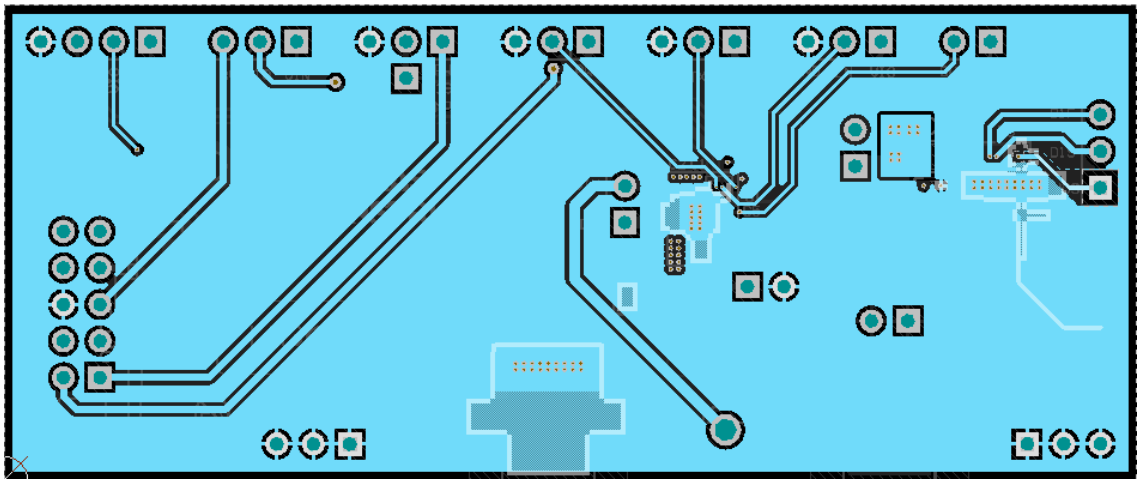


Figure 9. Middle Layer 2 Routing

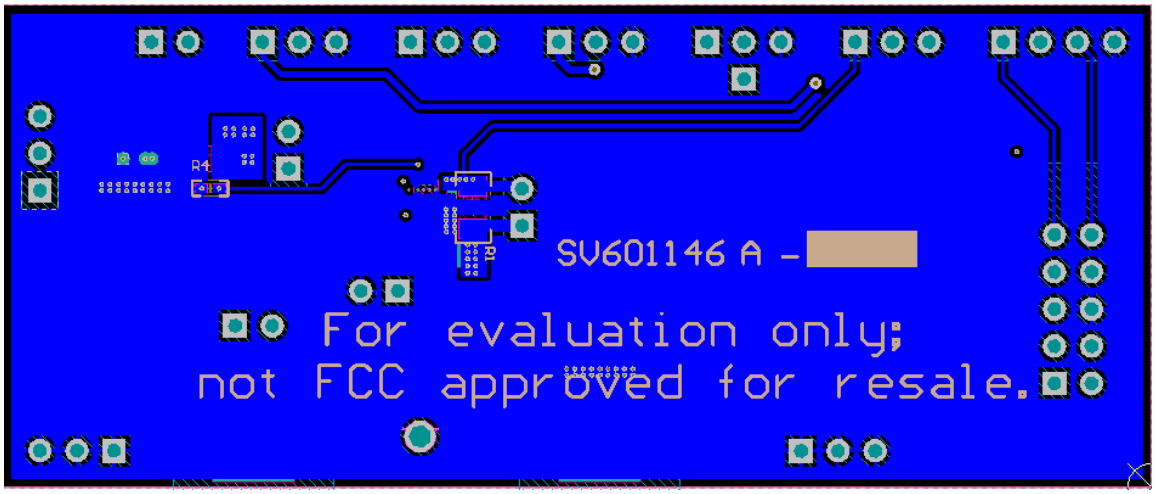


Figure 10. Bottom Assembly Layer (UNMIRRORED)

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4 Schematic

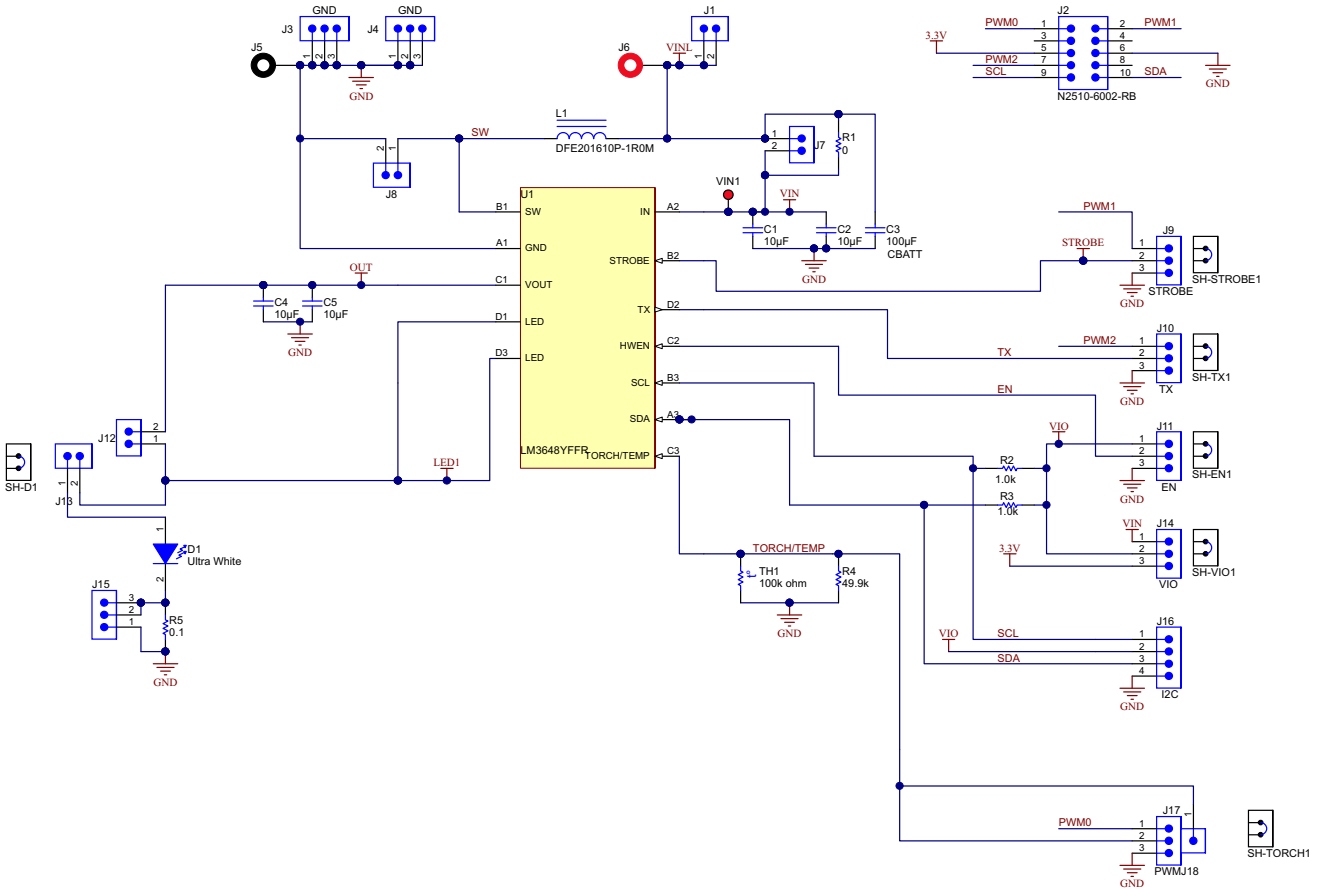


Figure 11. LM3648EVM Schematic

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Table 2. Bill of Materials

DESIGNATOR	DESCRIPTION	MANUFACTURER	PART NUMBER	QTY
!PCB1	Printed Circuit Board	Any	SV601146	1
C1, C2, C4, C5	CAP, CERM, 10uF, 6.3V, +/-20%, X5R, 0402	Samsung	CL05A106MQ5NUNC	4
C3	CAP, CERM, 100uF, 6.3V, +/-20%, X5R, 1206	MuRata	GRM31CR60J107ME39L	1
D1	LED, Ultra White, SMD	OSRAM	LUW CAEP.G4	1
J1, J7, J8, J10, J13	Header, TH, 100mil, 2x1, Gold plated, 230 mil above insulator	Samtec, Inc.	TSW-102-07-G-S	5
J2	Header (shrouded), 100mil, 5x2, High-Temperature, Gold, TH	3M	N2510-6002-RB	1
J3, J4, J9, J10, J11, J14, J15, J17	Header, TH, 100mil, 3x1, Gold plated, 230 mil above insulator	Samtec, Inc.	TSW-103-07-G-S	8
J5	Standard Banana Jack, Insulated, Black	Keystone	6092	1
J6	Standard Banana Jack, Insulated, Red	Keystone	6091	1
J16	Header, TH, 100mil, 4x1, Gold plated, 230 mil above insulator	Samtec, Inc.	TSW-104-07-G-S	1
J18	Header, TH, 100mil, 1pos, Gold plated, 230 mil above insulator	Samtec, Inc.	TSW-101-07-G-S	1
L1	Inductor, Shielded, Metal Composite, 1uH, 2.6A, 0.058 ohm, SMD	Toko	DFE201610P-1R0M	1
LBL1	Thermal Transfer Printable Labels, 0.650" W x 0.200" H - 10,000 per roll	Brady	THT-14-423-10	1
R1	RES, 0 ohm, 5%, 0.25W, 1206	Yageo America	RC1206JR-070RL	1
R2, R3	RES, 1.0k ohm, 5%, 0.1W, 0603	Vishay-Dale	CRCW06031K00JNEA	2
R4	RES, 49.9k ohm, 1%, 0.063W, 0402	Vishay-Dale	CRCW040249K9FKED	1
R5	RES, 0.1 ohm, 5%, 0.125W, 0805	Panasonic	ERJ-6RSJR10V	1
SH-D1, SH-EN1, SH-STROBE1, SH-TORCH1, SH-TX1, SH-VIO1	Shunt, 100mil, Gold plated, Black	3M	969102-0000-DA	6
TH1	Thermistor NTC, 100k ohm, 5%, 0402	MuRata	NCP15WF104J03RC	1
U1	IC LED DRVR PHOTO FLASH 1.5A SMD, YFF0012AEAD	Texas Instruments	LM3648	1
VIN1	Test Point, TH, Compact, Red	Keystone	5005	1

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5 USB Interface Board and I²C-Compatible Interface Program

Texas Instruments has created an I²C-compatible program and USB docking board (USB2ANY) that can help exercise the part in a simple way. Contained in this document is a description of how to use the USB2ANY interface box and interface software.

The LM3648EVM has the means to “plug into” the USB docking board. The USB docking board provides all the control signals for the simple interface. Power to the part must be provided externally. A USB cable (provided) must be connected to the board from a PC.

The I²C-compatible interface program provides all of the control that the LM3648 part requires. For proper operation, the USB docking board should be plugged into the PC before the interface program is opened. Once connected, and the program is executed, a basic interface window will open. The image below shows the default settings.

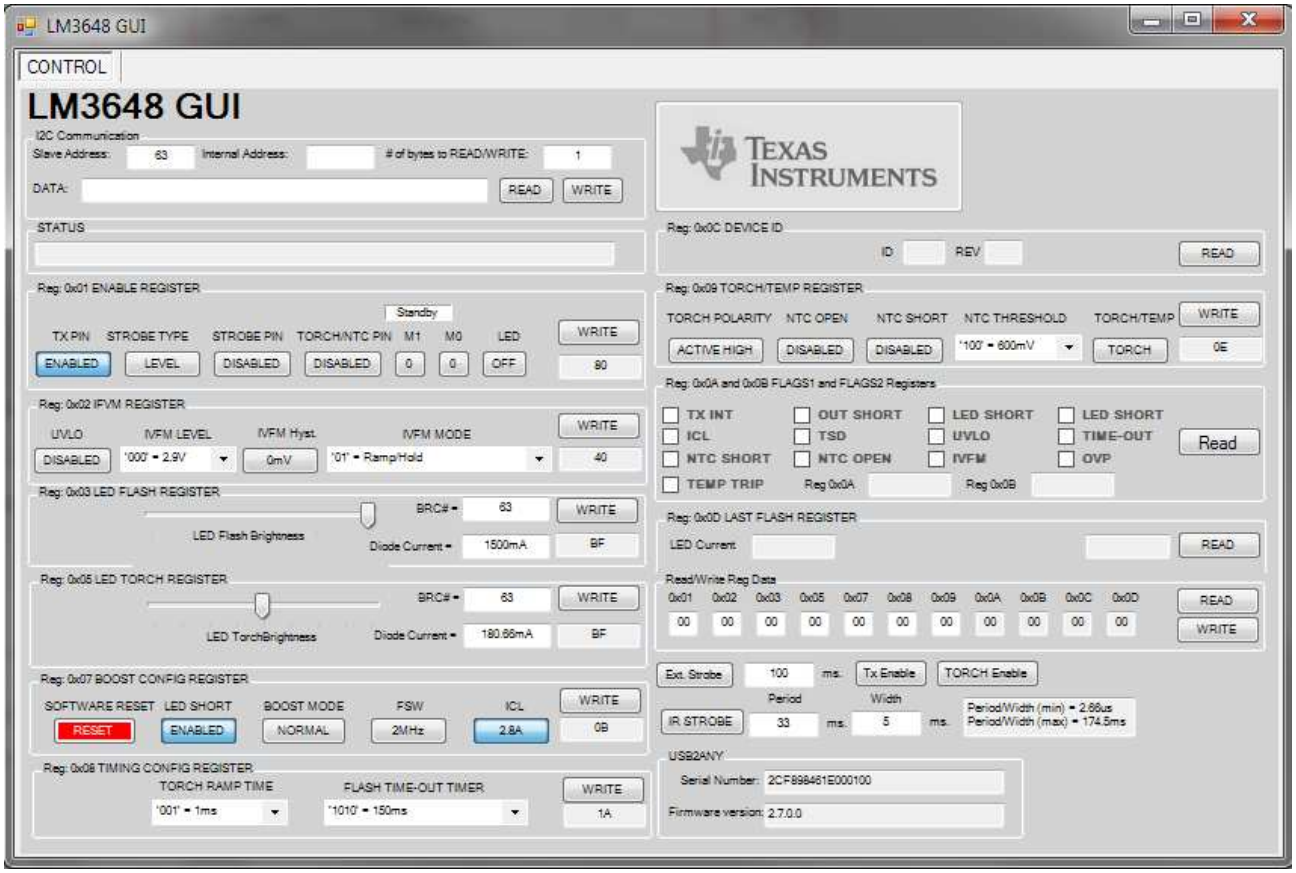


Figure 12. LM3648 General User Interface

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The “I²C Interface” fields may be used to write or read any LM3648 register. Selecting the "Set Default Settings" button resets all registers to their default values and updates all GUI fields.



Figure 13. I²C interface Fields

5.1 User Interface

the LM3648 GUI provides the user with access to all of the registers found on the device. Through a combination of buttons, drop-down boxes and sliders, the user can configure the LM3648 to perform in the desired mode. Please note that no data is written to the device until the Write button found within the corresponding register is pressed.

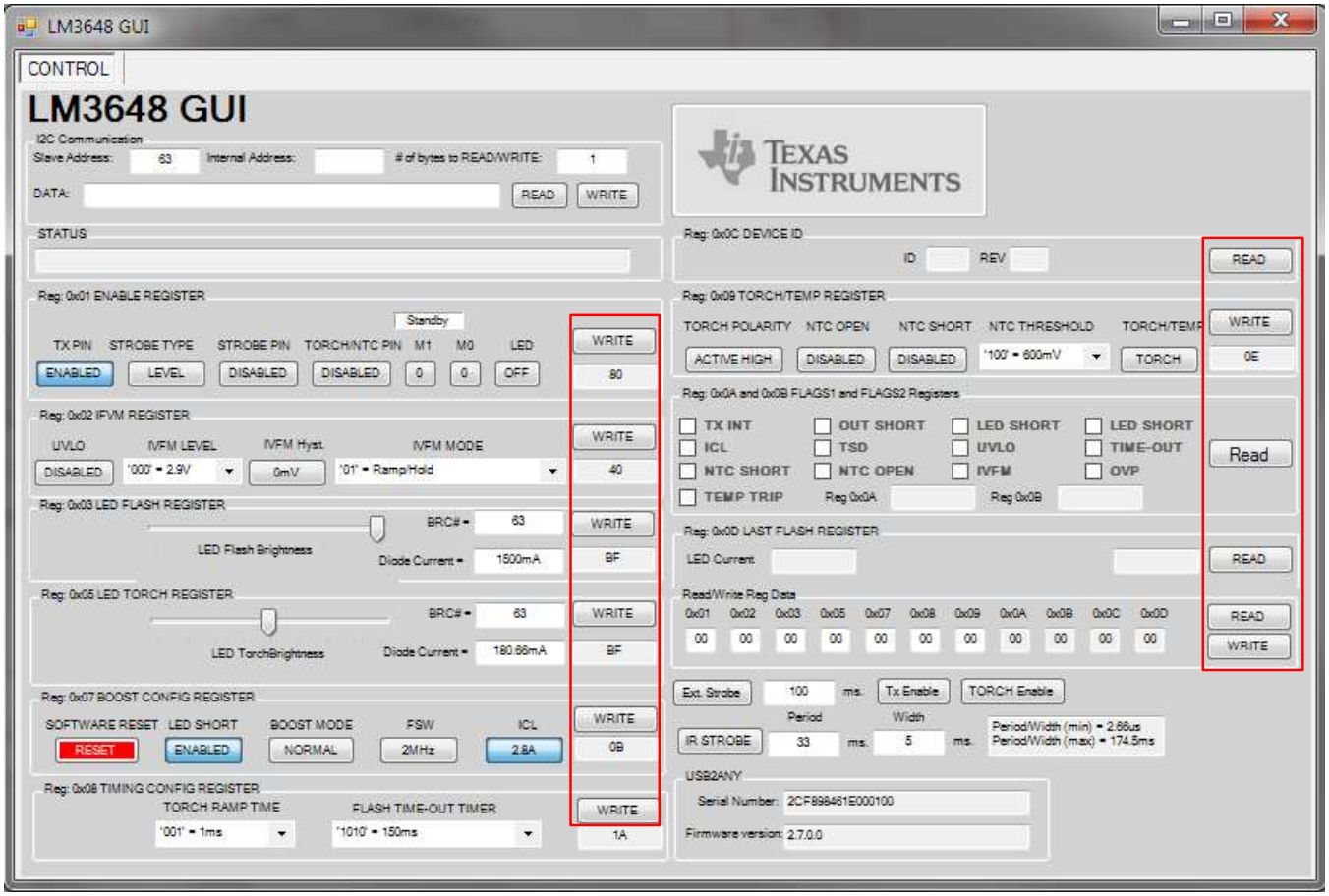


Figure 14. Write Buttons

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5.2 Flags

The contents of the LM3648 fault registers are read upon clicking the “Read Flags” button. The registers are cleared upon read back.

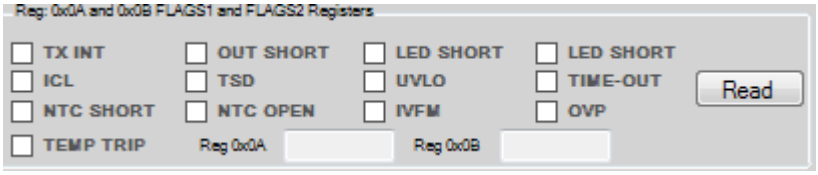


Figure 15. Flags

5.3 I/O Pin Controls

The LM3648EVM provides the user with the capability to control the TORCH, STROBE and TX inputs without the need of an external supply. The Tx Enable Button creates a continuous pulse train when depressed. The Torch EN button toggles the LM3648's TORCH/TEMP pin high when pressed and low depressed. The Ext. Strobe Button toggles the Strobe pin high for the duration entered in the field next to the button.

The IR Strobe Button along with the period and width fields generate a continuous pulse train that can be used to generate a current pulse pattern on the enabled LEDs.

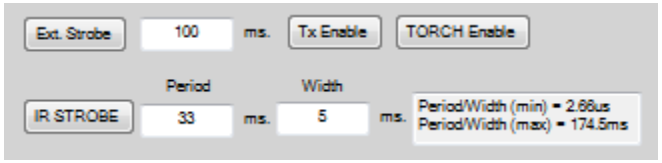


Figure 16. I/O Pin Controls

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3 *Regulatory Notices:*

   3.1 *United States*

      3.1.1 *Notice applicable to EVMs not FCC-Approved:*

      This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

      3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

      CAUTION

      This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

      Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

      FCC Interference Statement for Class A EVM devices

      NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 *Canada*

3.2.1 *For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210*

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 *Japan*

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan are NOT certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, User is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

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