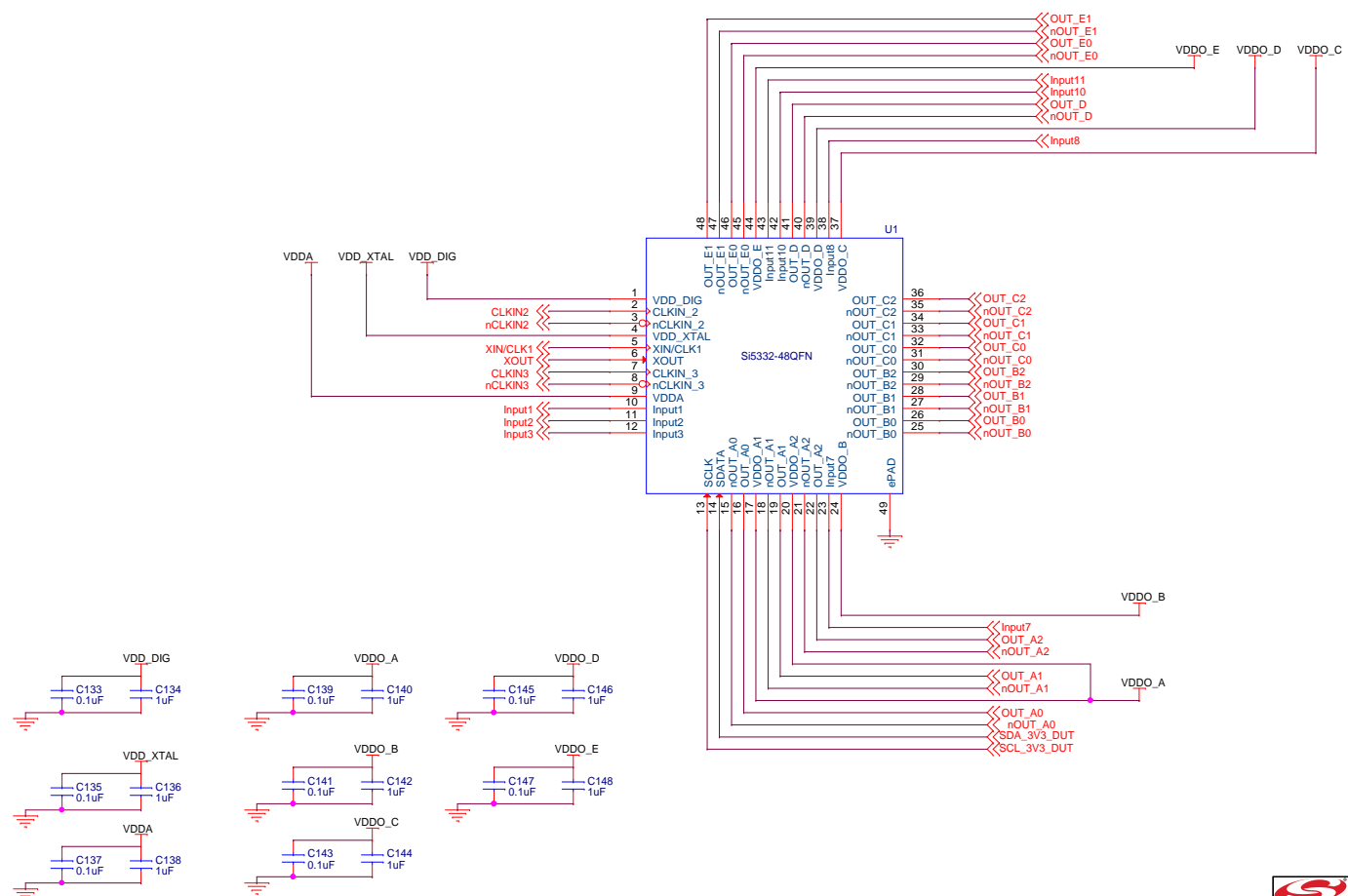
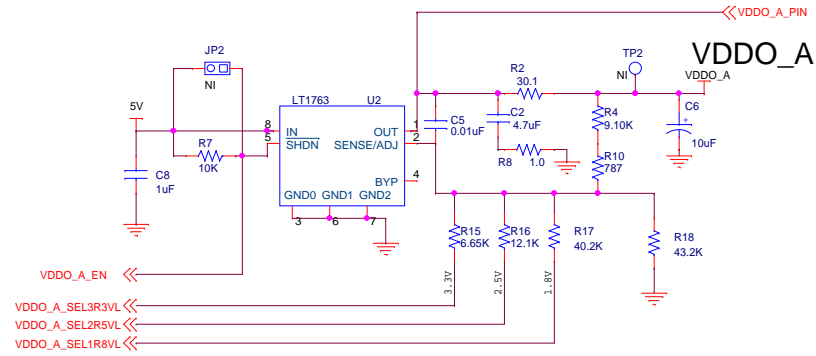


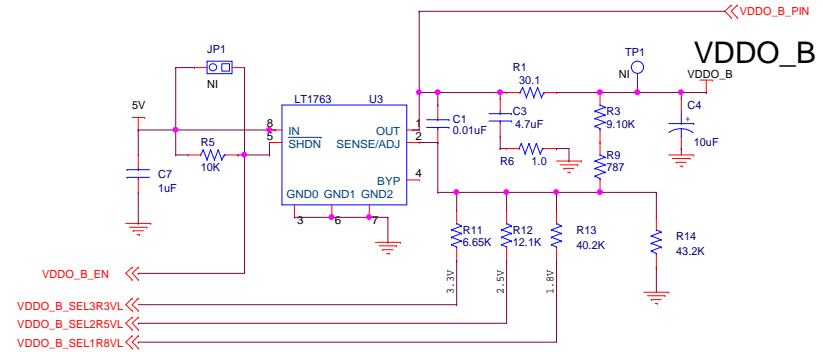
DUT CONNECTIONS



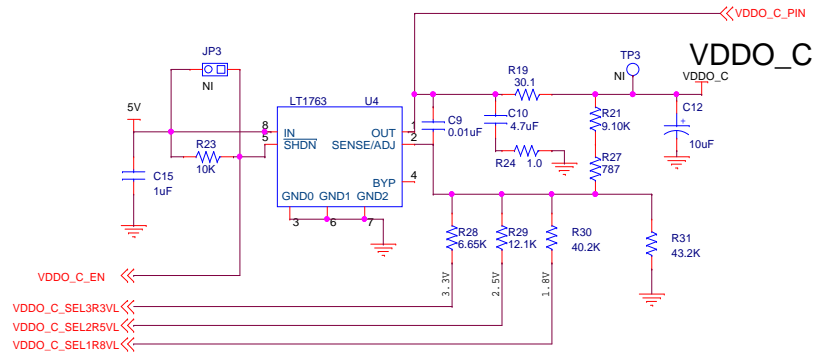
## 500 mA Adjustable Voltage Regulator



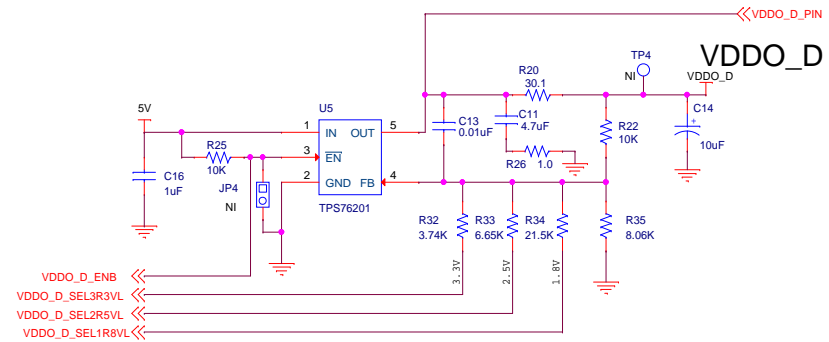
## 500 mA Adjustable Voltage Regulator



## 500 mA Adjustable Voltage Regulator



## 100 mA Adjustable Voltage Regulator



400 W Cesar Chavez  
Austin, TX 78701

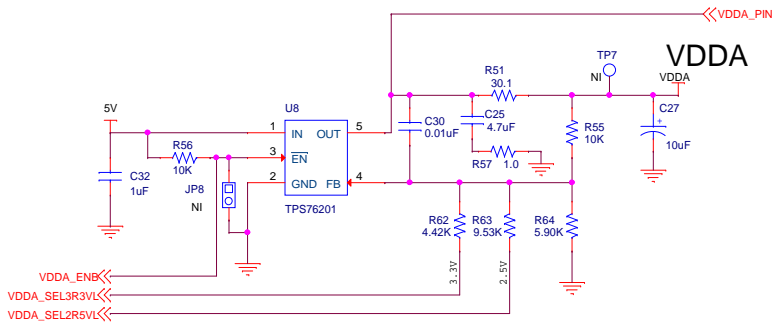
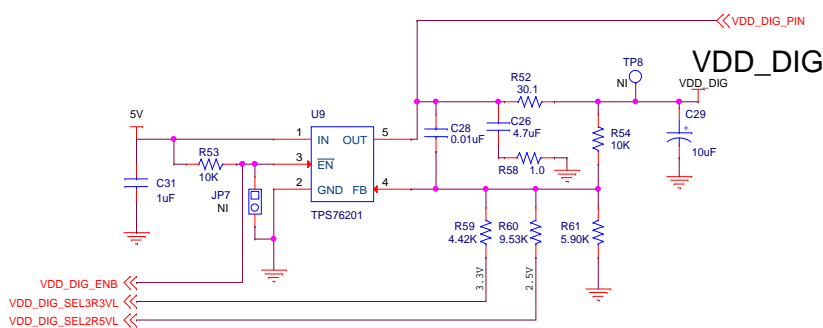
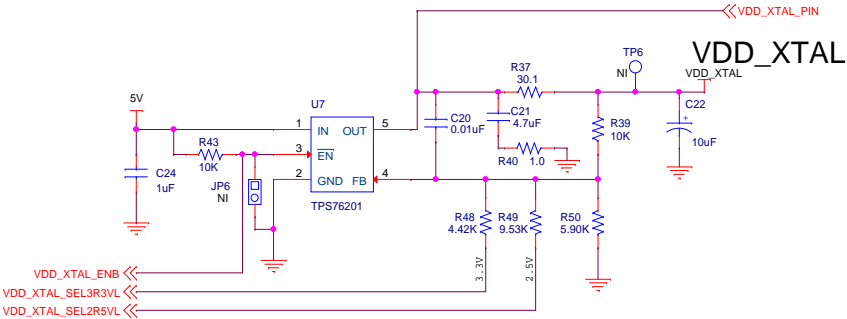
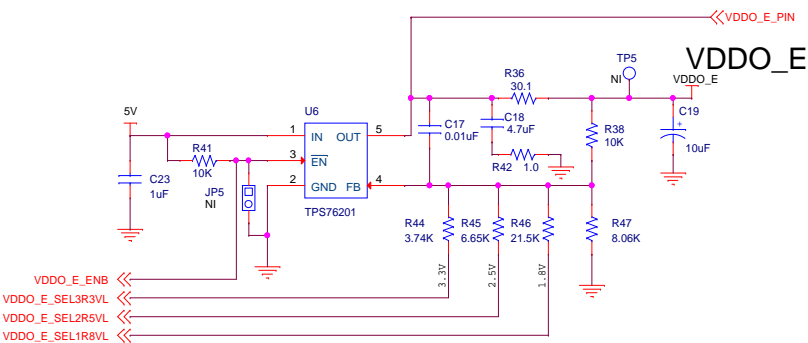
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Size B Document Number  
SIS332 48-PIN QFN CUSTOMER EVB

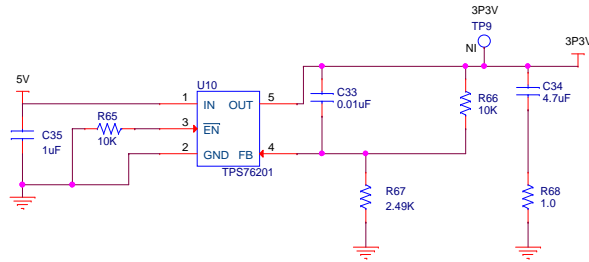
Rev  
1.0

Date: Thursday, December 08, 2016 Sheet 2 of 13

# 100 mA Adjustable Voltage Regulators



# Fixed 3.3V 100 mA Voltage Regulator (Enabled, no current measurement supported.)



3P3V (In Schematic)  
3P3V (Board Silkscreen)



400 W Cesar Chavez  
Austin, TX 78701

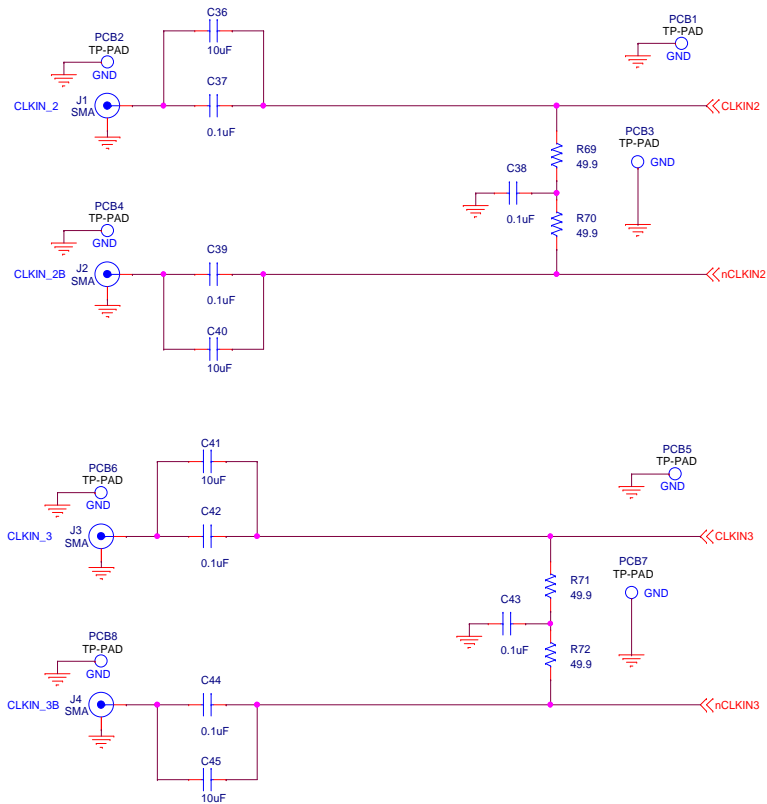
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Size B Document Number  
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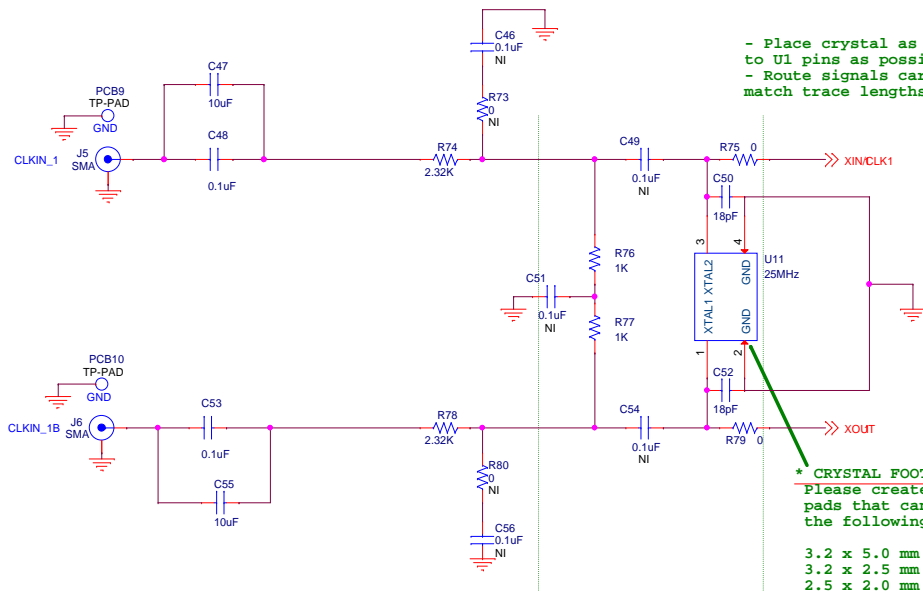
Rev  
1.0

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INPUT CLOCK



# Crystal Input Connections

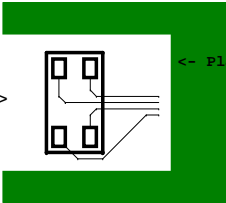


- Place crystal as close to U1 pins as possible!  
- Route signals carefully, match trace lengths!

Keep X as short as possible by placing all components close to the XA/XB pins

\* CRYSTAL FOOTPRINT:  
Please create a footprint with pads that can accommodate the following crystal sizes:  
3.2 x 5.0 mm  
3.2 x 2.5 mm  
2.5 x 2.0 mm

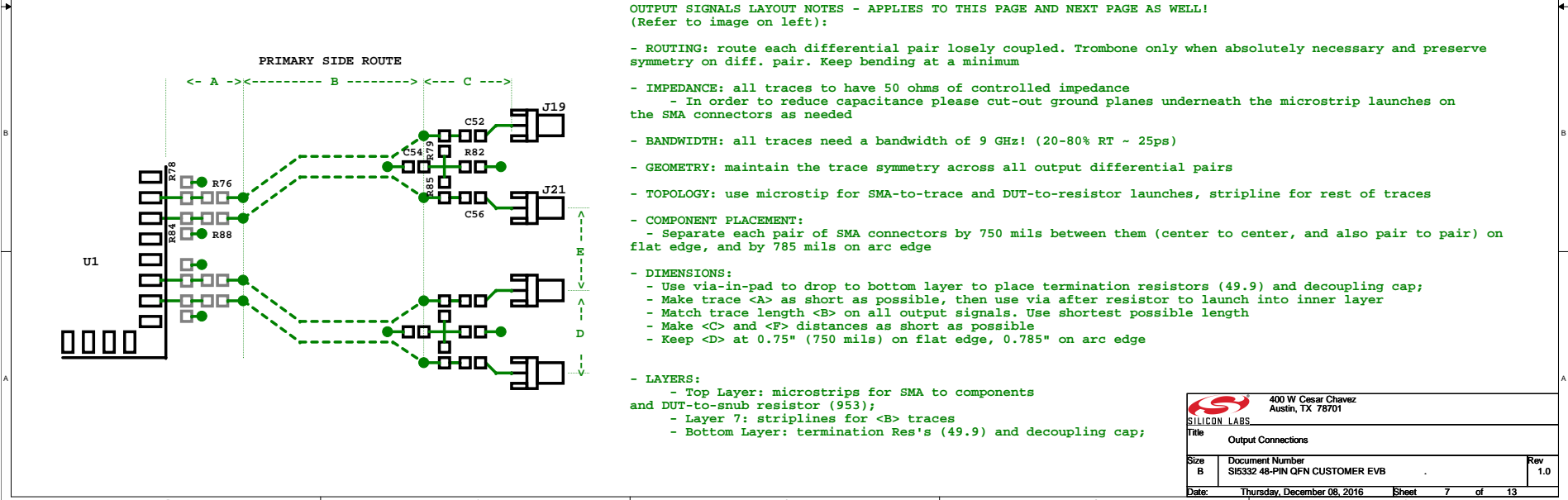
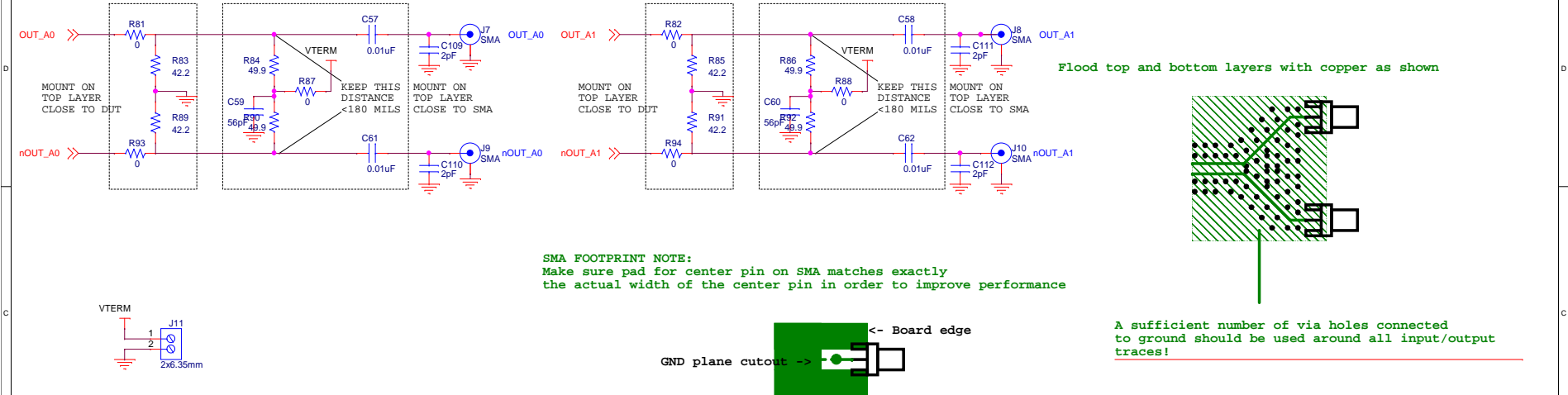
Cut planes underneath crystal to reduce capacitive coupling!



90 degree placement ->

Traces are just an illustration!

OUTPUT CONNECTIONS, PART 1

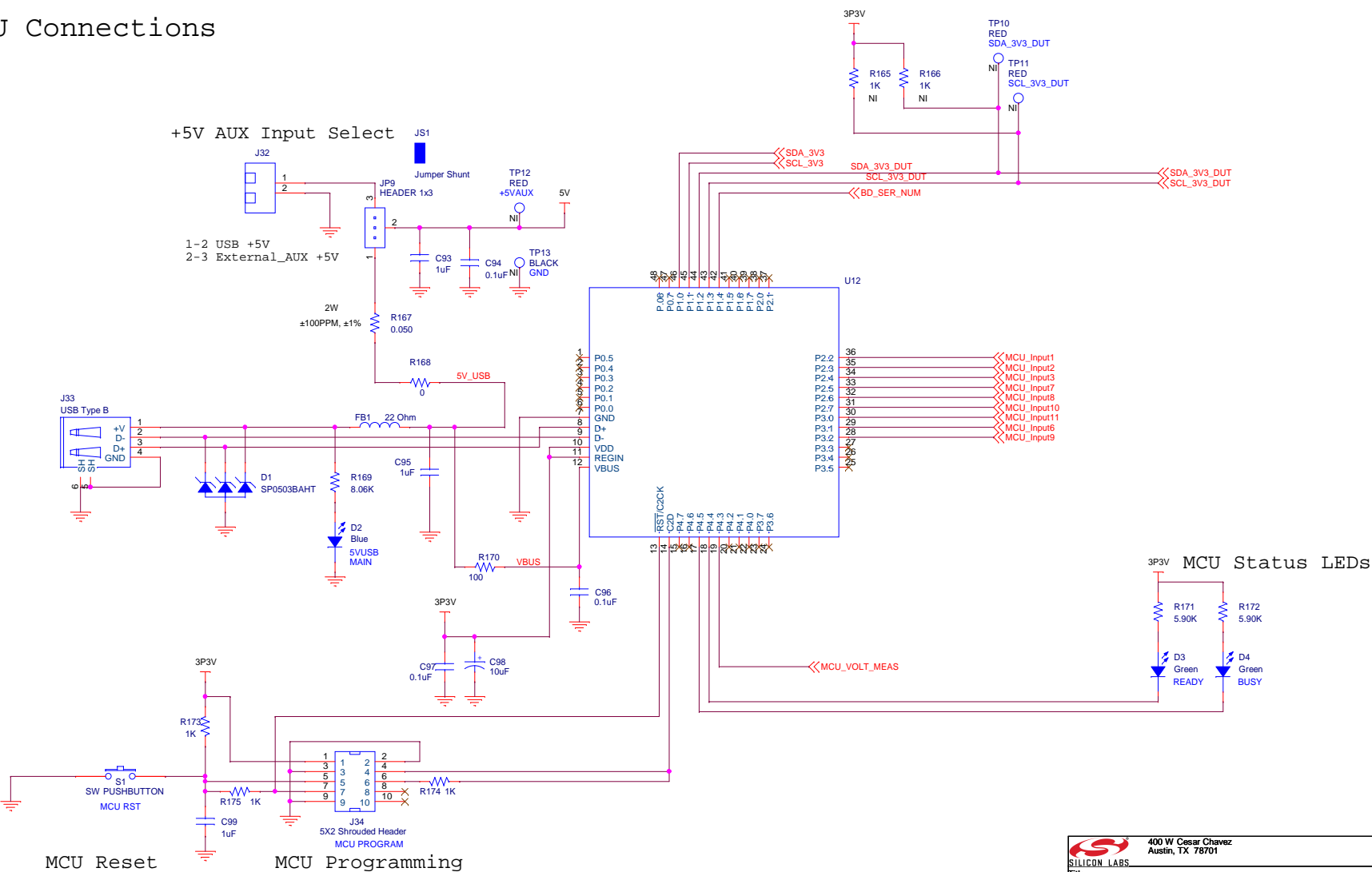


FORMAT	R572/573	R515/516	R524/525	R526/527	VTERM_x
LVDS (DC)	NI	0	49.9	0	NI
LVDS (AC)	NI	0.01uF	49.9	0	NI
HCSL(EXT)	49.9	0	NI	0	NI
LVPECL (DC)	NI	0	49.9	0	VDD0x-2
CMOS (33)	NI	27	4pF	0	GND
CMOS (25)	NI	20	4pF	0	GND

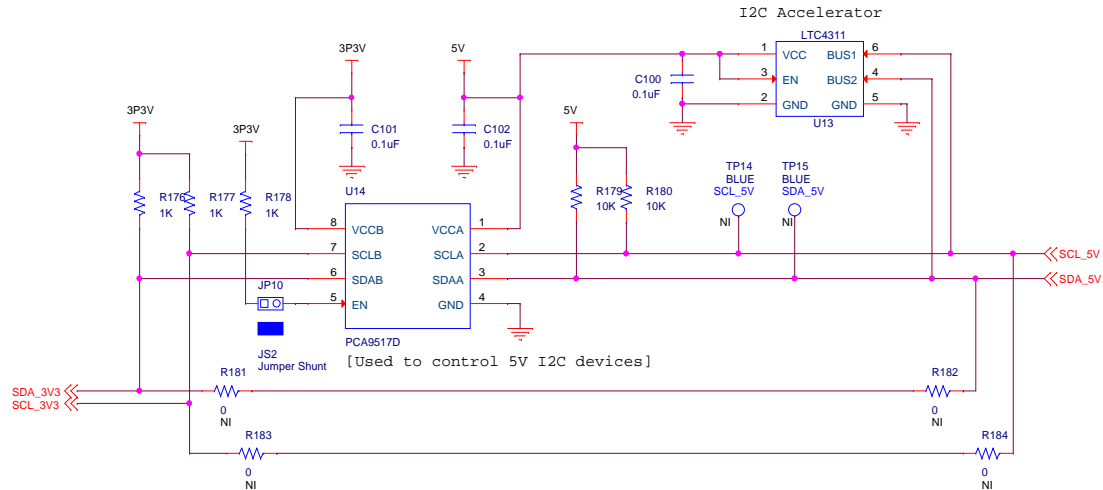
OUT\_B0 >> R95 0 R98 42.2 R99 49.9 R104 0 R107 42.2 R113 0 R116 42.2 R119 42.2 R120 49.9 R125 0 R128 42.2 R134 0 R137 0 R140 42.2 R141 49.9 R146 0 R149 42.2 R155 0 R158 0 R159 42.2 R160 49.9 R161 0 R162 42.2 R164 0 R165 42.2 R166 49.9 R167 0 R168 42.2 R169 42.2 R170 49.9 R171 0 R172 42.2 R173 42.2 R174 49.9 R175 0 R176 42.2 R177 42.2 R178 49.9 R179 0 R180 42.2 R181 42.2 R182 49.9 R183 0 R184 42.2 R185 42.2 R186 49.9 R187 0 R188 42.2 R189 42.2 R190 49.9 R191 0 R192 42.2 R193 42.2 R194 49.9 R195 0 R196 42.2 R197 42.2 R198 49.9 R199 0 R200 42.2 R201 42.2 R202 49.9 R203 0 R204 42.2 R205 42.2 R206 49.9 R207 0 R208 42.2 R209 42.2 R210 49.9 R211 0 R212 42.2 R213 42.2 R214 49.9 R215 0 R216 42.2 R217 42.2 R218 49.9 R219 0 R220 42.2 R221 42.2 R222 49.9 R223 0 R224 42.2 R225 42.2 R226 49.9 R227 0 R228 42.2 R229 42.2 R230 49.9 R231 0 R232 42.2 R233 42.2 R234 49.9 R235 0 R236 42.2 R237 42.2 R238 49.9 R239 0 R240 42.2 R241 42.2 R242 49.9 R243 0 R244 42.2 R245 42.2 R246 49.9 R247 0 R248 42.2 R249 42.2 R250 49.9 R251 0 R252 42.2 R253 42.2 R254 49.9 R255 0 R256 42.2 R257 42.2 R258 49.9 R259 0 R260 42.2 R261 42.2 R262 49.9 R263 0 R264 42.2 R265 42.2 R266 49.9 R267 0 R268 42.2 R269 42.2 R270 49.9 R271 0 R272 42.2 R273 42.2 R274 49.9 R275 0 R276 42.2 R277 42.2 R278 49.9 R279 0 R280 42.2 R281 42.2 R282 49.9 R283 0 R284 42.2 R285 42.2 R286 49.9 R287 0 R288 42.2 R289 42.2 R290 49.9 R291 0 R292 42.2 R293 42.2 R294 49.9 R295 0 R296 42.2 R297 42.2 R298 49.9 R299 0 R300 42.2 R301 42.2 R302 49.9 R303 0 R304 42.2 R305 42.2 R306 49.9 R307 0 R308 42.2 R309 42.2 R310 49.9 R311 0 R312 42.2 R313 42.2 R314 49.9 R315 0 R316 42.2 R317 42.2 R318 49.9 R319 0 R320 42.2 R321 42.2 R322 49.9 R323 0 R324 42.2 R325 42.2 R326 49.9 R327 0 R328 42.2 R329 42.2 R330 49.9 R331 0 R332 42.2 R333 42.2 R334 49.9 R335 0 R336 42.2 R337 42.2 R338 49.9 R339 0 R340 42.2 R341 42.2 R342 49.9 R343 0 R344 42.2 R345 42.2 R346 49.9 R347 0 R348 42.2 R349 42.2 R350 49.9 R351 0 R352 42.2 R353 42.2 R354 49.9 R355 0 R356 42.2 R357 42.2 R358 49.9 R359 0 R360 42.2 R361 42.2 R362 49.9 R363 0 R364 42.2 R365 42.2 R366 49.9 R367 0 R368 42.2 R369 42.2 R370 49.9 R371 0 R372 42.2 R373 42.2 R374 49.9 R375 0 R376 42.2 R377 42.2 R378 49.9 R379 0 R380 42.2 R381 42.2 R382 49.9 R383 0 R384 42.2 R385 42.2 R386 49.9 R387 0 R388 42.2 R389 42.2 R390 49.9 R391 0 R392 42.2 R393 42.2 R394 49.9 R395 0 R396 42.2 R397 42.2 R398 49.9 R399 0 R400 42.2 R401 42.2 R402 49.9 R403 0 R404 42.2 R405 42.2 R406 49.9 R407 0 R408 42.2 R409 42.2 R410 49.9 R411 0 R412 42.2 R413 42.2 R414 49.9 R415 0 R416 42.2 R417 42.2 R418 49.9 R419 0 R420 42.2 R421 42.2 R422 49.9 R423 0 R424 42.2 R425 42.2 R426 49.9 R427 0 R428 42.2 R429 42.2 R430 49.9 R431 0 R432 42.2 R433 42.2 R434 49.9 R435 0 R436 42.2 R437 42.2 R438 49.9 R439 0 R440 42.2 R441 42.2 R442 49.9 R443 0 R444 42.2 R445 42.2 R446 49.9 R447 0 R448 42.2 R449 42.2 R450 49.9 R451 0 R452 42.2 R453 42.2 R454 49.9 R455 0 R456 42.2 R457 42.2 R458 49.9 R459 0 R460 42.2 R461 42.2 R462 49.9 R463 0 R464 42.2 R465 42.2 R466 49.9 R467 0 R468 42.2 R469 42.2 R470 49.9 R471 0 R472 42.2 R473 42.2 R474 49.9 R475 0 R476 42.2 R477 42.2 R478 49.9 R479 0 R480 42.2 R481 42.2 R482 49.9 R483 0 R484 42.2 R485 42.2 R486 49.9 R487 0 R488 42.2 R489 42.2 R490 49.9 R491 0 R492 42.2 R493 42.2 R494 49.9 R495 0 R496 42.2 R497 42.2 R498 49.9 R499 0 R500 42.2 R501 42.2 R502 49.9 R503 0 R504 42.2 R505 42.2 R506 49.9 R507 0 R508 42.2 R509 42.2 R510 49.9 R511 0 R512 42.2 R513 42.2 R514 49.9 R515 0 R516 42.2 R517 42.2 R518 49.9 R519 0 R520 42.2 R521 42.2 R522 49.9 R523 0 R524 42.2 R525 42.2 R526 49.9 R527 0 R528 42.2 R529 42.2 R530 49.9 R531 0 R532 42.2 R533 42.2 R534 49.9 R535 0 R536 42.2 R537 42.2 R538 49.9 R539 0 R540 42.2 R541 42.2 R542 49.9 R543 0 R544 42.2 R545 42.2 R546 49.9 R547 0 R548 42.2 R549 42.2 R550 49.9 R551 0 R552 42.2 R553 42.2 R554 49.9 R555 0 R556 42.2 R557 42.2 R558 49.9 R559 0 R560 42.2 R561 42.2 R562 49.9 R563 0 R564 42.2 R565 42.2 R566 49.9 R567 0 R568 42.2 R569 42.2 R570 49.9 R571 0 R572 42.2 R573 42.2 R574 49.9 R575 0 R576 42.2 R577 42.2 R578 49.9 R579 0 R580 42.2 R581 42.2 R582 49.9 R583 0 R584 42.2 R585 42.2 R586 49.9 R587 0 R588 42.2 R589 42.2 R590 49.9 R591 0 R592 42.2 R593 4

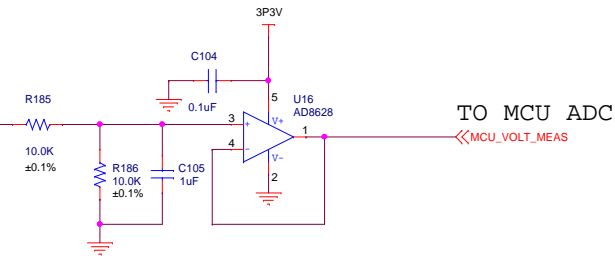
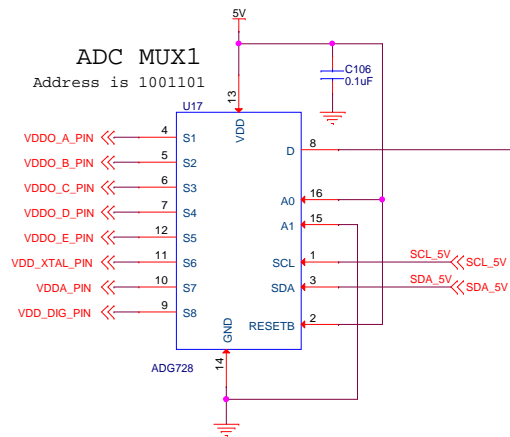
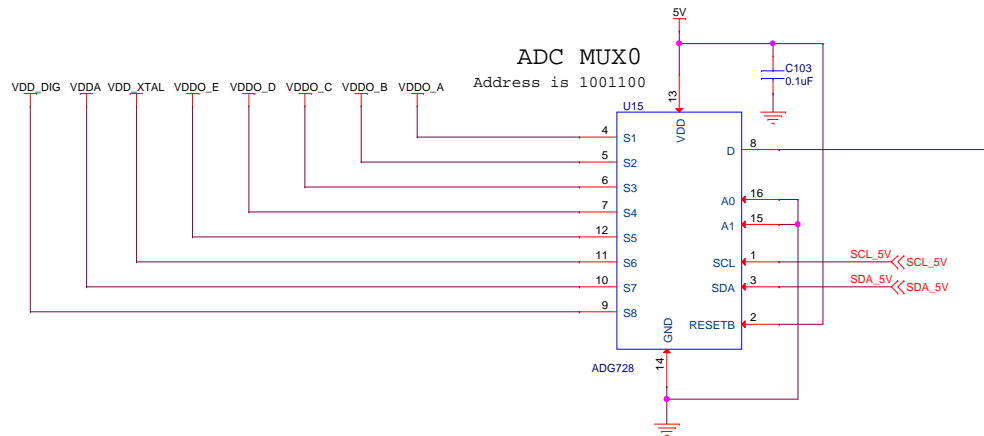


# MCU Connections

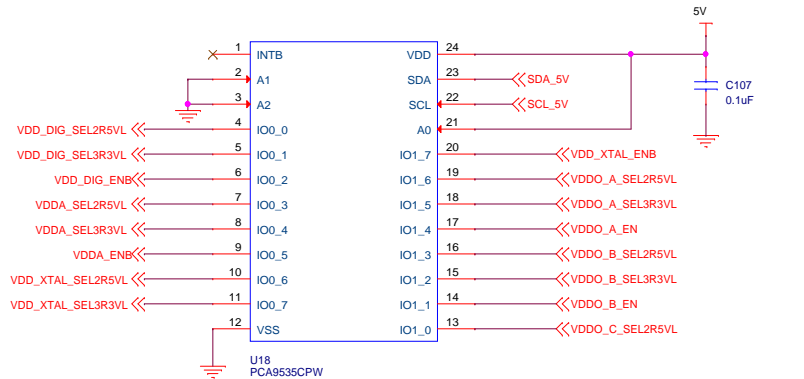


# I2C LEVEL TRANSLATOR

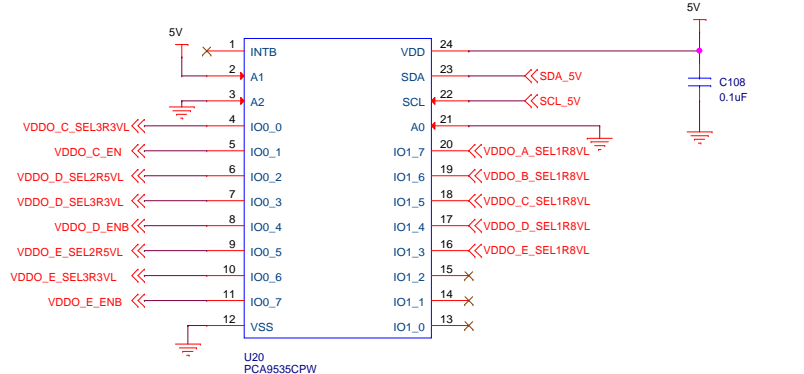




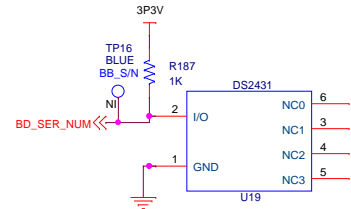
VDD Enables and SN



I2C VDD ENABLES/SELECTS FOR REGULATORS  
I2C address: 0100001



I2C VDD ENABLES/SELECTS FOR REGULATORS  
I2C address: 0100010



Board Serial Number

