



# Si5347/46

## Data Sheet Errata for Product Revision D

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This document contains information on the errata of product revision D of Si5347/46.

The device data sheet explains how to identify product revision, either from package marking or electronically.

Errata effective date: 29 July 2016.

**Note:** This document applies to Ordering Part Numbers (OPNs) which refer to product revision **D**. For example: Si5345A-**D**-GM or Si5345-**D**xxxxx-GM, where xxxxx is the custom OPN ID, and D refers to the product revision.

### ERRATA DEFINITIONS

Impact Definition: Each erratum is marked with an impact, as defined below:

- Minor—Workaround(s) exists.
- Major—Errata that do not conform to the data sheet or standard.
- Information—The device behavior is not ideal but acceptable. Typically, the data sheet and/or [ClockBuilder Pro](#) may be changed to match or address the device behavior.

**Table .1. Errata Status Summary**

| Erratum | Title/Problem                              | Impact      | Workarounds | Resolution                                  |
|---------|--|-------------|-------------|---|
| 1       | INTRb pin activity while RSTb pin held low | Information | Yes         | Will be fixed in a future silicon revision. |

## 1. INTRb\_pin\_activity\_while\_RSTb\_pin\_held\_low

### Description

The INTRb pin voltage varies at approximately 5 MHz while the RSTb reset input pin is held low (active).

### Impact

Activity on the INTRb pin while RSTb is held low may appear as an interrupt to the system.

Note: The INTRb pin is driven low (active) when exiting the reset state.

### Workaround

The INTRb signal can be ignored or masked by the system until RSTb is released or driven high.

### Resolution

This erratum will be fixed in the next silicon revision.

## 2. Document History

v1.0: July 2016

- Initial release of this document for Si5347/46-D errata, documented separately from other Si534x family members.



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