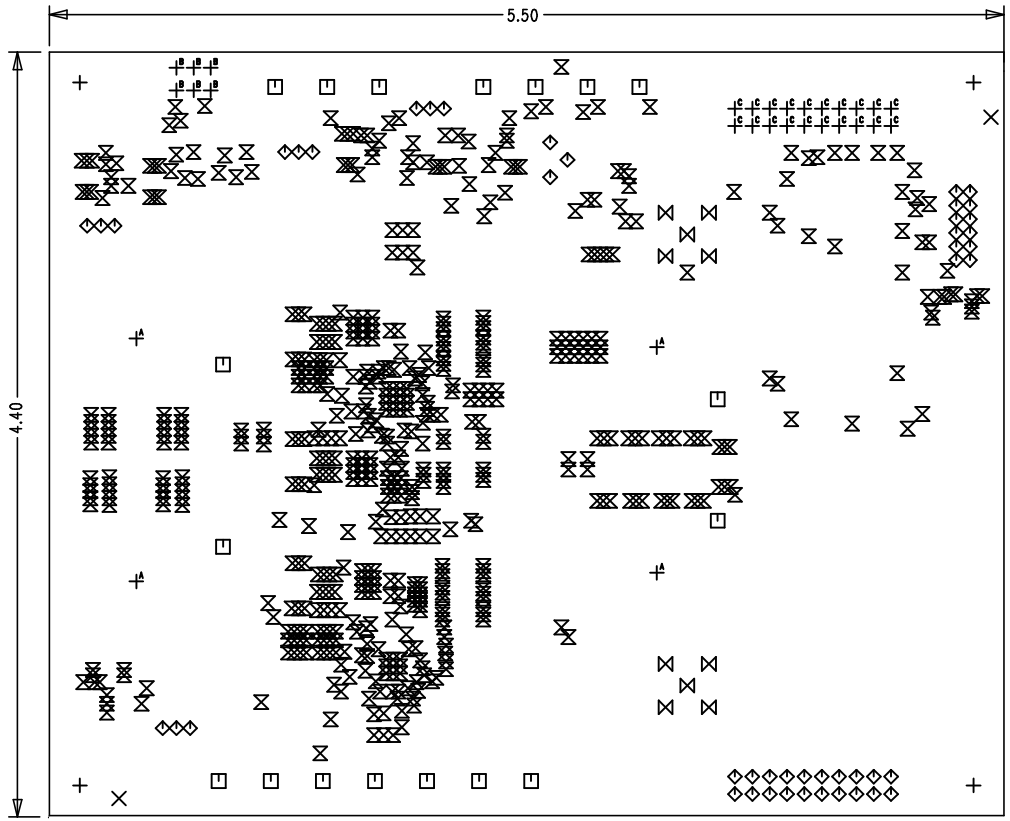
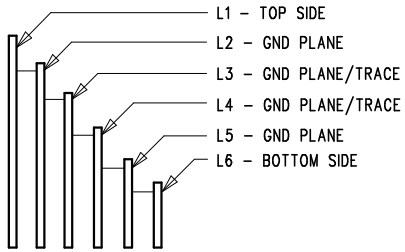
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	1	PRODUCTION	YI S.	11-1-12



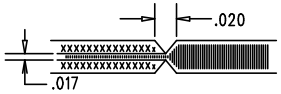
SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	YES	+/-0.003
0.07	2	X	NO	+/-0.003
0.067	18	□	YES	+/-0.003
0.035	47	◇	YES	+/-0.003
0.01	623	⧖	YES	+/-0.003
0.055	10	⋈	YES	+/-0.003
0.189	4	+A	YES	+/-0.003
0.03543	6	+B	YES	+/-0.003
0.037	20	+C	YES	+/-0.003

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
   -FINISHED THICKNESS TO BE 0.062 +/- .005
   -TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE LAYER 1, LAYER 2, LAYER 5 AND LAYER 6. 1 OZ. CU ON THE LAYER 3 AND LAYER4
   -FLAMMABILITY RATING: 94 V-O MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
   -ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
   -HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
   -GOLD IMMERSION BOTH SIDES. (LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
   -FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX = ± 0.01
0.XXX = ± 0.005
INTERPRET DIM AND TOL PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS	
PCB DES.	HZ
APP ENG.	YI S.

SCALE = NONE

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TITLE: FABRICATION DRAWING
THREE-PHASE DC/DC CONVERTER WITH INPUT CURRENT SENSE AND DIGITAL POWER MANAGEMENT

SIZE	IC NO.	REV
N/A	LTC3880EUJ/LTC3883EUH DEMO CIRCUIT 2052A	1

FILENAME: DC2052A-1.PCB
SHT 1 OF 1