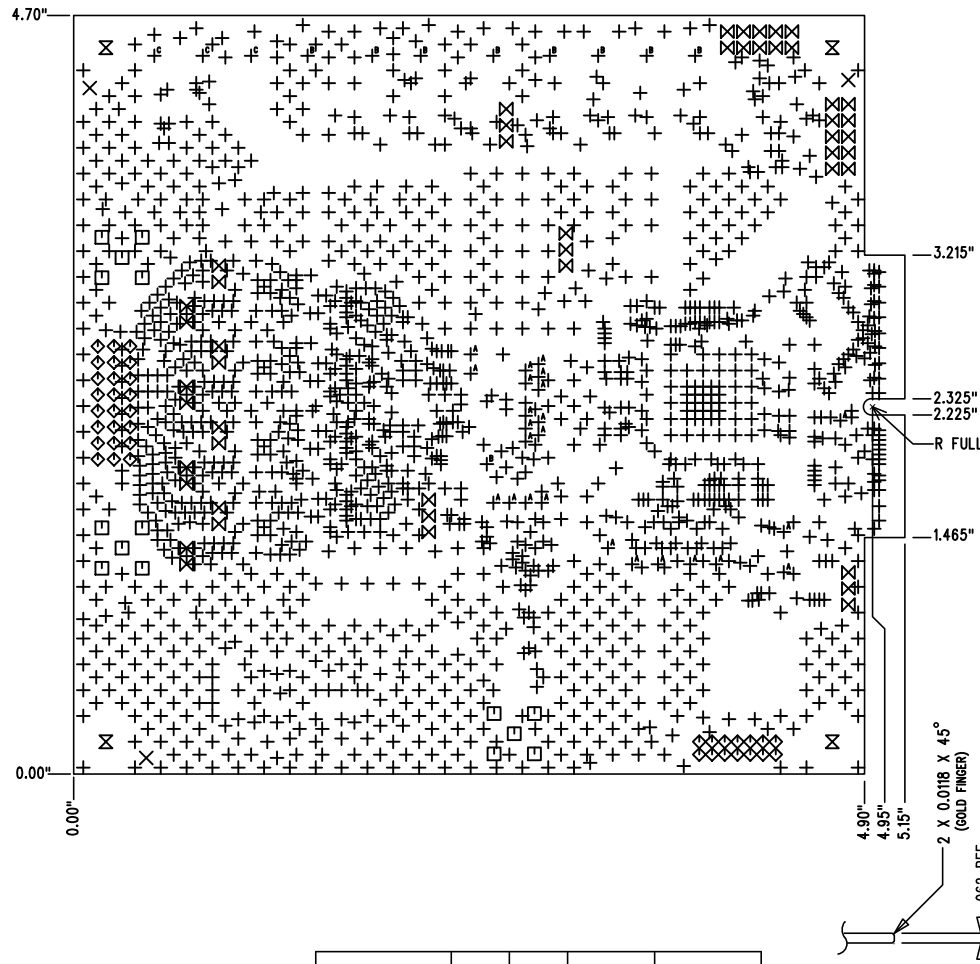
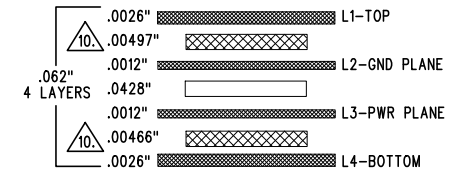


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	GUY H.	03-11-15



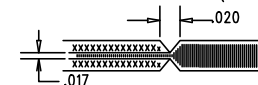
SIZE	QTY	SYM	PLATED	TOL
0.008	1772	+	YES	+/-0.003"
0.07	3	X	NO	+/-0.003"
0.055	15	□	YES	+/-0.003"
0.04	38	◇	YES	+/-0.003"
0.19	4	⊗	NO	+/-0.003"
0.035	48	⊗	YES	+/-0.003"
0.015	24	⊕ <sup>A</sup>	YES	+/-0.003"
0.063	9	⊕ <sup>B</sup>	YES	+/-0.003"
0.094	3	⊕ <sup>C</sup>	YES	+/-0.003"

## LAYER STRUCTURE



## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- CONTROLLED 50 OHM +/-10% IMPEDANCE FOR LAYERS 1-2 USING 0.009" TRACE AT 2GHz FREQ.

- SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

- FINGER AREA: GOLD PLATE TO MIN. OF 0.00003 INCHES.  
PER MIL-G-45204, TYP II, CL1, GRADE C, OVER 0.0002 INCHES  
LOW STRESS NICKEL PER QQ-N-290, CL2.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		APPROVALS PCB DES. KIM T. APP ENG. GUY H.		LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-9000 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
SCALE = NONE		TITLE: FABRICATION DRAWING 8/4/2-CHANNEL, SOFTSPAN, HIGH SPEED, SERIAL, SAR, ADC		SIZE N/A	IC NO. LTC23XXILX FAMILY DEMO CIRCUIT 2094A
FILENAME: DC2094A-2.PCB		REV 2		SHT 1 OF 1	