

EFR32MG13 2.4 GHz 19 dBm Radio Board BRD4168A Reference Manual



The BRD4168A Mighty Gecko Radio Board enables developers to develop Zigbee®, Thread, Bluetooth® low energy and proprietary wireless applications. The board contains a 2.4 GHz Mighty Gecko Wireless System on Chip and is optimized for operating at 19 dBm output power. Radiated and conducted testing is supported with the on-board printed antenna and UFL connector.

The BRD4168A Mighty Gecko Radio Board plugs into the Wireless Starter Kit Main-board, which is provided with the Mighty Gecko Starter Kit and gives access to display, buttons and additional features from Expansion Boards. With the supporting Simplicity Studio suite of tools, developers can take advantage of graphical wireless application development; mesh networking debug and packet trace; and visual energy profiling and optimization. The board also serves as an RF reference design for applications targeting 2.4 GHz wireless operation with 19 dBm output power.

This document contains brief introduction and description of the BRD4168A Radio Board features focusing on the RF sections and performance.

RADIO BOARD FEATURES

- Wireless SoC: EFR32MG13P732F512GM48
- CPU core: ARM Cortex®-M4 with FPU
- Flash memory: 512 kB
- RAM: 64 kB
- Operation frequency: 2.4 GHz
- Transmit power: 19 dBm
- Integrated PCB antenna, UFL connector (optional).
- Crystals for LFXO and HFXO: 32.768 kHz and 38.4 MHz.

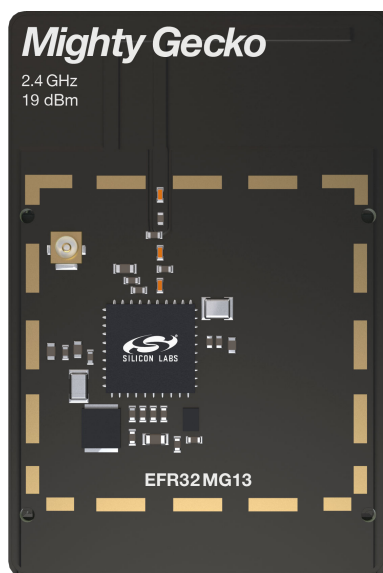


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1. Introduction

The EFR32 Mighty Gecko Radio Boards provide a development platform (together with the Wireless Starter Kit Mainboard) for the Silicon Labs EFR32 Mighty Gecko Wireless System on Chips and serve as reference designs for the matching network of the RF interface.

The BRD4168A Radio Board is designed to operate in the 2400-2483.5 MHz band with the RF matching network optimized for operating at 19 dBm output power.

To develop and/or evaluate the EFR32 Mighty Gecko, the BRD4168A Radio Board can be connected to the Wireless Starter Kit Mainboard to get access to display, buttons and additional features from Expansion Boards and also to evaluate the performance of the RF interface.

3. Radio Board Block Summary

3.1 Introduction

This section gives a short introduction to the blocks of the BRD4168A Radio Board.

3.2 Radio Board Block Diagram

The block diagram of the BRD4168A Radio Board is shown in the figure below.

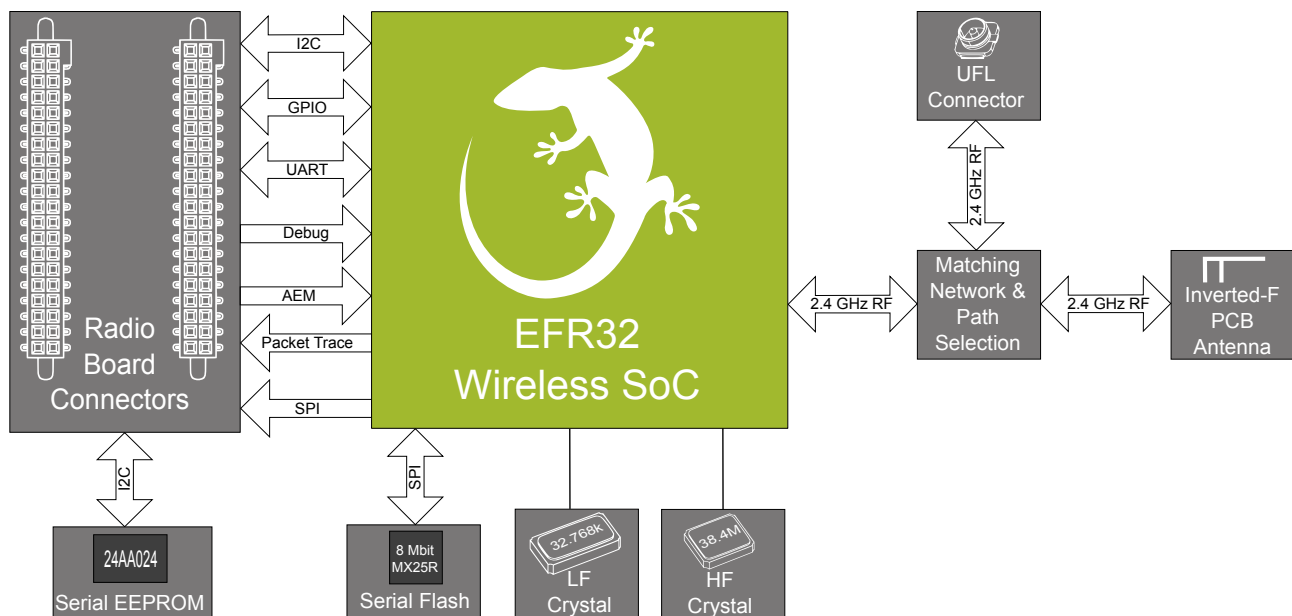


Figure 3.1. BRD4168A Block Diagram

3.3 Radio Board Block Description

3.3.1 Wireless MCU

The BRD4168A Mighty Gecko Radio Board incorporates an EFR32MG13P732F512GM48 Wireless System on Chip featuring 32-bit Cortex®-M4 with FPU core, 512 kB of flash memory, 64 kB of RAM and a 2.4 GHz band transceiver with output power up to 19 dBm. For additional information on the EFR32MG13P732F512GM48, refer to the EFR32MG13 Data Sheet.

3.3.2 LF Crystal Oscillator (LFXO)

The BRD4168A Radio Board has a 32.768 kHz crystal mounted. For details regarding the crystal configuration, refer to Application Note AN0016: Oscillator Design Considerations.

3.3.3 HF Crystal Oscillator (HFXO)

The BRD4168A Radio Board has a 38.4 MHz crystal mounted. For details regarding the crystal configuration, refer to Application Note AN0016: Oscillator Design Considerations.

3.3.4 Matching Network for 2.4 GHz

The BRD4168A Radio Board incorporates a 2.4 GHz matching network which connects the 2.4 GHz TRX pin of the EFR32MG13 to the one on-board printed Inverted-F antenna. The component values were optimized for the 2.4 GHz band RF performance and current consumption with 19 dBm output power.

For detailed description of the matching network, see Chapter [4.2.1 Description of the 2.4 GHz RF Matching](#).

3.3.5 UFL Connector

To be able to perform conducted measurements Silicon Labs added an UFL connector to the Radio Board. The connector allows an external 50 Ohm cable or antenna to be connected during design verification or testing.

Note: By default the output of the matching network is connected to the printed Inverted-F antenna by a series component. It can be connected to the UFL connector as well through a series 0 Ohm resistor which is not mounted by default. For conducted measurements through the UFL connector the series component to the antenna should be removed and the 0 Ohm resistor should be mounted (see Chapter [4.2 Schematic of the RF Matching Network](#) for further details).

3.3.6 Radio Board Connectors

Two dual-row, 0.05" pitch polarized connectors make up the BRD4168A Radio Board interface to the Wireless Starter Kit Mainboard.

For more information on the pin mapping between the EFR32MG13P732F512GM48 and the Radio Board Connector, refer to Chapter [2.2 Radio Board Connector Pin Associations](#).

3.3.7 Inverted-F Antenna

The BRD4168A Radio Board includes a printed inverted-F antenna (IFA) tuned to have close to 50 Ohm impedance at the 2.4 GHz band.

For detailed description of the antenna see Chapter [4.5 Inverted-F Antenna](#).

3.3.8 Serial Flash

The BRD4168A Radio Board is equipped with an 8 Mbit Macronix MX25R SPI flash that is connected directly to the EFR32MG13. For additional information on the pin mapping see the schematic of the BRD4168A.

3.3.9 Serial EEPROM

The BRD4168A Radio Board is equipped with a serial I²C EEPROM for board identification and to store additional board related information.

4. RF Section

4.1 Introduction

This section gives a short introduction to the RF section of the BRD4168A Radio Board.

4.2 Schematic of the RF Matching Network

The schematic of the RF section of the BRD4168A Radio Board is shown in the following figure.

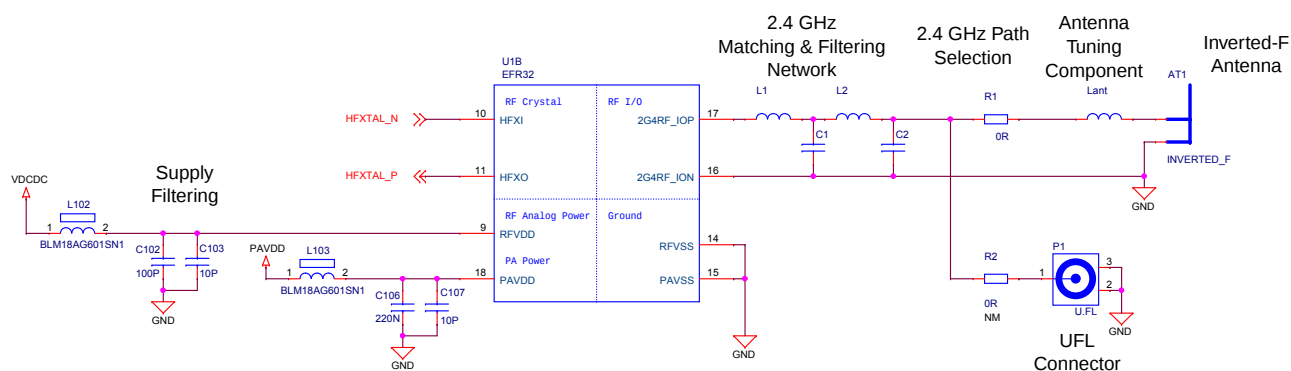


Figure 4.1. Schematic of the RF Section of the BRD4168A

4.2.1 Description of the 2.4 GHz RF Matching

The 2.4 GHz matching connects the 2G4RF_IOP pin to the on-board printed Inverted-F Antenna. The 2G4RF_ION pin is connected to ground. For higher output powers (13 dBm and above), besides the impedance matching circuitry, it is recommended to use additional harmonic filtering as well at the RF output. The targeted output power of the BRD4168A board is 19 dBm. Therefore, the RF output of the IC is connected to the antenna through a four-element impedance matching and harmonic filter circuitry.

For conducted measurements the output of the matching network can also be connected to the UFL connector by removing the series R1 component between the antenna and the output of the matching and adding a 0 Ohm resistor to the R2 resistor position between the output of the matching and the UFL connector.

4.3 RF Section Power Supply

On the BRD4168A Radio Board the supply pin of the RF Analog Power (RFVDD) is connected directly to the output of the on-chip DC-DC converter while the supply for the 2.4 GHz PA (PAVDD) is provided directly by the mainboard. This way, by default, the DC-DC converter provides 1.8 V for the RF analog section, the mainboard provides 3.3 V for the PA (for details, see the schematic of the BRD4168A).

4.4 Bill of Materials for the 2.4 GHz Matching

The Bill of Materials of the 2.4 GHz matching network of the BRD4168A Radio Board is shown in the following table.

Table 4.1. Bill of Materials for the BRD4168A 2.4GHz RF Matching Network

Component name	Value	Manufacturer	Part Number
L1	1.8 nH	Murata	LQP15MN1N8W02D
L2	3.0 nH	Murata	LQP15MN3N0W02D
C1	2.0 pF	Murata	GRM1555C1H2R0WA01D
C2	1.0 pF	Murata	GRM1555C1H1R0WA01D

4.5 Inverted-F Antenna

The BRD4168A Radio Board includes an on-board printed inverted-F antenna tuned for the 2.4 GHz band. Due to the design restrictions of the Radio Board, the input of the antenna and the output of the matching network can't be placed directly next to each other. As a result, a 50 Ohm transmission line was necessary to connect them. With the actual line length, the impedance of the antenna at the double-harmonic frequency is transformed closer to a critical impedance range. This reduces the low-pass filter effect of the matching network resulting in the increase of the radiated level of the harmonic.

To reduce the harmonic radiation a tuning component was used between the matching network output and the antenna input. For the actual Radio Board design (with the actual transmission line length) a small value inductor was used (Lant inductor with value of 1.8 nH) to transform the impedance at the double-frequency harmonic away from the critical region while keeping the impedance at the fundamental close to 50 Ohm. With this the suppression of the radiated double-frequency harmonic increases by approximately 3-4 dB. The resulting impedance and reflection are shown in the following figure.

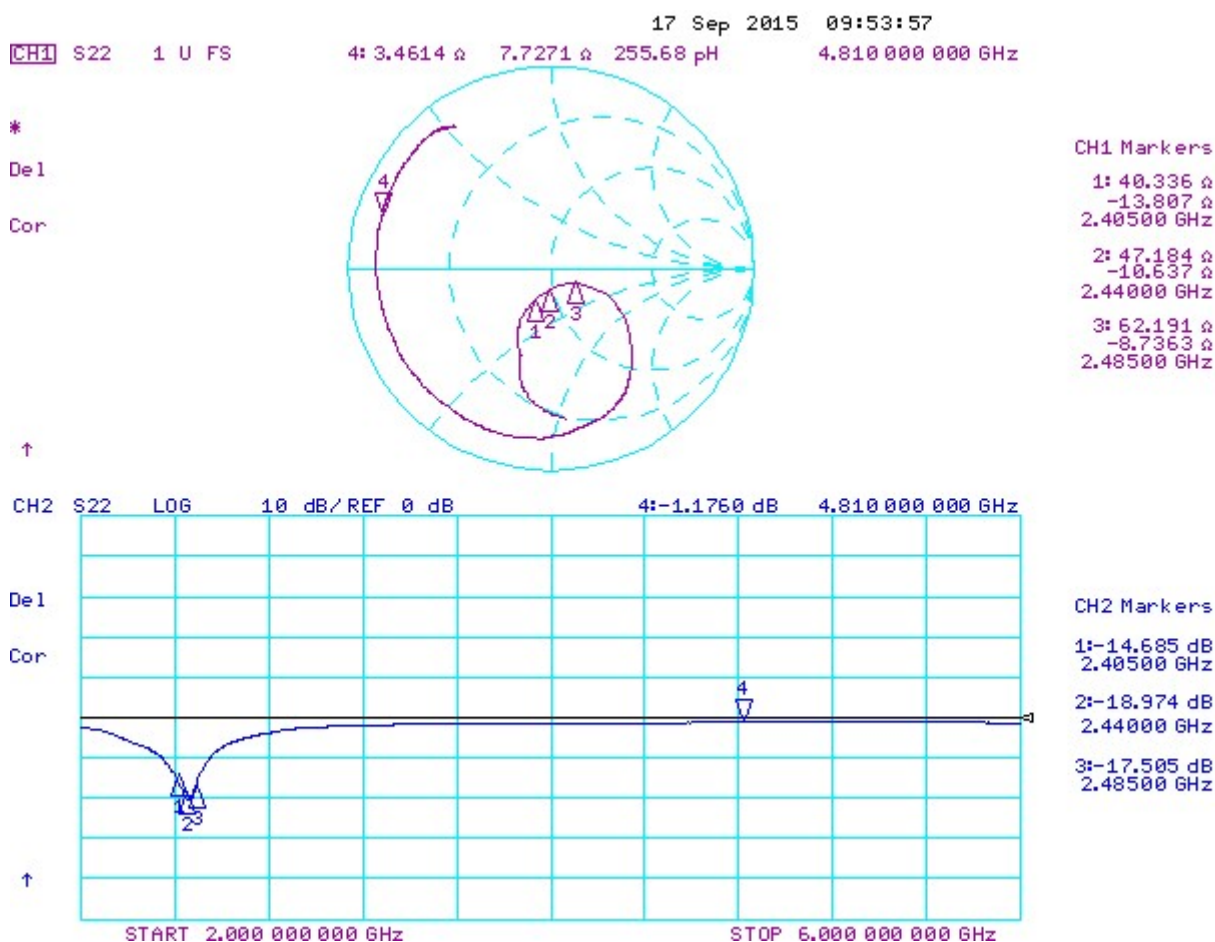


Figure 4.2. Impedance and Reflection of the Inverted-F Antenna of the BRD4168A

5. Mechanical Details

The BRD4168A Radio Board is illustrated in the figures below.

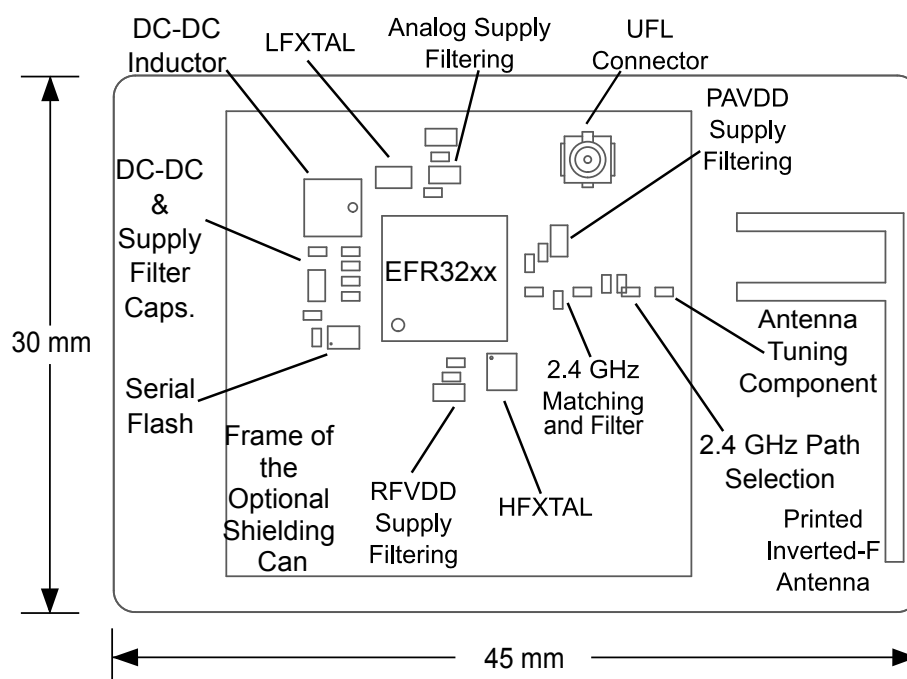


Figure 5.1. BRD4168A Top View

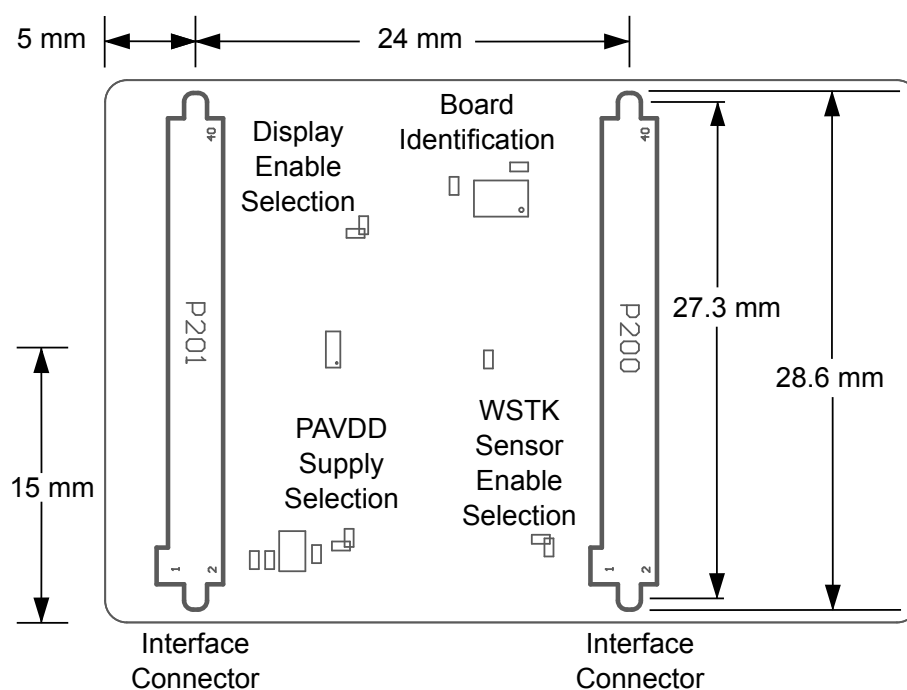


Figure 5.2. BRD4168A Bottom View

6. EMC Compliance

6.1 Introduction

Compliance of the fundamental and harmonic levels of the BRD4168A Radio Board is tested against the following standards:

- 2.4 GHz:
 - ETSI EN 300-328
 - FCC 15.247

6.2 EMC Regulations for 2.4 GHz

6.2.1 ETSI EN 300-328 Emission Limits for the 2400-2483.5 MHz Band

Based on ETSI EN 300-328 the allowed maximum fundamental power for the 2400-2483.5 MHz band is 20 dBm EIRP. For the unwanted emissions in the 1 GHz to 12.75 GHz domain the specified limit is -30 dBm EIRP.

6.2.2 FCC15.247 Emission Limits for the 2400-2483.5 MHz Band

FCC 15.247 allows conducted output power up to 1 Watt (30 dBm) in the 2400-2483.5 MHz band. For spurious emissions the limit is -20 dBc based on either conducted or radiated measurement, if the emission is not in a restricted band. The restricted bands are specified in FCC 15.205. In these bands the spurious emission levels must meet the levels set out in FCC 15.209. In the range from 960 MHz to the frequency of the 5th harmonic it is defined as 0.5 mV/m at 3 m distance which equals to -41.2 dBm in EIRP.

Additionally, for spurious frequencies above 1 GHz, FCC 15.35 allows duty-cycle relaxation to the regulatory limits. For the EmberZNet PRO the relaxation is 3.6 dB. Therefore, the -41.2 dBm limit can be modified to -37.6 dBm.

If operating in the 2400-2483.5 MHz band the 2nd, 3rd and 5th harmonics can fall into restricted bands. As a result, for those the -37.6 dBm limit should be applied. For the 4th harmonic the -20 dBc limit should be applied.

6.2.3 Applied Emission Limits for the 2.4 GHz Band

The above ETSI limits are applied both for conducted and radiated measurements.

The FCC restricted band limits are radiated limits only. Besides that, Silicon Labs applies those to the conducted spectrum i.e. it is assumed that, in case of a custom board, an antenna is used which has 0 dB gain at the fundamental and the harmonic frequencies. In that theoretical case, based on the conducted measurement, the compliance with the radiated limits can be estimated.

The overall applied limits are shown in the table below.

Table 6.1. Applied Limits for Spurious Emissions for the 2.4 GHz Band

Harmonic	Frequency	Limit
2nd	4800~4967 MHz	-37.6 dBm
3rd	7200~7450.5 MHz	-37.6 dBm
4th	9600~9934 MHz	-30 dBm
5th	12000~12417.5 MHz	-37.6 dBm

7. RF Performance

7.1 Conducted Power Measurements

During measurements, the BRD4168A Radio Board was attached to a Wireless Starter Kit Mainboard which was supplied by USB. The voltage supply for the Radio Board was 3.3 V.

7.1.1 Conducted Measurements in the 2.4 GHz band

The BRD4168A Radio Board was connected directly to a Spectrum Analyzer through its UFL connector (the R1 component was removed and a 0 Ohm resistor was soldered to the R2 resistor position). The supply for the RF section (RFVDD) was 1.8 V provided by the on-chip DCDC converter, the supply for the 2.4 GHz power amplifier (PAVDD) was 3.3 V provided by the mainboard; for details, see the schematic of the BRD4168A. The transceiver was operated in continuous carrier transmission mode. The output power of the radio was set to 19 dBm.

The typical output spectrum is shown in the following figure.

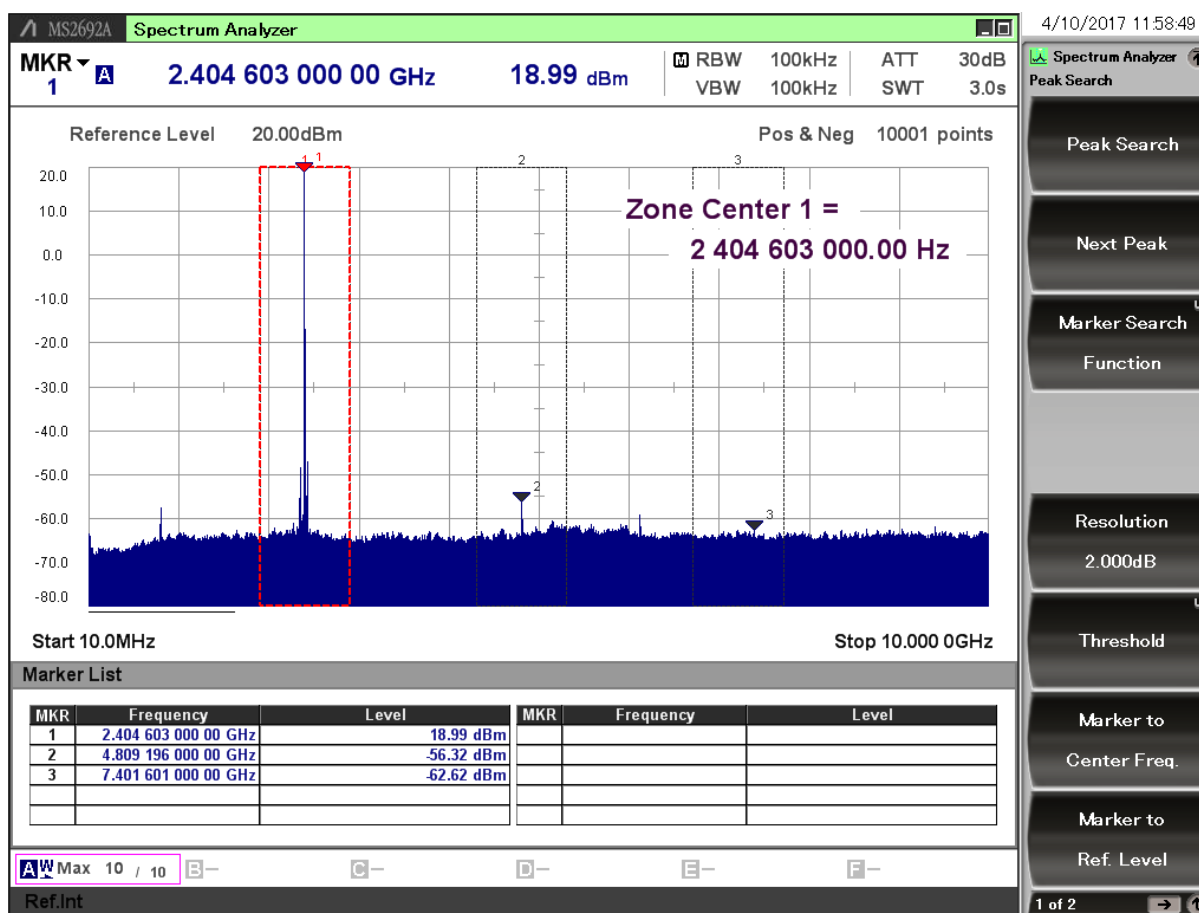


Figure 7.1. Typical Output Spectrum of the BRD4168A

As it can be observed, the fundamental is slightly lower than 19 dBm and all of the unwanted emissions are under the -37.6 dBm applied limit.

Note: The conducted measurement is performed by connecting the on-board UFL connector to a Spectrum Analyzer through an SMA Conversion Adapter (P/N: HRMJ-U.FLP(40)). This connection itself introduces approximately 0.3 dB insertion loss.

7.2 Radiated Power Measurements

During measurements, the BRD4168A Radio Board was attached to a Wireless Starter Kit Mainboard which was supplied by USB. The voltage supply for the Radio Board was 3.3 V. The radiated power was measured in an antenna chamber by rotating the board 360 degrees with horizontal and vertical reference antenna polarizations in the XY, XZ and YZ cuts. The measurement planes are shown in the figure below.

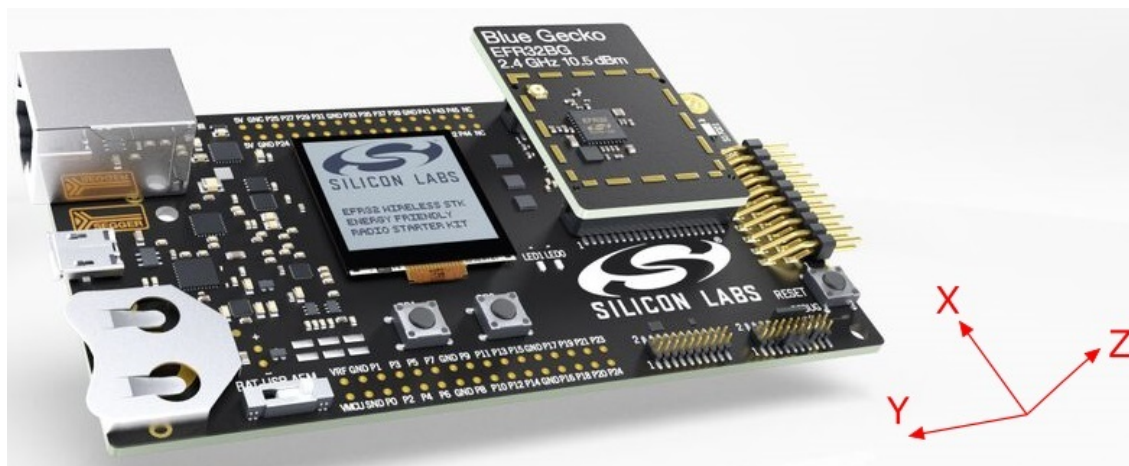


Figure 7.2. Illustration of reference planes with a Radio Board plugged into the Wireless Starter Kit Mainboard

Note: The radiated measurement results presented in this document were recorded in an unlicensed antenna chamber. Also the radiated power levels may change depending on the actual application (PCB size, used antenna, and so on). Therefore, the absolute levels and margins of the final application are recommended to be verified in a licensed EMC testhouse.

7.2.1 Radiated Measurements in the 2.4 GHz band

For the transmitter antenna the on-board printed inverted-F antenna of the BRD4168A Radio Board was used (the R1 component was mounted). The supply for the RF section (RFVDD) was 1.8 V provided by the on-chip DCDC converter, the supply for the 2.4 GHz power amplifier (PAVDD) was 3.3 V provided by the mainboard; for details, see the schematic of the BRD4168A. The transceiver was operated in continuous carrier transmission mode. The output power of the radio was set to 19 dBm based on the conducted measurement.

The results are shown in the table below.

Table 7.1. Maximums of the measured radiated powers in EIRP [dBm]

Frequency	EIRP [dBm]	Orientation	Margin [dB]	Limit in EIRP [dBm]
Fund	21.7	XZ/H	8.3	30
2nd	-48.4	YZ/H	10.8	-37.6
3rd	-50.4	YZ/H	12.8	-37.6
4th	<-50*	-/-	>10	-30
5th	<-50*	-/-	>10	-37.6

* Signal level is below the Spectrum Analyzer noise floor.

As it can be observed, thanks to the high gain of the inverted-F antenna, the level of the fundamental is higher than 19 dBm. The strongest harmonic is the double-frequency one and it is compliant with the -37.6 dBm applied limit with ~10 dB margin.

8. EMC Compliance Recommendations

8.1 Recommendations for 2.4 GHz ETSI EN 300-328 compliance

As it was shown in the previous chapter, the radiated power of the fundamental of the BRD4168A Mighty Gecko Radio Board complies with the 20 dBm limit of the ETSI EN 300-328 in case of the conducted measurement. However, in case of the radiated measurement, due to the high antenna gain, the radiated power is higher than the limit by 2 dB. In order to comply, the output power should be reduced (with different antennas, depending on the gain of the used antenna, the necessary reduction can be different). The harmonic emissions are under the -30 dBm limit. Although the BRD4168A Radio Board has an option for mounting a shielding can, that is not required for the compliance.

8.2 Recommendations for 2.4 GHz FCC 15.247 compliance

As it was shown in the previous chapter, the radiated power of the fundamental of the BRD4168A Mighty Gecko Radio Board complies with the 30 dBm limit of the FCC 15.247. The harmonic emissions are under the -37.6 dBm applied limit both in case of the conducted and the radiated measurements. Although the BRD4168A Radio Board has an option for mounting a shielding can, that is not required for the compliance.

9. Document Revision History

Revision 1.0

2017-05-22

Initial document revision.

10. Board Revision History

Table 10.1. BRD4168A Radio Board Revisions

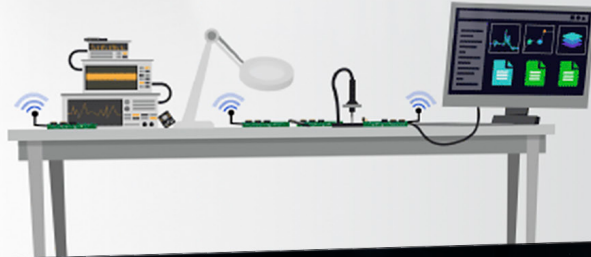
Radio Board Revision	Description
A00	Initial revision.

11. Errata

There are no known errata at present.

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