STEVAL-ISA159V1

High efficiency synchronous step-down regulator based on the L6984

Data brief



Features

- 400 mA DC output current
- 4.5 V to 36 V operating input voltage
- Synchronous rectification
- Low consumption mode or low noise mode
- 100 µA I_Q at light load (LCM V_{OUT} = 3.3 V)
- 13 µA $I_{Q\text{-}SHTDWN}$
- Adjustable f_{SW} (250 kHz - 600 kHz)
- Output voltage adjustable from 0.9 V
- No resistor divider required for 3.3 V V_{OUT}
- V_{BIAS} maximizes efficiency at light load
- 350 mA valley current limit
- Constant on-time control scheme
- PGOOD open collector
- Thermal shutdown
- RoHS compliant

Description

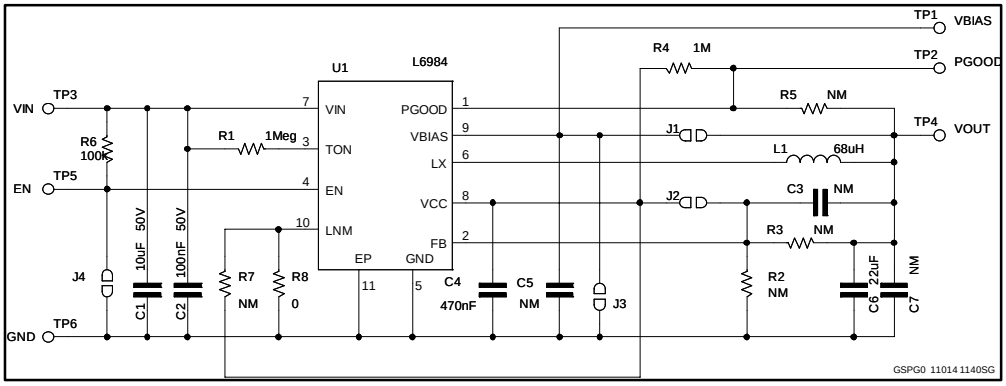
The STEVAL-ISA159V1 product evaluation board is based on L6984, a high-efficiency monolithic synchronous step-down regulator, capable of delivering up to 400 mA of DC current. The fixed 3.3 V output requires no external resistor divider. The L6984 is suited for home appliances, factory and building automation. The Low Consumption Mode (LCM) is designed for eco-friendly home appliances and automotive applications active during car parking, so it maximizes the efficiency at light load with controlled output voltage ripple. The Low Noise Mode (LNM) makes the switching frequency nearly constant over the load current range, serving low noise application specifications such as audio and sensors. The PGOOD open collector output can implement output voltage sequencing during the power-up phase. The synchronous rectification, designed for high efficiency at medium-heavy load, and the high switching frequency capability ensure a compact application size. Pulse-by-pulse current sensing on the low-side power element implements effective constant current protection.

For further information contact your local STMicroelectronics sales office

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1 Schematic diagram

Figure 1: STEVAL-ISA159V1 circuit schematic



2 Revision history

Table 1: Document revision history

Data	Revision	Changes
03-Oct-2014	1	Initial release
06-May-2015	2	Updated description on the cover page

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