

MAX14916 Evaluation Kit

Evaluates: MAX14916

General Description

The MAX14916 evaluation kit (EV kit) provides a proven design to evaluate the MAX14916, 2.4A (min) quad high-side switch with extended diagnostics. The EV kit includes the MAX14916 evaluation board and a graphical user interface (GUI) that provides communication from a PC to the target device through a USB port and the USB2GPIO interface board. The USB2GPIO EV kit should be ordered separately.

The GUI is compatible with Windows 10 for exercising the features of the MAX14916 IC. The EV kit GUI allows individually controlling of eight high-side switches, which hardware configured for quad high-current operation, through the high-speed SPI interface and receive a diagnostic information from the MAX14916, including open-wire/open-load conditions, state of the output channels, multiple undervoltage alarms, global and per channel overtemperature alarms, and multiple fault alarms.

The MAX14916 EV kit must be powered from an external +24V power supply and can consume more than 10A when fully loaded. The USB2GPIO interface board is powered from the USB port.

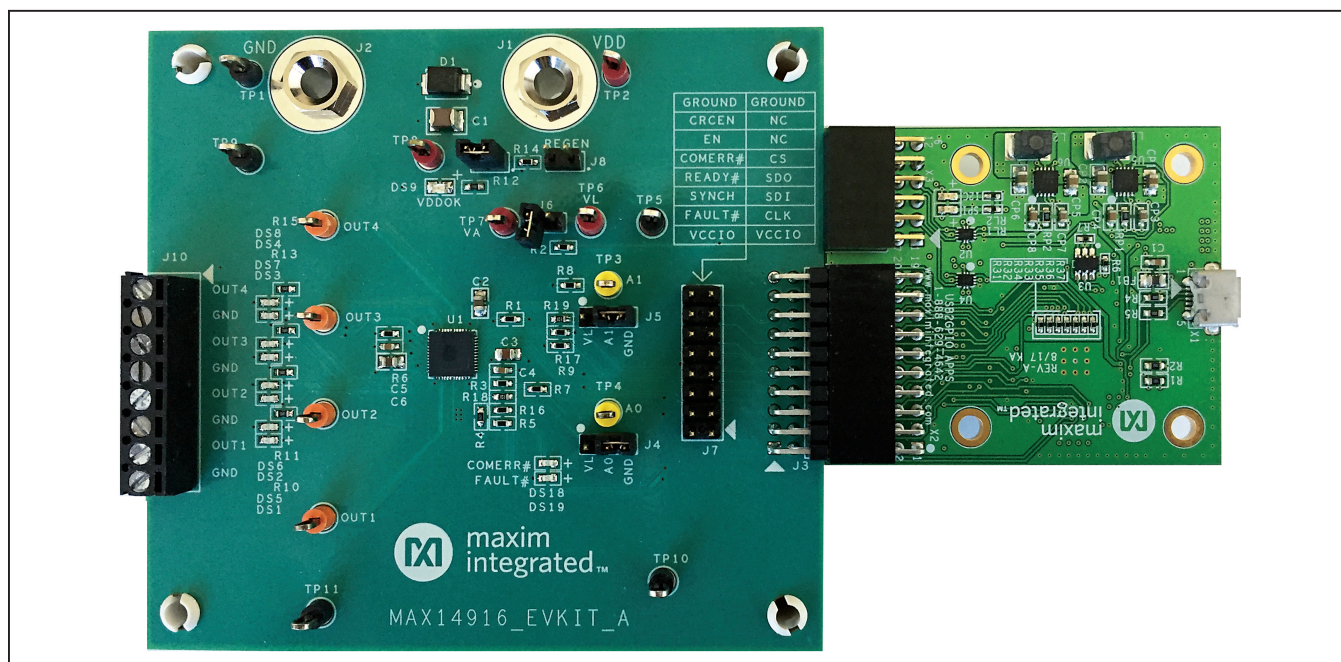
The MAX14916 EV kit board comes with a MAX14916AFM+ installed in a 48-pin, 6 x 6mm FC2QFN package.

Features

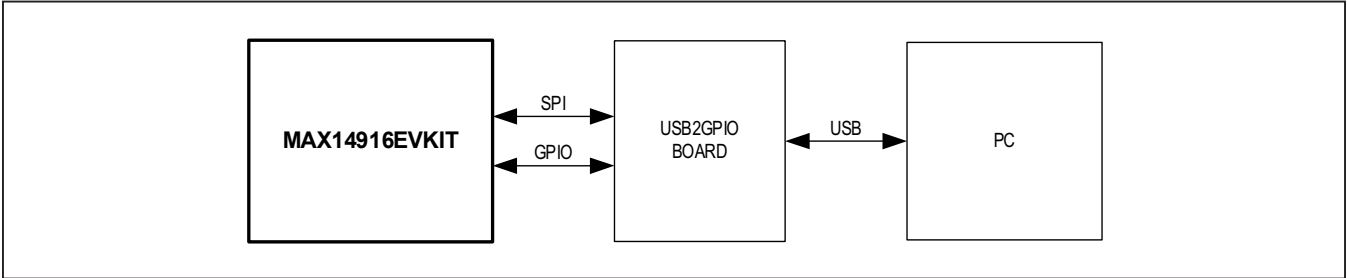
- Robust Operation with Wide Range Of Input Voltages and Load Conditions
- VDDOK Indication
- LED Indication of Channels Status and Fault Conditions
- Fast Inductive Load Demagnetization
- Open-wire, Overload, Undervoltage, Overcurrent, Thermal Shutdown Fault Conditions Indication
- Supports Watchdog and SYNCH Features
- Communication Error Indication
- Wide Logic Voltage Range
- Pin Addressable SPI Communication
- -40°C to +125°C Temperature Range
- Proven PCB Layout
- Fully Assembled and Tested
- Windows 10 Compatible Software

Ordering Information appears at end of data sheet.

MAX14916 EV System



System Block Diagram



MAX14916 EV kit Files

FILE	DESCRIPTION
MAX14915_6EV kitSetupV1.1.exe	Application Program (GUI)

Quick Start

Required Equipment

- MAX14916 EV kit
- USB2GPIO EV kit (must be ordered separately)
- +24V DC power supply
- PC with installed Windows 10 and a USB port
- USB-A to micro-USB cable (not included)

Note: In the following section(s), software-related items are identified by **bolding**. Text in **bold** refers to items directly from the EV system software. Text in **bold and underline** refers to items from the Windows operating system.

Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation:

- 1) Visit [HERE](#) and click on *Design Resources* to download the latest version of the EV kit software, MAX14915_6EVKITSetupV1.1.exe.
- 2) Install the EV kit software on your computer by running the MAX14915_6EVKITSetupV1.1.exe program inside the temporary folder. The program files are copied to your PC and icons are created in the Windows **Start | Programs** menu.
- 3) Verify that all jumpers are in their default positions ([Table 1](#)).
- 4) Power up the EV kit with +24V from an external power supply through J1 and J2 Banana Plugs.
- 5) Connect the EV SYS to a USB port of a PC. A micro-USB cable is not included and should be ob-

tained locally.

- 6) Start the EV kit software by opening its icon in the **Start | Programs** menu. Select the MAX14916EVKIT button in the startup window. The EV kit software appears as shown in [Figure 1](#). Verify that the lower-right status bar indicates the EV kit hardware is **Connected**. The GUI automatically detects EV kit is connected to the PC and enables serial communication. Any configuration change can be made on Register Settings tab. (The following steps are used to verify functionality of the MAX14916)
- 7) Select Register Settings tab and press the Read All button twice to clear the initially detected undervoltage global conditions in the GlobalErr register 0x09.
- 8) Enable the desirable diagnostics in registers 0x0A through 0x0F. For example, allowing STATUS LEDs and FAULT LEDs to be controlled autonomously by the internal logic, by disabling SLEDSet and FLEDSet bits in the Config1 register 0x0D[1:0] = 00b. Select register 0x0D in the Register map table on the left and choose “0: Disabled” from the pull-down menu of the bit Setting column of the register description table on the right. The font color of the modified register is changed from black to red. Click Write Modified button to write a new configuration into the register.
- 9) Set all OUTPUT switches ON, by typing in 255 decimal number into SetOUT register 0x00. Note, the GUI accept decimal, hex or binary numbers (e.g., 255, or 0xFF, or 0b11111111). The user can enable Auto Write button to allow auto write the changes instead of clicking Write Modified or Write Selected buttons, that allow individual command to be sent to the MAX14916.

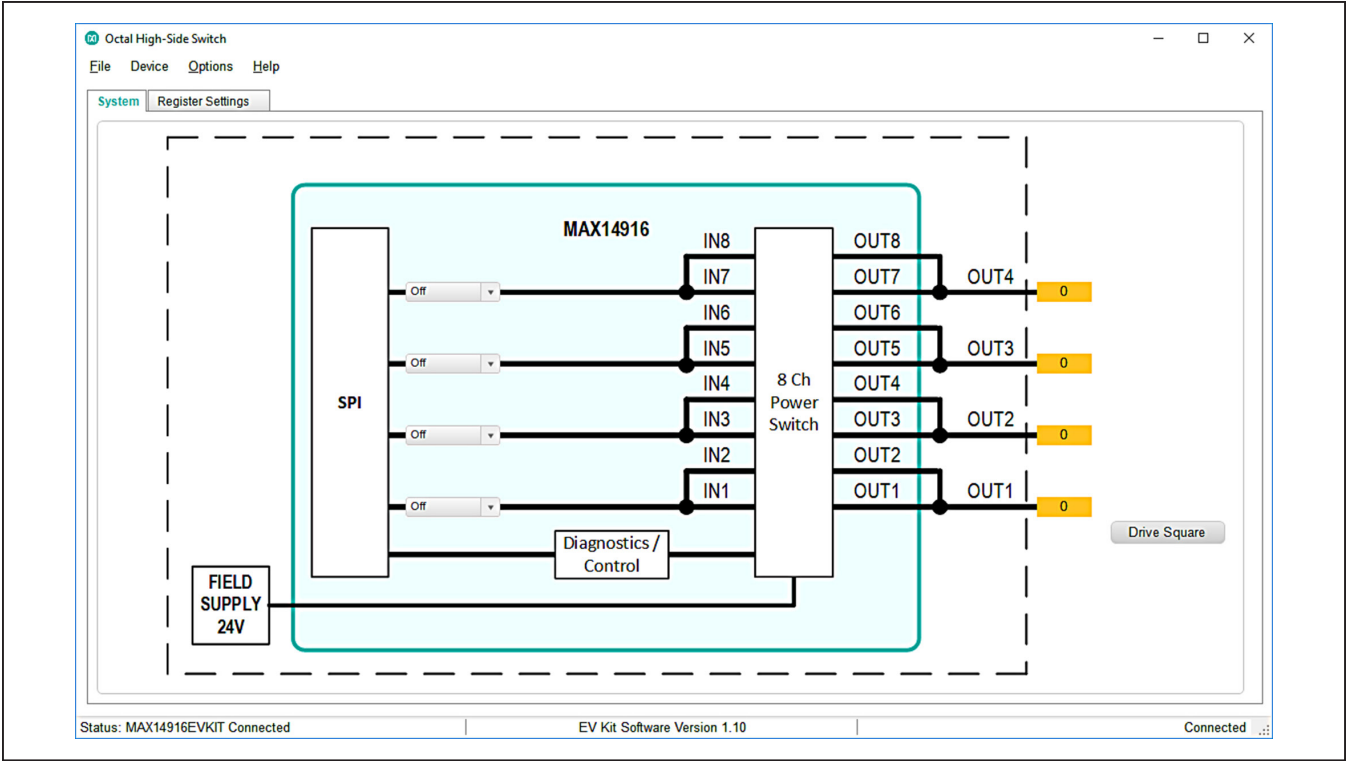


Figure 1. MAX14916 EV Kit GUI System Tab

The Register Settings tab displays the configuration for the MAX14916. It includes a table of registers, a table of I/O pins, and a section for jumper settings on the EV-KIT.

Addr	R/W	Register	Value	Description
0x00	R/W	SetOUT	0b00000000	Sets the Output Switch on / off
0x01	R/W	SetFLED	0b00000000	Sets Fault LEDs
0x02	R/W	SetSLED	0b00000000	Sets Status LEDs
0x03	R	Interrupt	0b00000000	Provides IRQ Info
0x04	R	OWChF	0b00000000	Over Load fault per Channel
0x05	R	CurrLimF	0b00000000	Current Limit fault per Channel
0x06	R	OwOffChF	0b00000000	Open Wire Fault Off-State per Channel
0x07	R	OwOnChF	0b00000000	Open Wire Fault On-State per Channel
0x08	R	ShTVDDChF	0b00000000	Short to VDD Fault per Channel
0x09	R	GlobalErr	0b00000000	Global Errors
0x0A	R/W	OwOffEn	0b00000000	Enable Open Wire detection Off-State
0x0B	R/W	OwOnEn	0b00000000	Enable Open Wire detection On-State
0x0C	R/W	ShTVddEn	0b00000000	Enable Short to VDD Fault
0x0D	R/W	Config1	0b01010011	Global Configuration 1
0x0E	R/W	Config2	0b00000000	Global Configuration 2
0x0F	R/W	Mask	0b10111110	Enable Global Error detection

Bit	Name	Value	Setting	Description
B[7]	On8	0b0	0: Off	Switch 8
B[6]	On7	0b0	0: Off	Switch 7
B[5]	On6	0b0	0: Off	Switch 6
B[4]	On5	0b0	0: Off	Switch 5
B[3]	On4	0b0	0: Off	Switch 4
B[2]	On3	0b0	0: Off	Switch 3
B[1]	On2	0b0	0: Off	Switch 2
B[0]	On1	0b0	0: Off	Switch 1

Write Modified Write Selected Read Selected Read All Auto Write

MAX14916 I/O pins

Pin Name	Set	Setting	Read	Direction
EN	Enabled	1	1	IN
CRREN	Off	0	0	IN
SYNCH	On	1	1	IN

Status

Signal	Value	Direction
READY#	Normal Operation	0 OUT
FAULT#	Normal Operation	1 OUT
COMERR#	Normal Operation	1 OUT

Last SDO Diagnostic Result

Signal	Value	Description
SHRT24	0	Short to 24V present
WBOF	0	Wire Break Switch On
WBOFF	0	Wire Break Switch Off
CurrLim	0	Current Limiting
OVdF	0	Overload Fault
GLOBLF	0	Global Fault
Fault Bits	0b00000000	

Device Mode Info

Chip Mode: SPI-Addressing, without CRC, Atr=0

SPI: SPI_TX: 0x0100; SPI_RX: 0xc0; 0x00

Jumper Settings on EV-KIT

Address (set by jumpers): 00

Figure 2. MAX14916 EV Kit GUI Register Settings Tab

Detailed Description of Hardware

The MAX14916EV kit in conjunction with the USB2GPIO# adapter board provides easy to use and flexible solution for evaluation of the MAX14916, octal high-side switch for industrial applications. It allows SPI communication between the Windows compatible GUI installed on a PC, and the MAX14916. The USB2GPIO# adapter board is a plug and play device that is powered from the USB port and does not require any additional configuration, refer to the USB2GPIO data sheet [HERE](#). A USB driver for the USB2GPIO# board is installed automatically with the MAX14916 GUI.

The MAX14916EVKIT# can be used as a standalone board connected to the SPI bus using J4 and/or J5 headers, refer to the MAX14916 EV kit schematic. Up to four EV kits can be connected to the same SPI bus with

selectable address for each board by appropriate J4 and J5 jumper settings. For full configuration options, refer to [Table 1](#).

Load for each channel should be connected to the J10 terminal block. Each channel (switch) can provide about 1A of current (typ) and can handle either resistive or inductive load.

On board diagnostics provide VDD status through VDDOK LED (DS9), communication error via COMERR# LED (DS18) and a global fault condition via FAULT# LED (DS19). Per channel output state and per channel fault conditions are visible via LED matrix, DS1 through DS4, and DS5 through DS8, correspondently. Other diagnostics are provided through the SPI interface by reading the diagnostic registers 0x03 through 0x09.

Table 1. MAX14916 Board Shunt Positions & Settings

HEADER	SHUNT POSITION	DESCRIPTION
J9	1-2*	VLED supplied from VDD.
	Open	Use an external VLED source. Apply VLED power between VLED test point TP8 and GND (TP9).
J6	1-2	Select 3.3V logic level (VL = VA).
	Open*	Logic voltage (VL) supplied from USB2GPIO board (3.3V). Use an external source between VL and GND (TP5) if another host controller is used.
J8	Open*	Internal 3.3V VA regulator enabled.
	1-2	Internal VA regulator disabled (REGEN = GND). Use an external VA source between VA test point and GND (TP4).
J4	1-2	Set address bit A0 = 1.
	2-3*	Set address bit A0 = 0.
J5	1-2	Set address bit A1 = 1.
	2-3*	Set address bit A1 = 0.

*Default configuration

Detailed Description of Software

The MAX14916 GUI provides access to all registers and allows full configuration and control of the MAX14916. There are two tabs available to control the EV kit. The System tab provides system-level control of the selected output pins, including static and dynamic control. The Register Settings tab provides per-channel and enhanced diagnostic configuration.

System Tab

The **System** tab allows driving the output pins by configuring each output either on, off, or driving Square wave frequency from pull-down menu, as shown in [Figure 3](#).

Click Drive Pins button on the right-side of the GUI to drive the outputs. The indicators connected to the OUT_ pins show the state of each output.

Connect the oscilloscope probe to OUT_ test points on the **EV kit** to see the output signal in real-time.

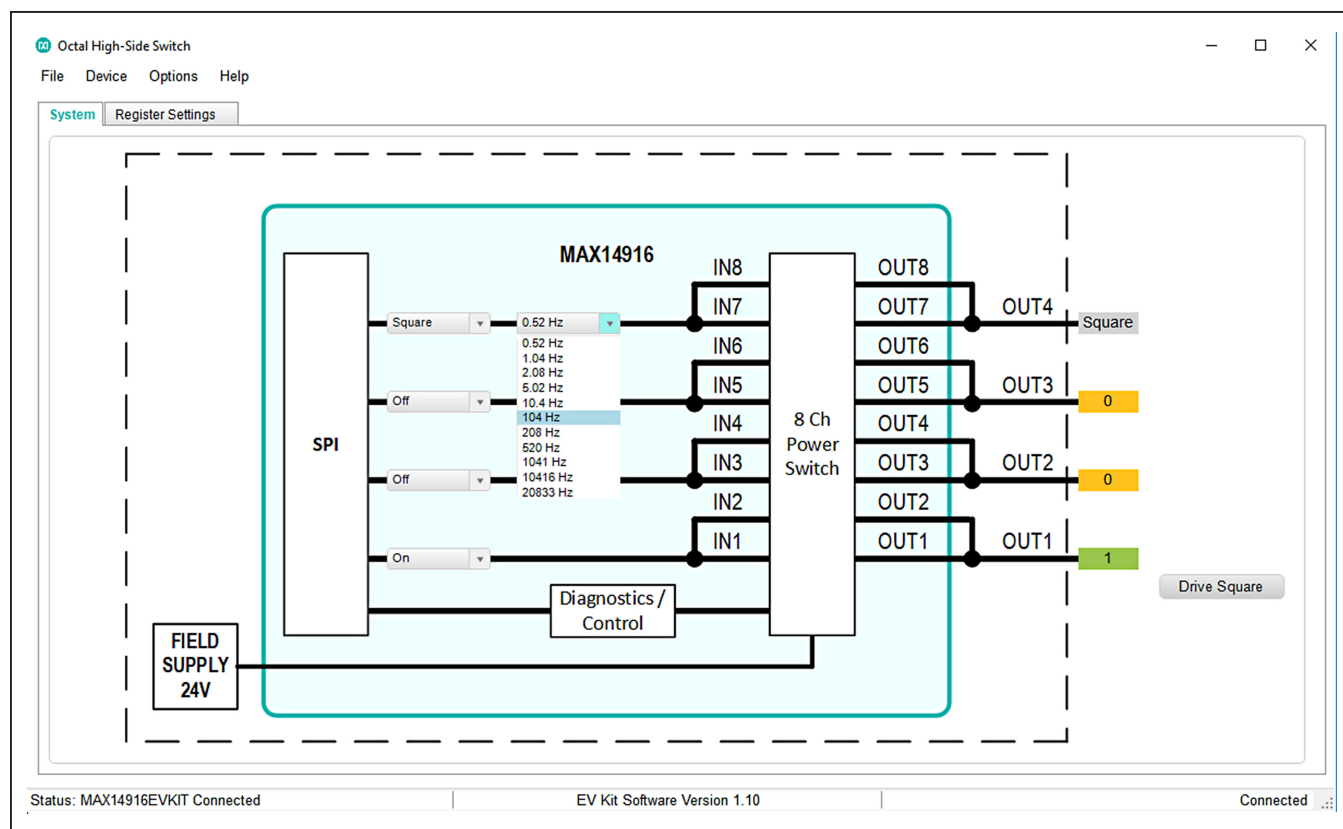


Figure 3. System Tab. Output Configuration

Register Settings Tab

The Register Settings tab allows detailed configuration of the device to explore all the available features, refer to [Figure 4](#). The full register map table of the MAX14916 is located on the left-side of the tab, and the bit-by-bit control and description table is located on the right side. When the register is selected in the register map table, the detailed description of each bit is shown on the right table. The register setting can be changed directly in the register map table by double-clicking on the Value cell. Each data entry should follow by the “Enter/Return” button on the keyboard. The Value cell accepts binary (0b), decimal or hex (0x) numbers. The modified register changes its color from black to red until the data will be actually written to the register. The data in the right table can be changed using drop down menus in the Setting cell for each bit individually. Both tables are synchronized that changes made in one table appear at both tables. There are several write and read options available through the corresponding control buttons located below the register bit-by-bit description table.

When the Auto Write button is selected, any data typed in, or selected through the Setting pull-down menu will be automatically written into the corresponding writable register. The button renamed to Stop Auto Write and auto-write function can be canceled by clicking on this button second time.

When the Auto Read button is selected, the write function is disabled, and the GUI is constantly monitoring the status and fault conditions of the device. Clicking a second time

on the button, which becomes Stop Auto Read, allows canceling this operation.

The Read All button performs a read operation of all registers after each click.

When the fault conditions occur, they will set the bit(s) in the corresponding read-only registers 0x03 to 0x09. The fault condition should be carefully evaluated and removed externally (over/under voltage, overload, open wire, etc.). It is recommended to read Interrupt (0x03) and Global Error (0x09) registers first to identify what kind of fault conditions happened, then read per-channel diagnostic registers 0x04 to 0x08 twice to make sure that condition is gone and to clear interrupts.

The Write Selected button allows to write to the selected register only, while the Write Modified button performs write operation to all modified registers after each click.

There are an I/O pins control and status box and per-bit diagnostic result provided by the MAX14916 after each SPI write or read operation below the buttons. The EN slider allows enable or disable OUTs, CRCEN enables or disables error-detecting code to be added to each SPI transaction and SYNCH slider allows manual synchronization of multiple settings.

A user must match the A0 and A1 jumper position EV kit with the SPI address selected from the Address pulldown menu, located below the register map table. The default address is 00.

Each SPI transaction is displayed in the Device Mode Info box for user convenience.

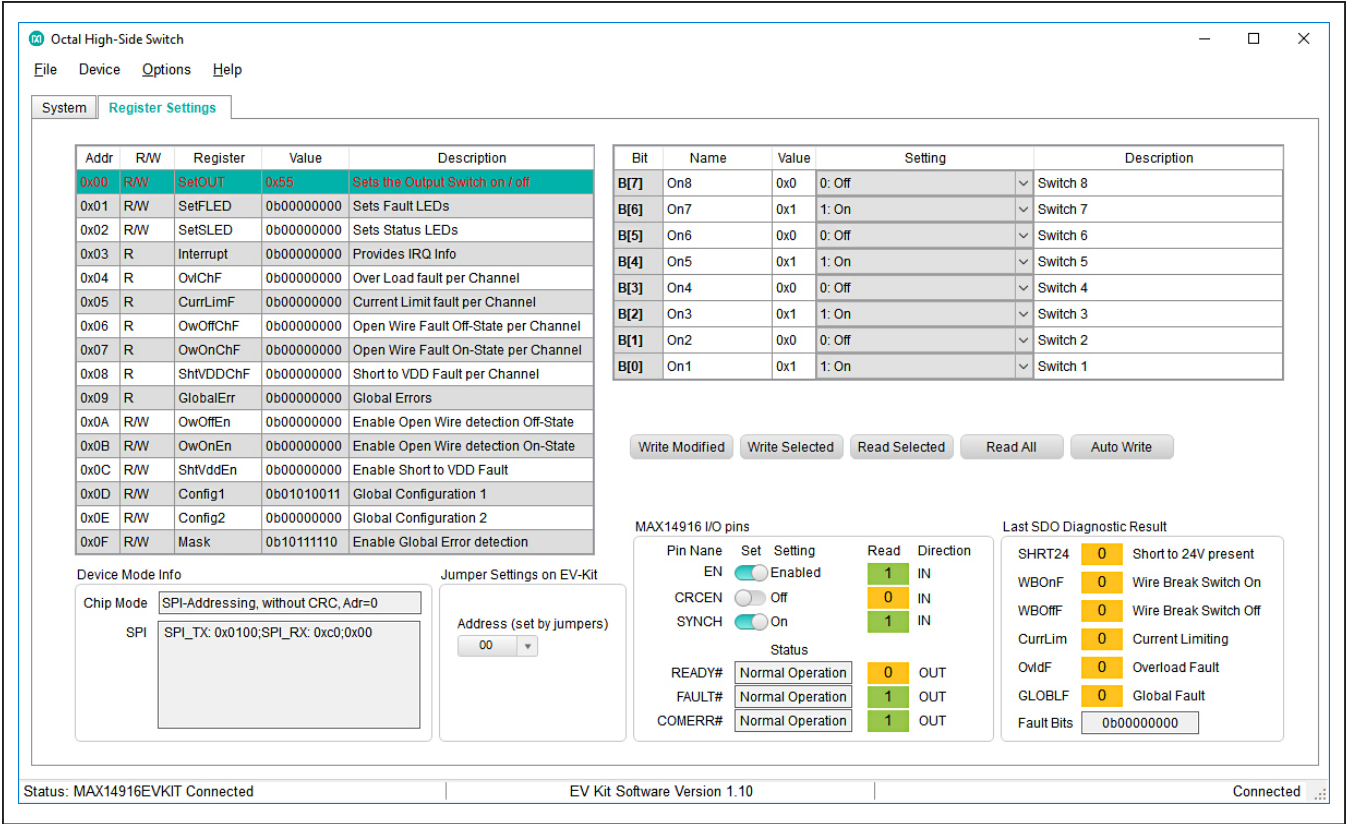


Figure 4. Register Settings Tab

Ordering Information

PART	TYPE
MAX14916EVKIT#	EV Kit
USB2GPIO#	EV Kit

#Denotes RoHS compliance.

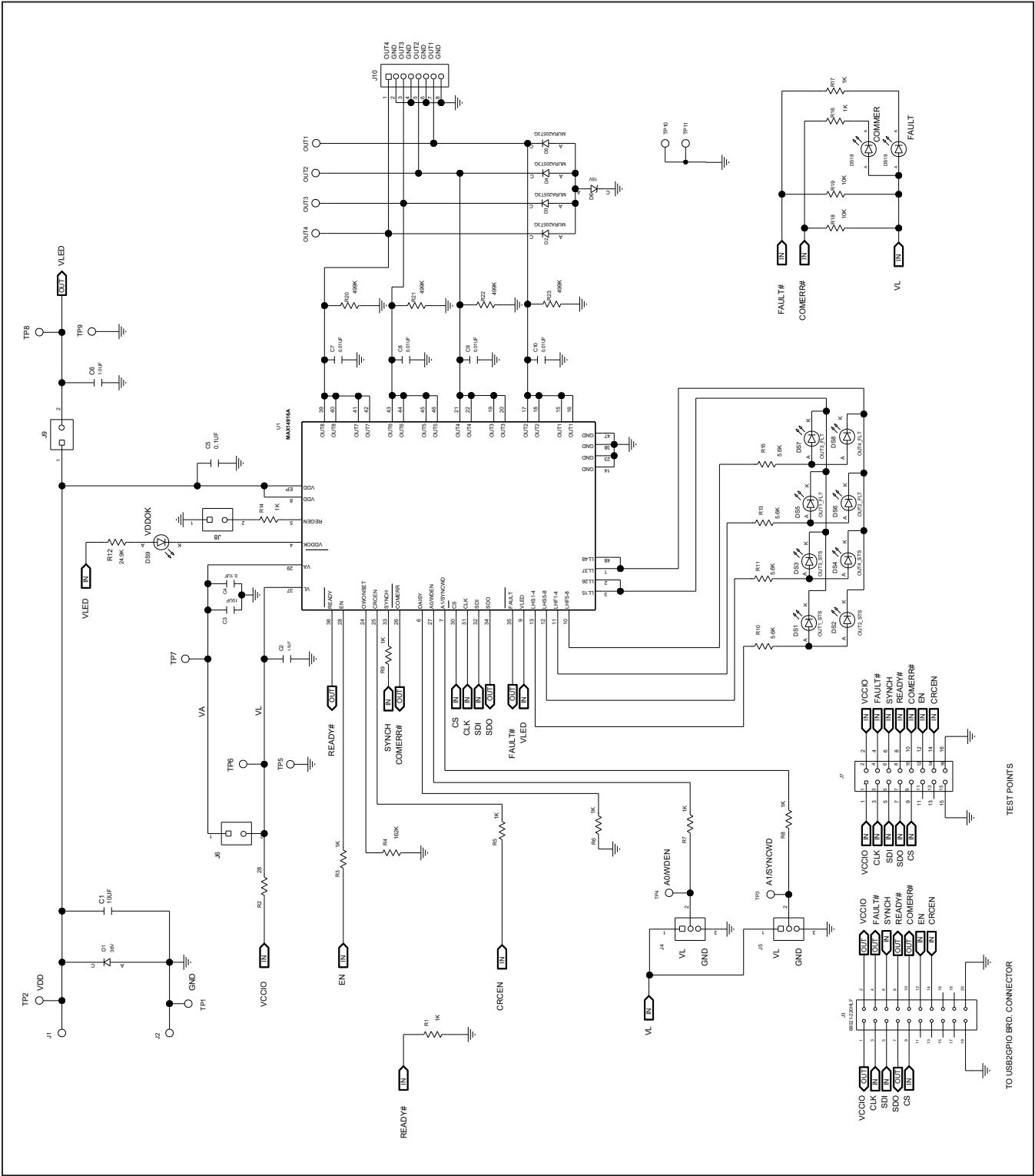
MAX14916 EV Kit Bill of Materials

ITEM	REF_DES	QTY	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
1	C1	1	C3225X7S1H106K250AB; CGA6P3X7S1H106K250AB; GCM32EC71H106K	TDK;TDK;MURATA	10μF	CAPACITOR; SMT (1210); CERAMIC CHIP; 10μF; 50V; TOL = 10%; TG = -55°C TO +125°C; TC = X7S
2	C2, C6	2	GMK212B7105KG; GRM219R7YA105KA12	TAIYO YUDEN; MURATA	1.0μF	CAPACITOR; SMT (0805); CERAMIC; 1UF; 35V; TOL = 10%; MODEL = GMK SERIES; TG = -55°C TO +125°C; TC = X7R
3	C3	1	CGA4J1X7S1C106K125; GCM21BC71C106KE35	TDK;MURATA	10μF	CAPACITOR; SMT (0805); CERAMIC CHIP; 10μF; 16V; TOL = 10%; TG = -55°C TO +125°C; TC = X7S; AUTO
4	C4, C5	2	CC0603KRX7R0BB104; GRM188R72A104KA35; GCJ188R72A104KA01; HMK107B7104KA; 06031C104KAT2A	YAGEO; MURATA; MURATA; TAIYO YUDEN; AVX	0.1μF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.1μF; 100V; TOL = 10%; TG = -55°C TO +125°C; TC = X7R
5	C7-C10	4	CGA3EANP02A103J080AC	TDK	0.01μF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.01μF; 100V; TOL = 5%; MODEL = MULTILAYER CERAMIC CHIP CAPACITOR; TC = NPO
6	D1	1	SMBJ36A-E3	VISHAY GENERAL SEMICONDUCTOR	36V	DIODE; TVS; SMB (DO-214AA); VRM = 36V; IPP = 10.3A
7	D2-D5	4	MURA205T3G	ON SEMICONDUCTOR	MURA205T3G	DIODE; RECT; SMA (DO-214AC); PIV = 50V; IF = 2A
8	D6	1	SM30T15AY	ST MICROELECTRONICS	15V	DIODE; TVS; SMC (DO-214AB); VRM = 15V; IPP = 140A
9	DS1-DS4	4	LGL29K-G2J1-24-Z	OSRAM	LGL29K-G2J1-24-Z	DIODE; LED; SMARTLED; GREEN; SMT; PIV = 1.7V; IF = 0.02A
10	DS5-DS8, DS18, DS19	6	LS L29K-G1J2-1-Z	OSRAM	LS L29K-G1J2-1-Z	DIODE; LED; SMART; RED; SMT (0603); PIV = 1.8V; IF = 0.02A; -40°C TO +100°C
11	DS9	1	LTST-C171GKT	LITE-ON ELECTRONICS INC.	LTST-C171GKT	DIODE; LED; STANDARD; GREEN; SMT (0805); PIV = 5.0V; IF = 0.12A; -55°C TO +85°C
12	J1, J2	2	3267	POMONA ELECTRONICS	3267	CONNECTOR; MALE; PANELMOUNT; STANDARD UNINSULATED BANANA JACK; STRAIGHT; 1PIN
13	J3	1	68021-220HLF	AMPHENOL ICC	68021-220HLF	EVKIT PART-CONNECTOR; MALE; THROUGH HOLE; BERGSTIK II BREAKAWAY HEADER; RIGHT ANGLE; 20PINS;
14	J4, J5	2	PCC03SAAN	SULLINS	PCC03SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT THROUGH; 3PINS; -65°C TO +125°C
15	J6, J8, J9	3	PCC02SAAN	SULLINS	PCC02SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT THROUGH; 2PINS; -65°C TO +125°C
16	J7	1	PBC08DAAN	SULLINS ELECTRONICS CORP.	PBC08DAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 16PINS; -65°C TO +125°C
17	J10	1	OSTTE080104	ON-SHORE TECHNOLOGY INC.	OSTTE080104	CONNECTOR; MALE; THROUGH HOLE; TERMINAL BLOCKS-WIRE TO BOARD; STRAIGHT; 8PINS

MAX14916 EV Kit Bill of Materials (continued)

ITEM	REF_DES	QTY	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
18	J11-J14	4	9032	KEystone	9032	MACHINE FABRICATED; ROUND-THRU HOLE SPACER; NO THREAD; M3.5; 5/8IN; NYLON
19	OUT1-OUT4	4	5013	KEystone	N/A	TEST POINT; PIN DIA = 0.125IN; TOTAL LENGTH = 0.445IN; BOARD HOLE = 0.063IN; ORANGE; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH;
20	R1, R3, R5-R9, R14, R16, R17	10	CRCW06031K00FK; ERJ-3EKF1001	VISHAY DALE; PANASONIC	1K	RESISTOR; 0603; 1K; 1%; 100PPM; 0.10W; THICK FILM
21	R2	1	ERJ-3EKF28R0	PANASONIC	28	RESISTOR; 0603; 28Ω; 1%; 100PPM; 0.10W; THICK FILM
22	R4	1	CRCW0603162KFK	VISHAY DALE	162K	RESISTOR; 0603; 162KΩ; 1%; 100PPM; 0.1W; THICK FILM
23	R10, R11, R13, R15	4	CRCW06035K60FK	VISHAY DALE	5.6K	RESISTOR, 0603, 5.6KΩ, 1%, 100PPM, 0.10W, THICK FILM
24	R12	1	CRCW060324K9FK; ERJ-3EKF2492	VISHAY DALE; PANASONIC	24.9K	RESISTOR; 0603; 24.9KΩ; 1%; 100PPM; 0.10W; THICK FILM
25	R18, R19	2	301-10K-RC	XICON	10K	RESISTOR, 0603, 10KΩ, 5%, 200PPM, 1/16W, THICK FILM
26	R20-R23	4	CRCW0603499KFK; ERJ-3EKF4993	VISHAY DALE; PANASONIC	499K	RESISTOR; 0603; 499KΩ; 1%; 100PPM; 0.1W; THICK FILM
27	SU1-SU5	5	S1100-B; SX1100-B; STC02SYAN	KYCON; KYCON; SULLINS ELECTRONICS CORP.	SX1100-B	TEST POINT; JUMPER; STR; TOTAL LENGTH = 0.24IN; BLACK; INSULATION = PBT; PHOSPHOR BRONZE CONTACT=GOLD PLATED
28	TP1, TP5, TP9-TP11	5	5011	KEystone	N/A	TEST POINT; PIN DIA = 0.125IN; TOTAL LENGTH = 0.445IN; BOARD HOLE = 0.063IN; BLACK; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH;
29	TP2, TP6-TP8	4	5010	KEystone	N/A	TEST POINT; PIN DIA = 0.125IN; TOTAL LENGTH = 0.445IN; BOARD HOLE = 0.063IN; RED; PHOSPHOR BRONZE WIRE SIL;
30	TP3, TP4	2	5009	KEystone	N/A	TEST POINT; PIN DIA = 0.125IN; TOTAL LENGTH = 0.35IN; BOARD HOLE = 0.063IN; YELLOW; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH;
31	U1	1	MAX14916A	MAXIM	MAX14916A	EVKIT PART - IC; OCTAL 1A HIGH-SIDE SWITCH WITH DIAGNOSTICS; FCQFN48-EP; PACKAGE OUTLINE: 21-100232; PACKAGE LAND PATTERN: 90-100077
32	PCB	1	MAX14916	MAXIM	PCB	PCB:MAX14916
TOTAL		86				

MAX14916 EV Kit Schematic

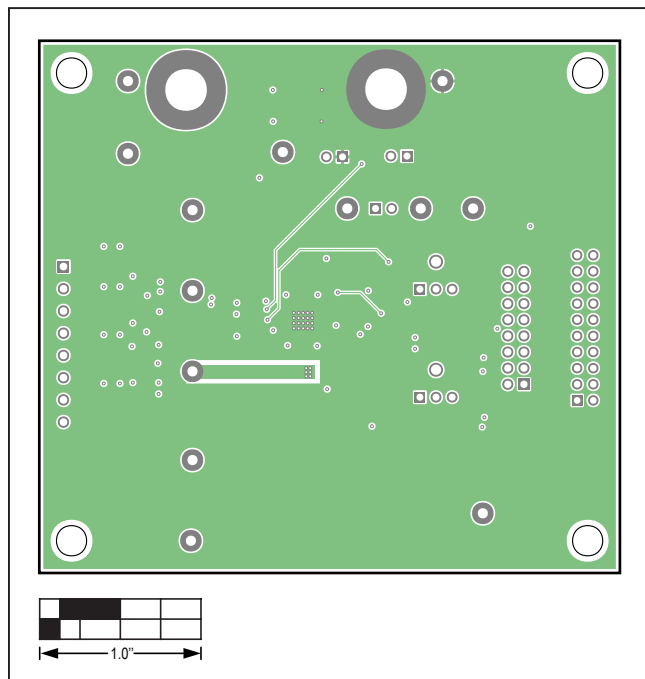


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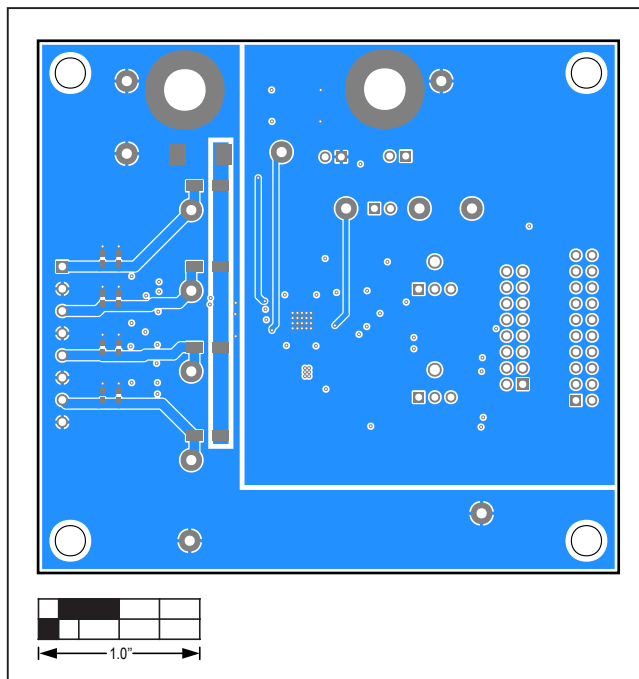
Figure 1 shows a grayscale image of a printed circuit board (PCB) layout. The board is rectangular with a central vertical slot. It features various components including circular pads, rectangular pads, and a grid of small squares. A scale bar at the bottom left indicates a length of 1.0 inch.

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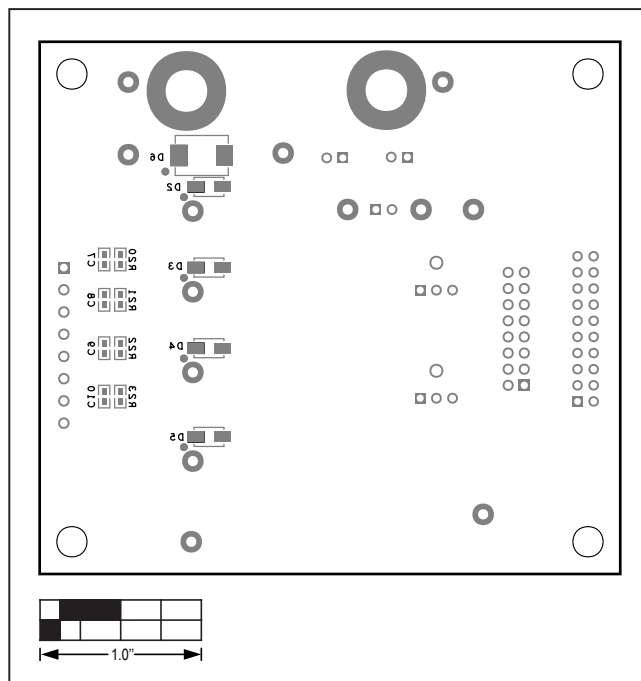
MAX14916 EV Kit PCB Layout Diagrams (continued)



MAX14916 EV Kit PCB Layout—Internal 3



MAX14916 EV Kit PCB Layout—Bottom View



MAX14916 EV Kit PCB Layout—Bottom Silkscreen

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	9/19	Initial release	—

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