

LTC2927

Single Power Supply Tracking Controller

DESCRIPTION

Demonstration circuit DC817A is intended for evaluating the performance of the LTC2927 Single Power Supply Tracking Controller. The board contains two LTC2927s tied to a common ramping signal, providing tracking and sequencing for two power supplies. As assembled, DC817A operates directly with two DC222 buck outputs.

Design files for this circuit board are available. Call the LTC factory.

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PERFORMANCE SUMMARY

Specification s are at $T_A = 25^\circ\text{C}$

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{CC}	Input Supply Voltage Range		2.6		5.5	V
$V_{CC(UVLO)}$	Supply Undervoltage Lockout	V_{CC} RISING	2.2	2.5	2.7	V
$\bullet V_{CC(UVLO)}$	Supply Undervoltage Lockout Hysteresis			25		mV
$V_{ON(TH)}$	ON Pin Threshold Voltage	V_{ON} RISING	1.210	1.230	1.250	V
$\bullet V_{ON(HYST)}$	ON Pin Hysteresis		30	75	150	mV
V_{TRACK}	TRACK Pin Voltage	$V_{TRACK} = -10\mu\text{A}$	0.77	0.8	0.82	V
		$V_{TRACK} = -1\text{mA}$	0.77	0.8	0.82	V
$V_{FB(CLAMP)}$	FB Pin Clamp Voltage	$1\mu\text{A} \cdot I_{FB} \cdot 1\text{mA}$	1.5	2.0	2.3	V

OPERATING PRINCIPLES

The LTC2927 controls the output voltage of a power supply by introducing a small current into the supply's feedback node. When this current is at its maximum, the supply's output is forced to zero; decreasing this current to zero allows the supply's output to rise to its normal, regulated output.

The RAMP pin is driven by an internally generated constant current source, which when loaded with capacitor, produces a linear voltage ramp. By varying the injected current under the control of this "master" ramping signal, the slave supply output is made to follow the master ramp in a predictable fashion.

Any slave power supply, which has an accessible inverting feedback node of less than 1.5V, and a feedback divider current in the range of $10\mu\text{A}$ to 1mA , may be controlled with the LTC2927. Tracking behavior is configured using two resistors, RTA and RTB. A valid V_{CC} supply for the LTC2927 must be present before the tracked supply is enabled or powered up.

Boost converters and certain linear regulators are generally incompatible with the LTC2927 control technique.

DC817A includes two LTC2927 controllers driven by a common ramp signal. The first controller

generates the ramp signal and monitors V_{CC} with its ON pin. The second controller's RAMP pin is driven by REMPBUF of the first; its ON pin is defeated by being tied to V_{CC} .

To reconfigure DC817A for use with other supplies, five components may be changed: C3 to control

the master ramp rate, and RTA1, RTB1, RTA2, and RTB2 to control the relative behavior of the two supplies. Hold-off time, ramp rate, and offset are all defined by the resistors. For details on calculating these component values, consult the LTC2927 data sheet.

QUICK START PROCEDURE

As a design example, DC817A is populated for operation with two DC222 slave supplies, using the LT1735 Buck Regulator, which are configured for 3.3V and 2.5V output. The first LTC2927 controls the 3.3V supply, while the second controls the 2.5V supply.

Connect the slave supplies as shown in Figure 1, attaching both the RUN/SS and VOSENSE lines to the DC817A. Next apply 5V to all boards; the outputs will begin tracking up once the ON pin of U1 detects a valid 5V input.

The tracking waveforms are shown in Figure 2 and Figure 3. Also shown in Figure 2 and Figure 3 is an optional "enable" waveform, which is simply a switch closure to ground on U1's ON pin (see Figure 1 for location of this pad). This may be driven by an open collector, such as an enable signal from a microcontroller or separate supply monitor.

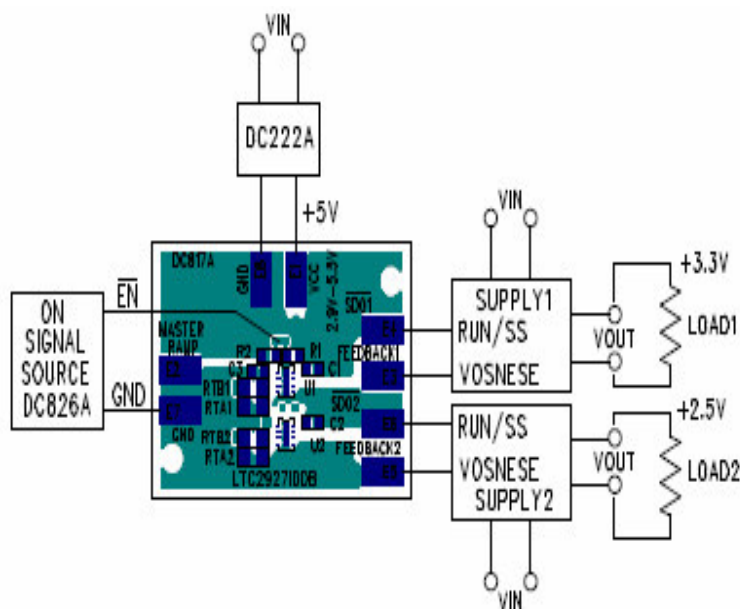


Figure 1. Test Setup

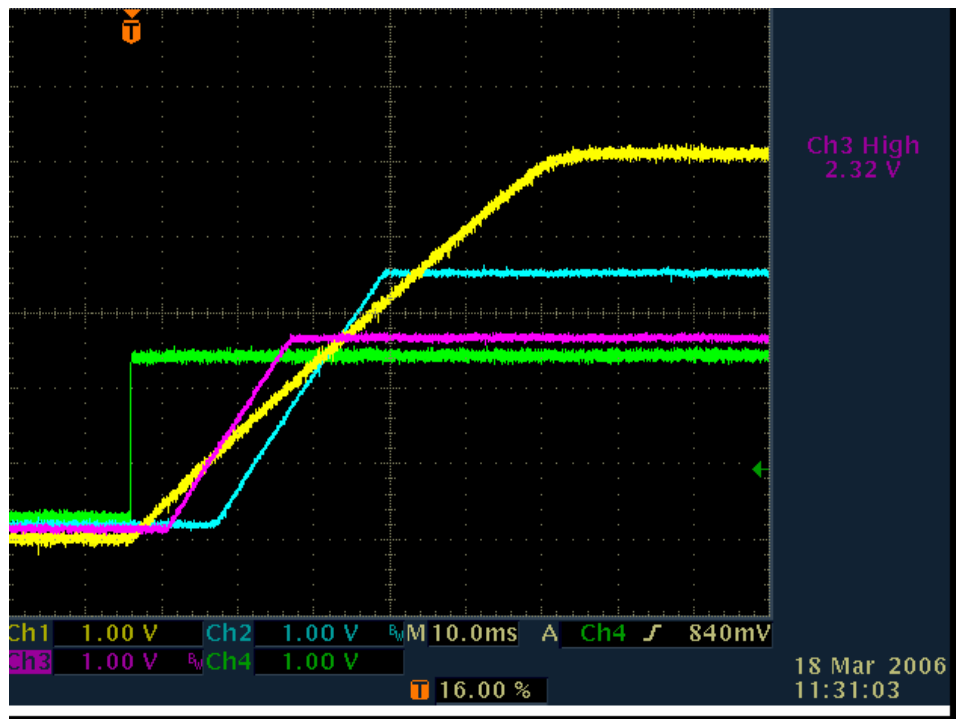


Figure 2. Startup Waveforms

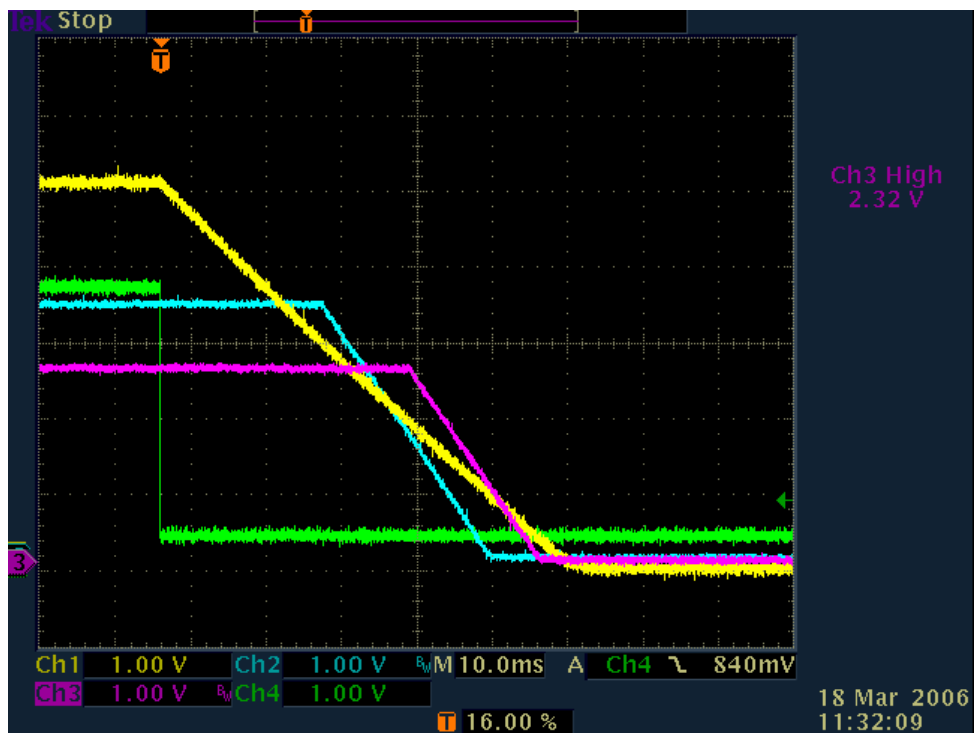


Figure 3. Shutdown Waveforms

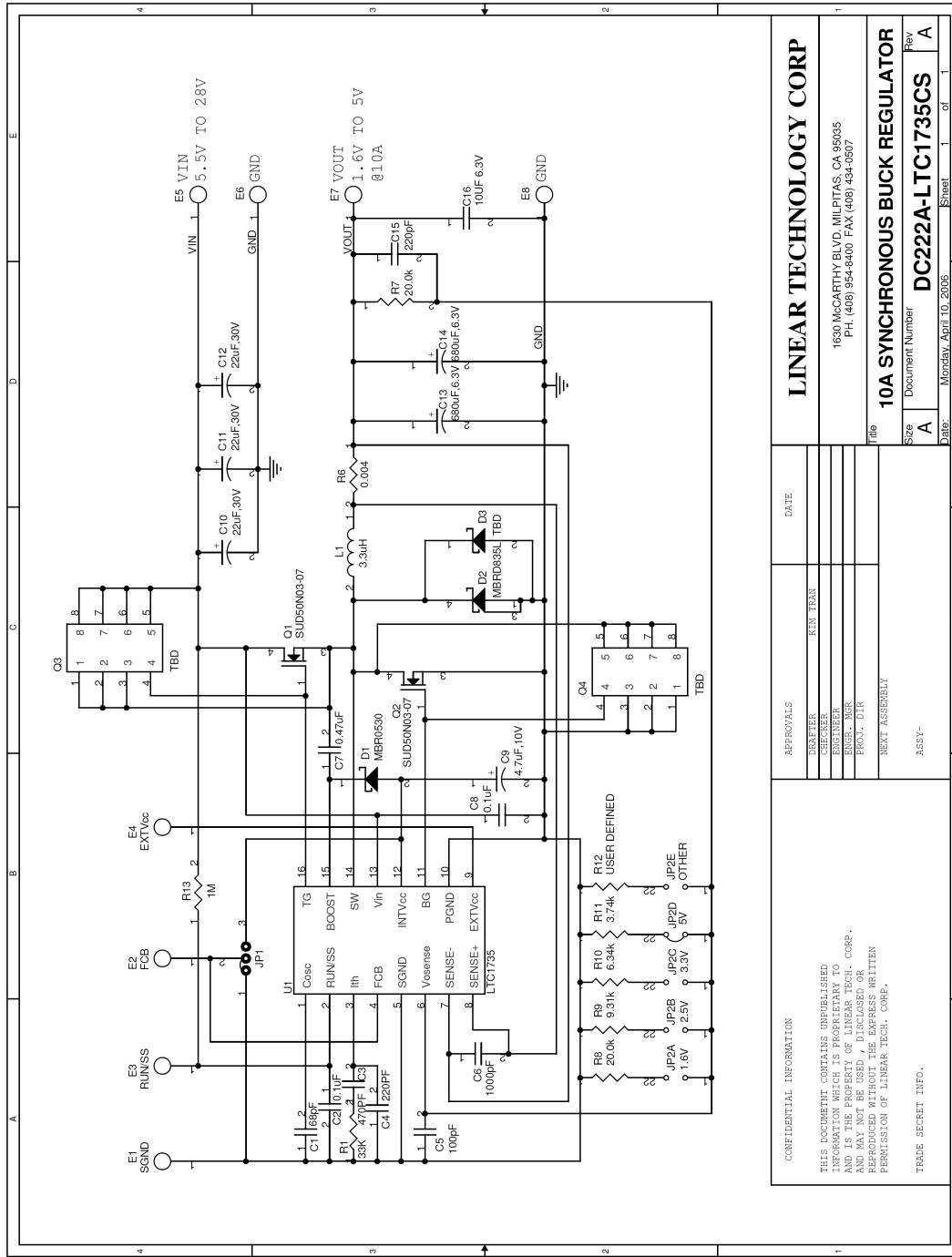


Figure 5. Reference B

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		CHECKER				PH: (408) 354-8400 FAX: (408) 444-6507	
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