

# Product Discontinuation Notice for Development Systems Products

XCN13030 (v1.0) September 30, 2013

**Product Discontinuation Notice** 

#### Overview

The purpose of this notification is to communicate that Xilinx is discontinuing Specific Development Systems part numbers only for Partial Reconfiguration for ISE<sup>®</sup> Software and Partial Reconfiguration Bundled Training, and select Development and Evaluation Kits.

# **Description**

As part of Xilinx regular product pruning activities the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at Xilinx.com.

#### **Products Affected**

- 1. Partial Reconfiguration
- 2. Virtex®-4 Series Development Kits
- 3. Development Accessories

# **Key Dates and Ordering Information**

#### **Partial Reconfiguration**

Final orders accepted until December 17, 2013.
Final orders shipped by December 26, 2013.
Existing Warranty Contracts will be honored.
Replacement parts will be available after December 17, 2013.

#### **Virtex-4 Series Development Kits**

Supplies of this product may be limited.
Final orders accepted until March 3, 2014 or until supply is exhausted.
Final orders shipped by June 2, 2014.
No technical support available after June 2, 2015.

#### **Development Accessories (Micron VGA CMOS Camera Module)**

Inventory of this product is exhausted. No new orders will be accepted. Existing Warranty Contracts will be honored.



# **Technical Support**

For Technical Support on these products refer to Solution Center Xilinx Answer Record 40174. Support for these products will be limited to tools usage and existing documentation.

Table 1: Partial Reconfiguration

Part Number	Description	Replacement Part
EF-PR-ISE-FL	Partial Reconfiguration Feature for Vivado and ISE Software, Floating License	EF-PR-VIVADO-FL
EF-PR-ISE-NL	Partial Reconfiguration Feature for Vivado and ISE Software, Node-Locked License	EF-PR-VIVADO-NL
UEF-PR-ISE	University Partial Reconfiguration Feature for Vivado and ISE Software	UEF-PR-VIVADO
EF-PR-ISE-NL-TC1	Partial Reconfiguration Feature for ISE Software, Node Locked License; incl 2 Day On-Site Training	No Replacement, use standard training credits
EF-PR-ISE-FL-TC1	Partial Reconfiguration Feature for ISE Software, Floating License; incl 2 Day On-Site Training	No Replacement, use standard training credits

Table 2: Virtex-4 Series Development Kits

Part Number	Description	Replacement Part
HW-V4-ML402-UNI-G	Virtex-4 DSP and General Purpose/FPGA	EK-Z7-ZC702-G
	Evaluation Board	
HW-V4-ML402-UNI-G-J	Virtex-4 DSP and General Purpose/FPGA	EK-Z7-ZC702-G-J
	Evaluation Board, Japan Specific	

Table 3: Development Accessories

Part Number	Description	Replacement Part
HW-MCCAM-G	Micron VGA CMOS Camera Module	No Replacement

#### NOTE:

1. The last version of software supporting this original Virtex FPGA family is ISE® 10.1, which can be downloaded on http://www.xilinx.com/support/download/index.htm



### Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis. All orders placed within 60 days of the last time buy date will be non-cancellable and non-returnable (NCNR). For additional information or questions, contact your local Xilinx Sales representative.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<a href="http://www.xilinx.com/support">http://www.xilinx.com/support</a>). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Xilinx Answer Record 18683.

# **Revision History**

The following table shows the revision history for this document.

Date	Version	Revision
9/30/13	1.0	Initial release

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