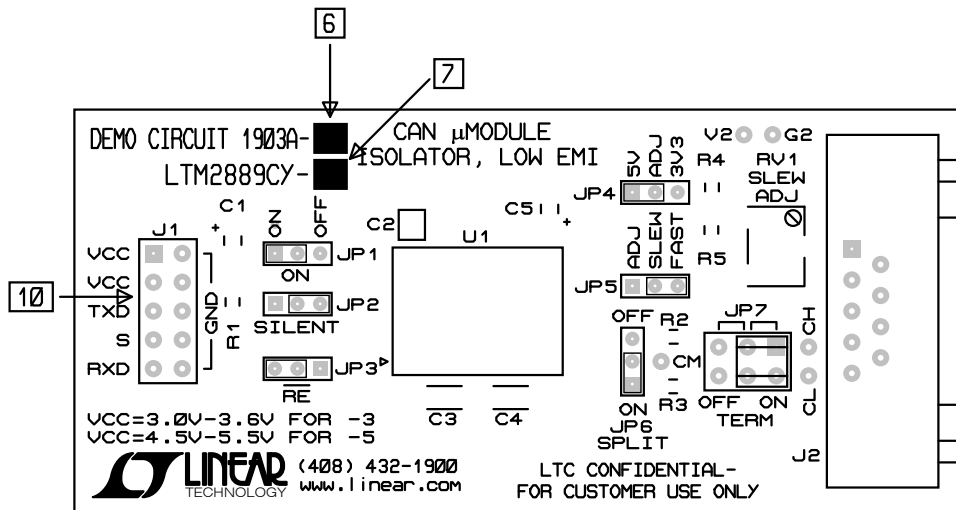


# REVISION HISTORY

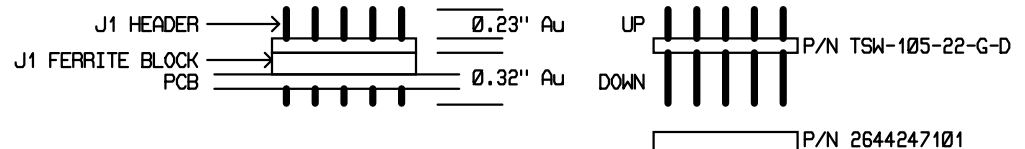
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	2ND PROTOTYPE	KEITH B.	05-18-15

## NOTES: UNLESS OTHERWISE SPECIFIED


1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610
2. MAXIMUM SOLDER TEMPERATURE IS 240 DEGREES CELSIUS
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD
4. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
5. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD
6. MARK ASSEMBLY TYPE WITH BLACK PERMANENT MARKER WHERE SHOWN
7. MARK IC PART NUMBER WITH BLACK PERMANENT MARKER WHERE SHOWN
8. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD
9. INSTALL SHUNTS AS SHOWN
10. PLACE FERRITE BLOCK ON LONG SIDE HEADER PINS BEFORE BOARD INSERTION



TOP SILKSCREEN  
LINEAR TECHNOLOGY  
DC1903A-2 \* LTM2889CY-3/-5  
CAN μMODULE ISOLATOR, LOW EMI  
DATE: 05-18-15



ASSY	U1
-A	LTM2889CY-3
-B	LTM2889CY-5

APPROVALS		 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408) 432-1900 Fax: (408) 434-0507 www.linear.com		LTC CONFIDENTIAL FOR CUSTOMER USE ONLY
PCB DES.	KEITH B.			
APP ENG.	KEITH B.			
		TITLE: ASSEMBLY DRAWING CAN uMODULE ISOLATOR, LOW EMI		
		SIZE	IC NO.	REV.
		N/A	LTM2889CY-3/-5 DEMO CIRCUIT 1903A	2
SCALE = 1.5:1		DATE: Monday May 18, 2015		SHEET 1 OF 1