

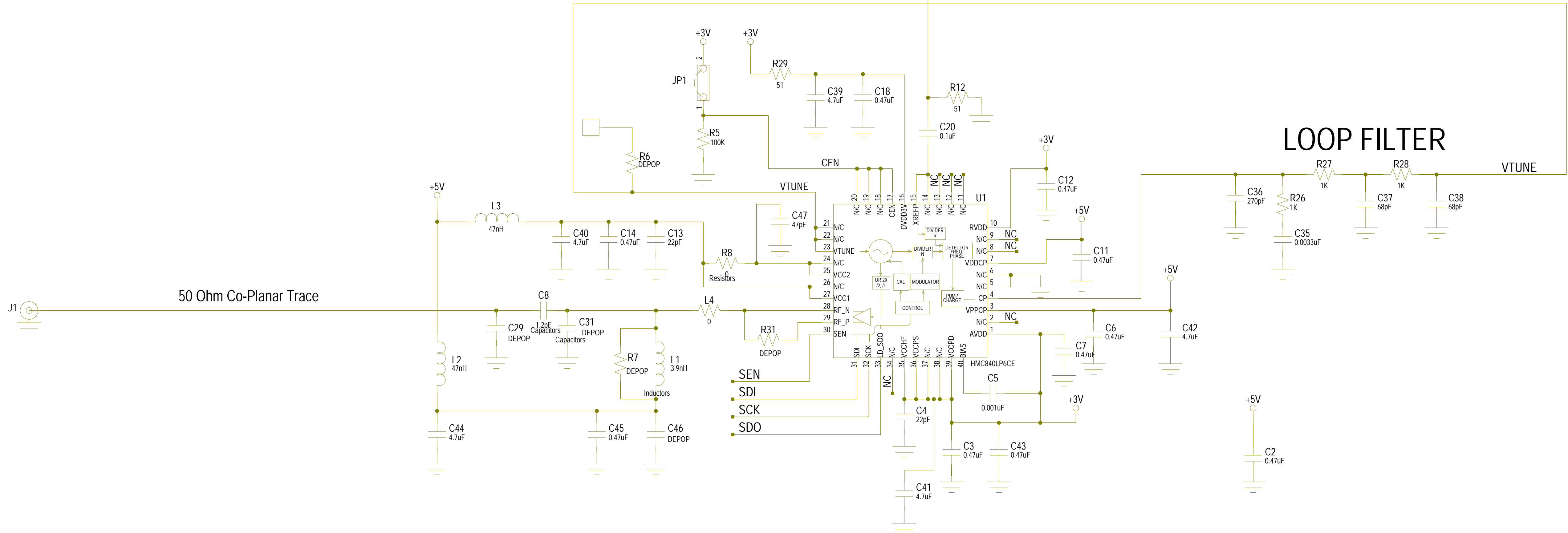
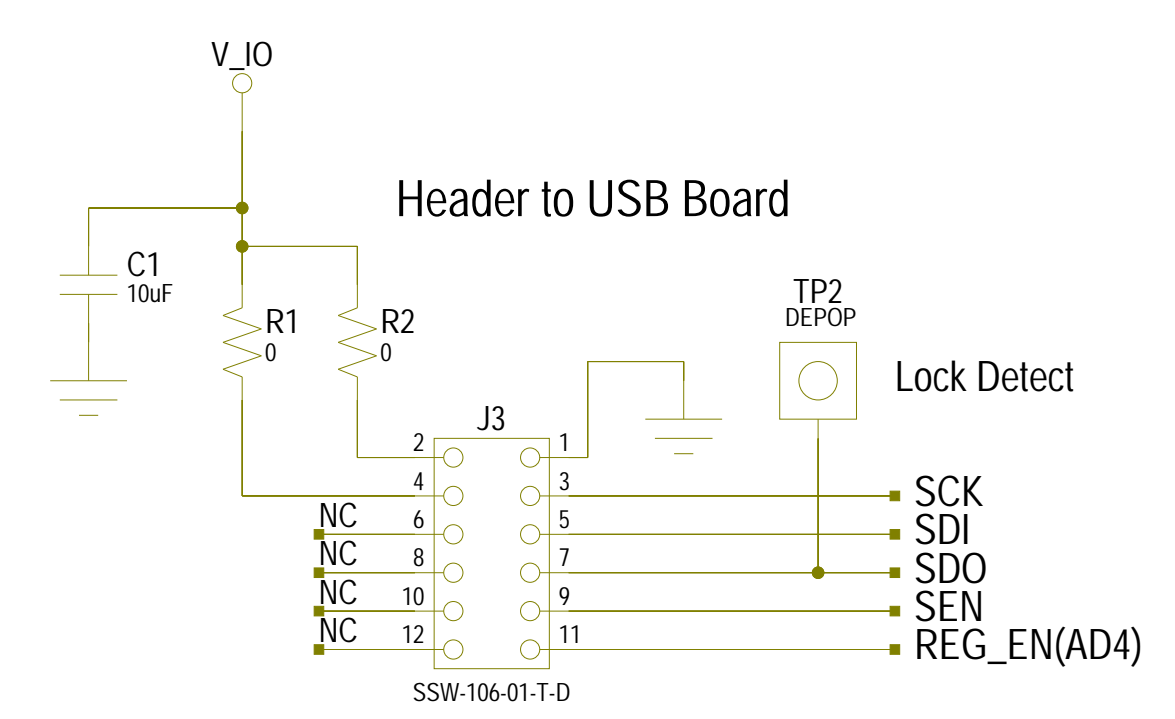
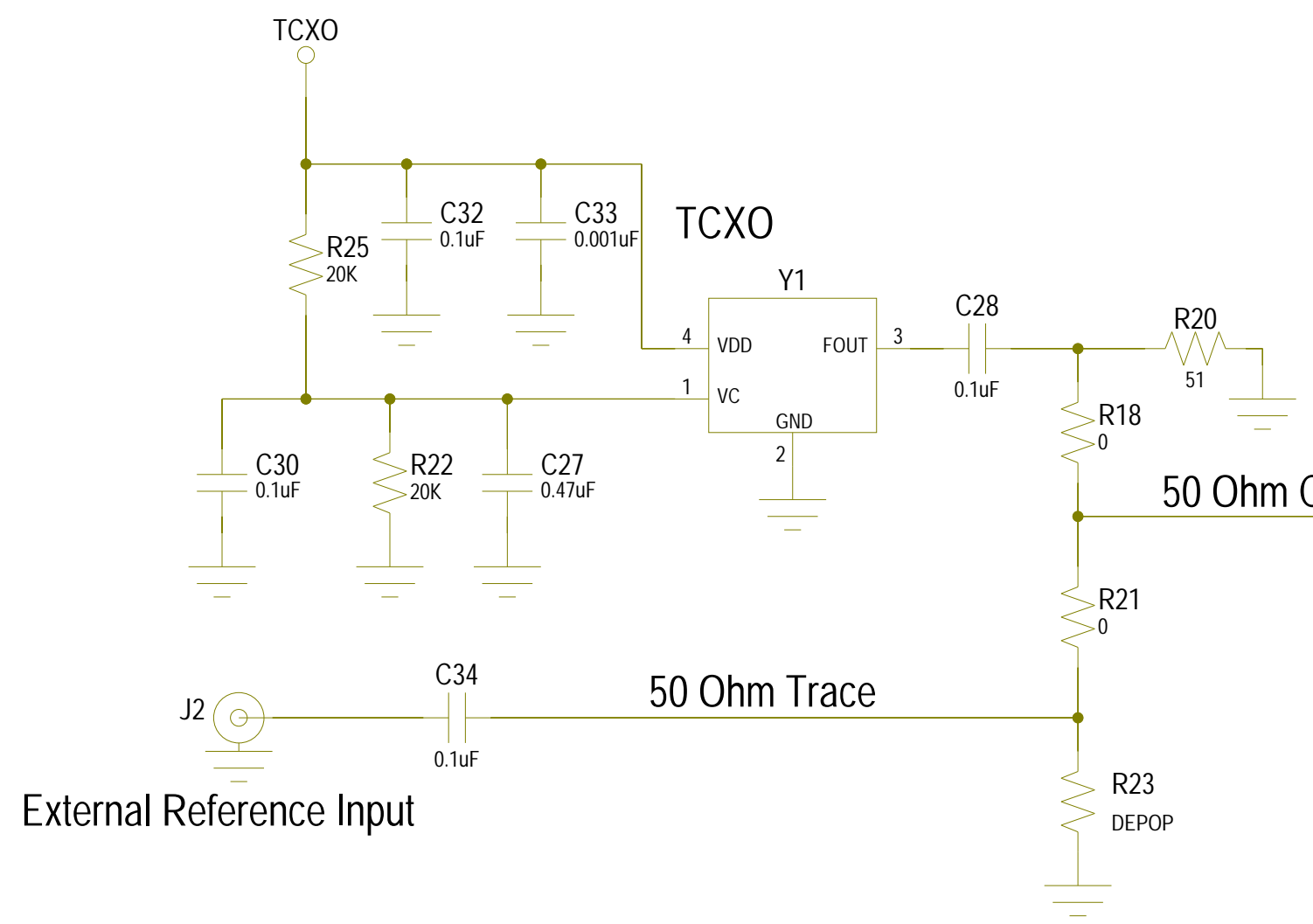
Analog Devices Welcomes Hittite Microwave Corporation

NO CONTENT ON THE ATTACHED DOCUMENT HAS CHANGED

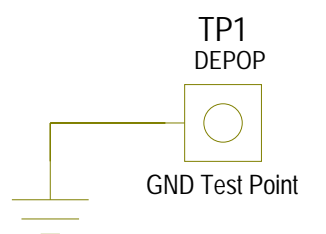


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REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
B	CP100519	----	RELEASED PRODUCTION ECN CP100519	V. VADUVA	12/29/2010
C	CP121275	----	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012

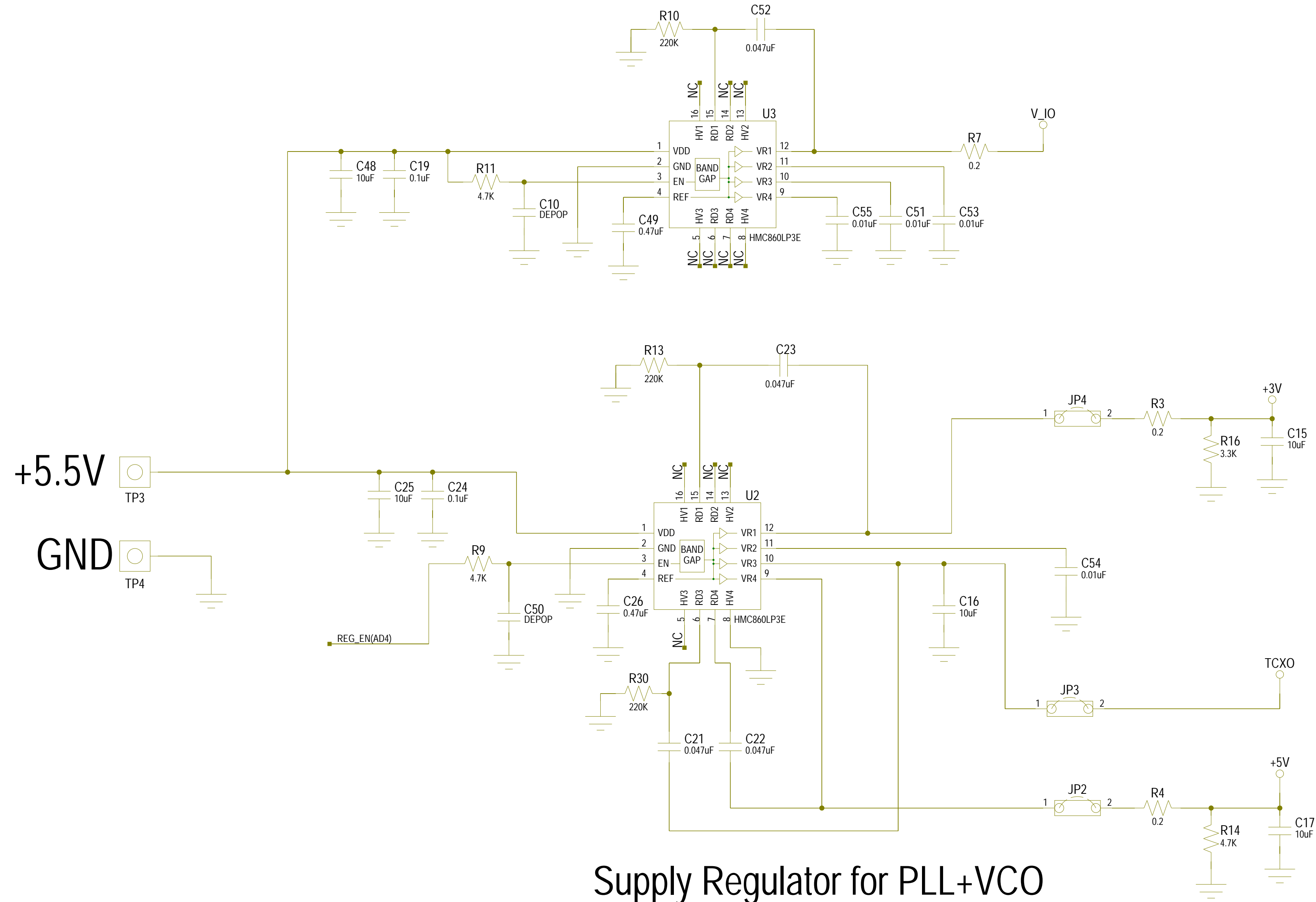


HMC840 Fo & FO/2
Integrated PLL & VCO
Eval




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HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824				
TITLE SCH, CUSTOMER EVALUATION PLL & VCO FO & FO/2				
PROJECT DRAWING #: 129509		SHEET 1 OF 2		
DRAWN BY: D.YOUNG	DATE: 11/08/10	CODE ID NO: 1CN88	SIZE: D	REV: C



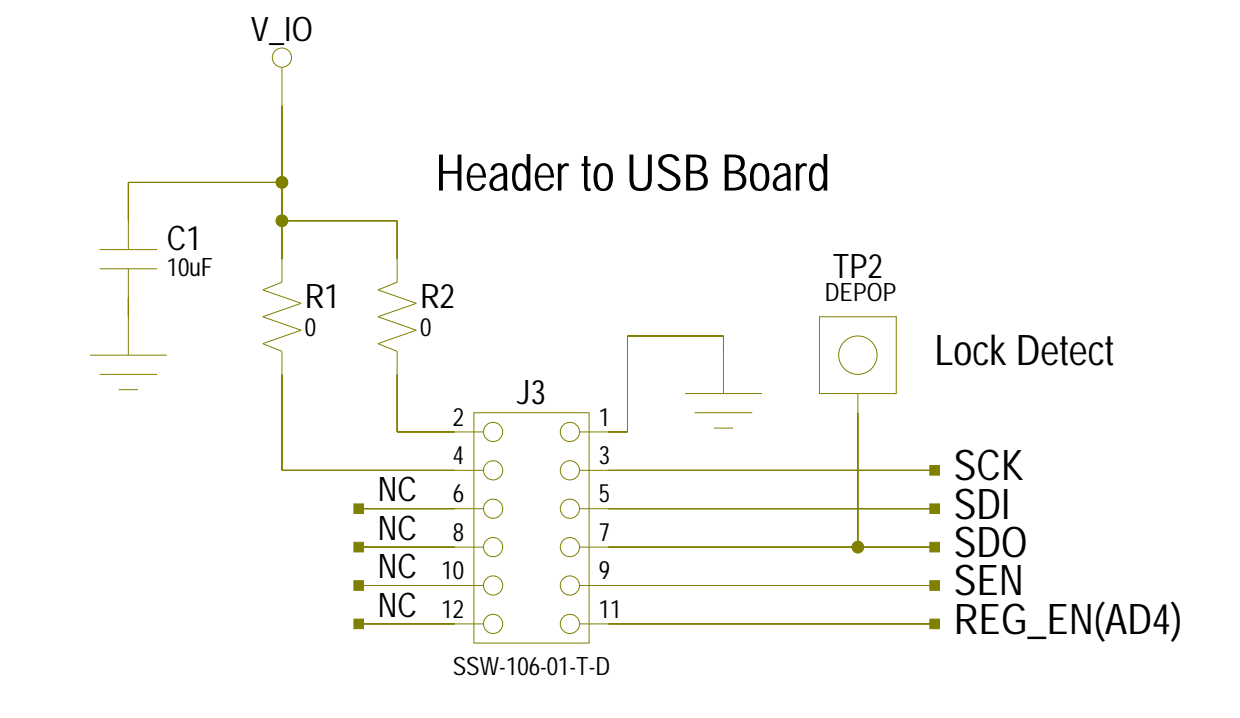
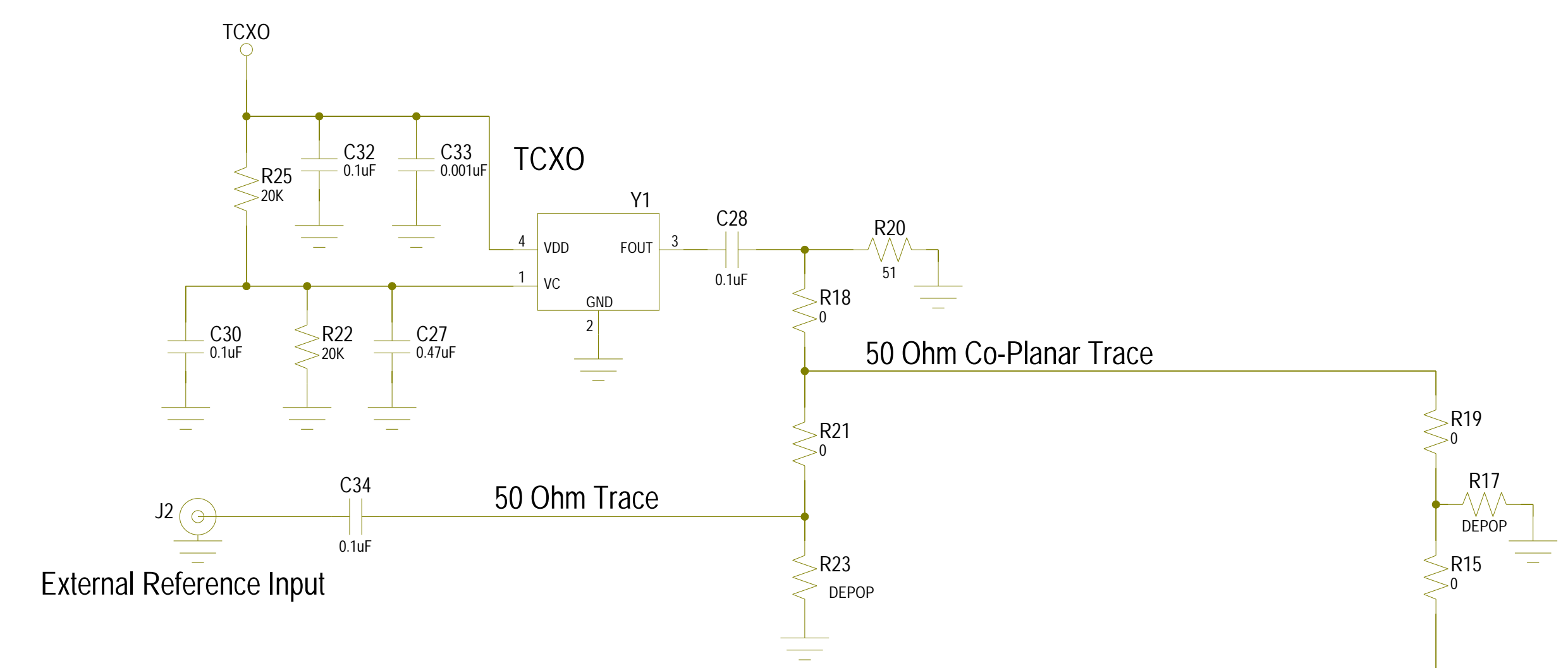
A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
 Small series resistors required in VR1 and VR4 output paths, as shown

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TITLE SCH, CUSTOMER EVALUATION PLL & VCO FO & FO/2	
PROJECT DRAWING #: 129509	SHEET CODE ID NO. 1CN88 SIZE D 2 OF 2 REV C
DRAWN BY D.YOUNG DATE 10/01/2010	1

REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
B	20100127	RELEASED PRODUCTION ECN CP100519	V.VADUVA	12/29/2010
C	CP121275	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012

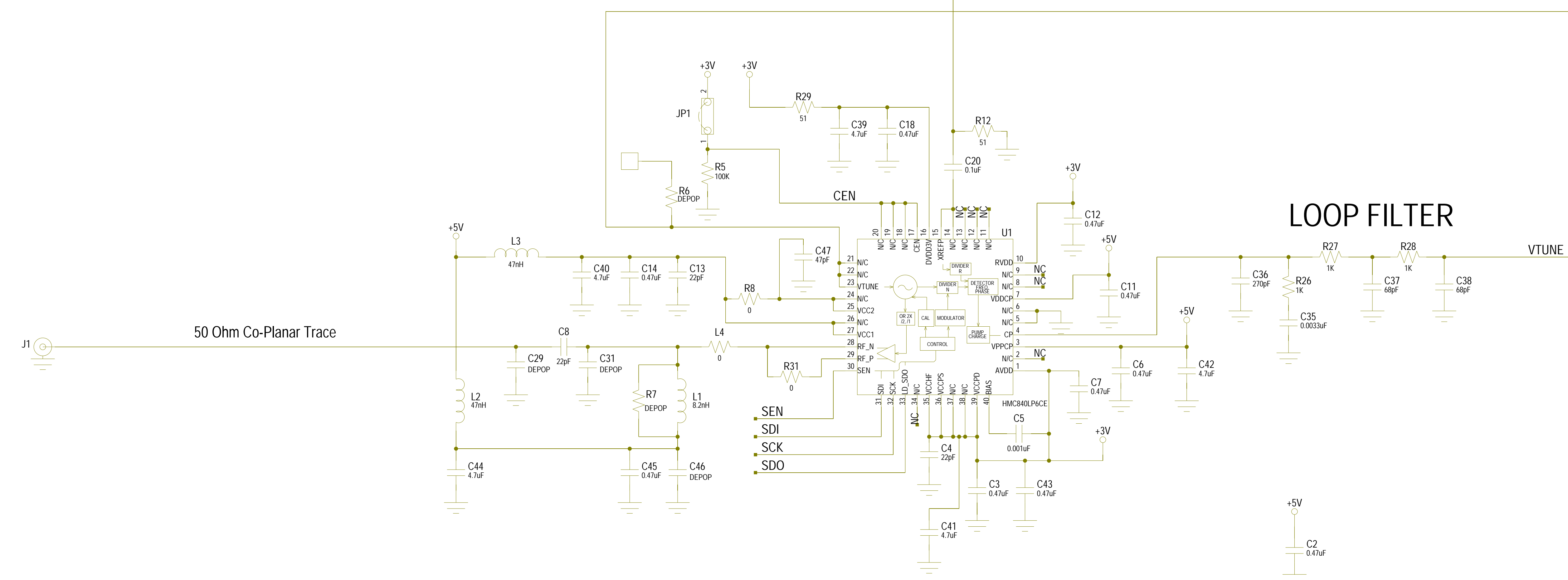
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D



C

C



B

B

HMC840 2xFO Integrated PLL & VCO Eval

A

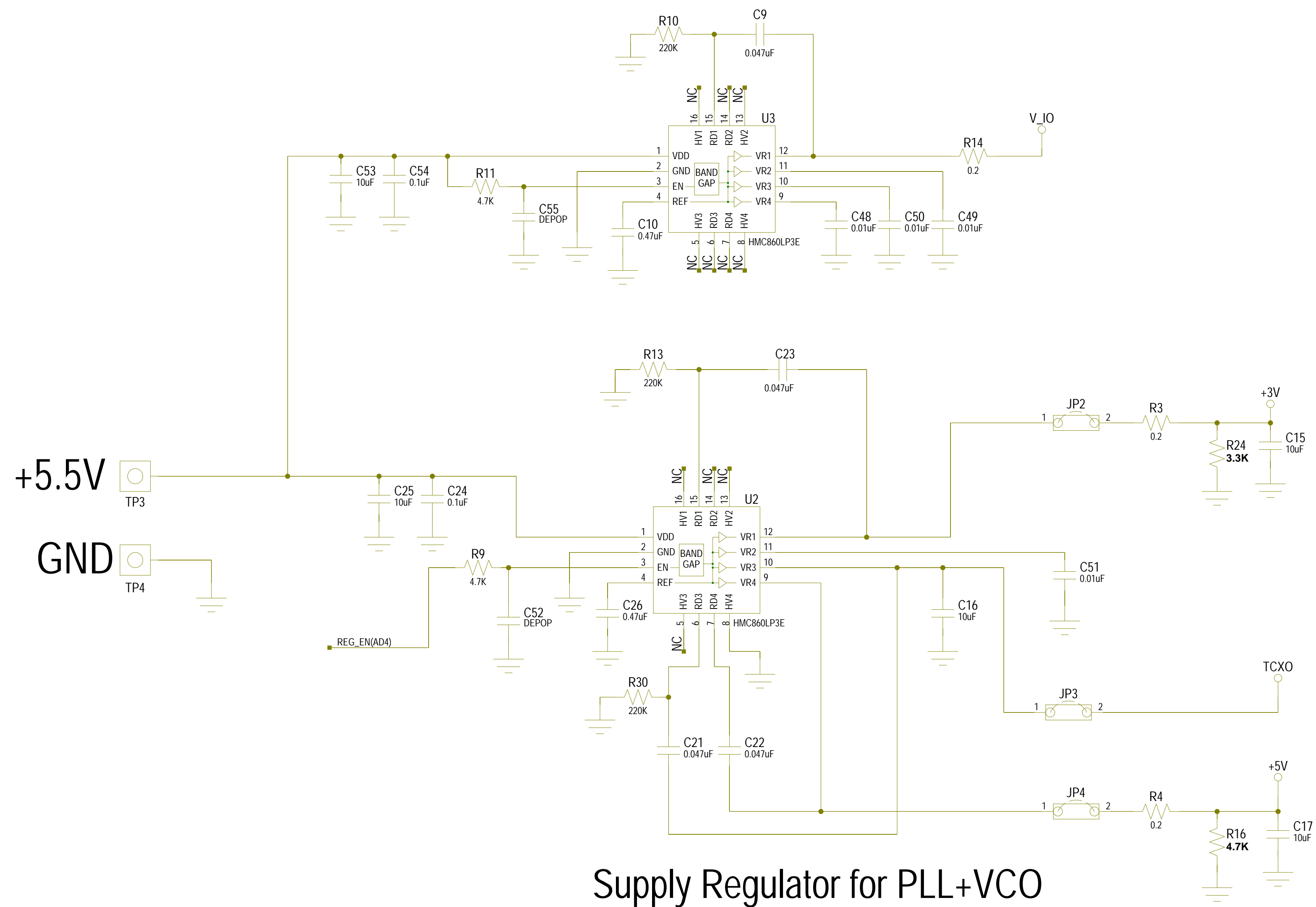
A

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TITLE
SCH, CUSTOMER EVALUATION PLL & VCO 2 X FO

PROJECT		SHEET 1 OF 2	
DRAWING #: 129510	CODE ID NO: 1CN88	SIZE: D	REV: C
DRAWN BY: D.YOUNG	DATE: 10/01/2010		



Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
 Small series resistors required in VR1 and VR4 output paths, as shown

 HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824	
TITLE SCH, CUSTOMER EVALUATION PLL & VCO 2 X FO	
PROJECT DRAWING #: 129510	SHEET CODE ID NO. 1CN88 SIZE D 2 OF 2 REV C
DRAWN BY D.YOUNG DATE 10/01/2010	1