

MAX44290

1.8V, 15MHz, Low-Offset, Low-Power, Rail-to-Rail I/O Op Amp

General Description

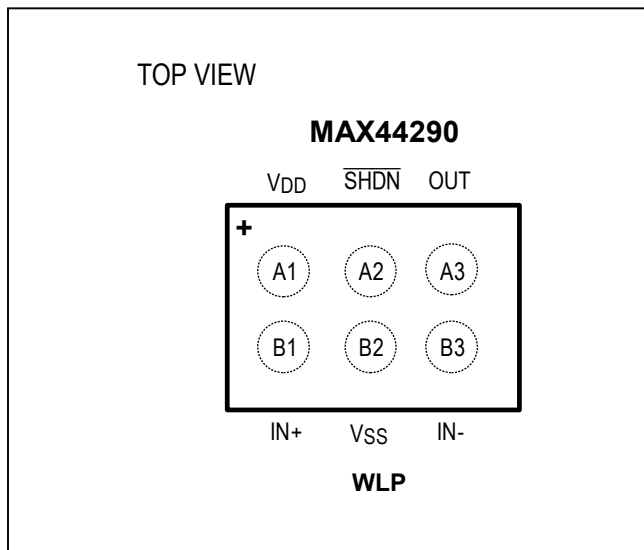
The MAX44290 offers a unique combination of high-speed, precision, and low-voltage operation, making it ideally suited for a large number of signal-processing functions, such as filtering and amplification of signals in portable and industrial equipment.

This amplifier features an input offset of less than 50 μ V and a high-gain bandwidth product of 15MHz while maintaining a low 1.8V supply rail. The device's rail-to-rail input/outputs and low noise guarantee maximum dynamic range in demanding applications, such as 12- to 14-bit SAR ADC drivers. Unlike traditional rail-to-rail input structures, input crossover distortion is absent due to an optimized input stage with an ultra-quiet charge pump.

The device includes a fast-power-on shutdown mode for further power savings. The operational amplifier operates from a supply range of 1.8V to 5.5V over the -40°C to +125°C temperature range, and can operate down to 1.7V over the 0°C to +70°C temperature range. It is available in a tiny, 6-bump wafer-level package (WLP) with 0.4mm pitch.

Ordering Information appears at end of data sheet.

Pin Configuration



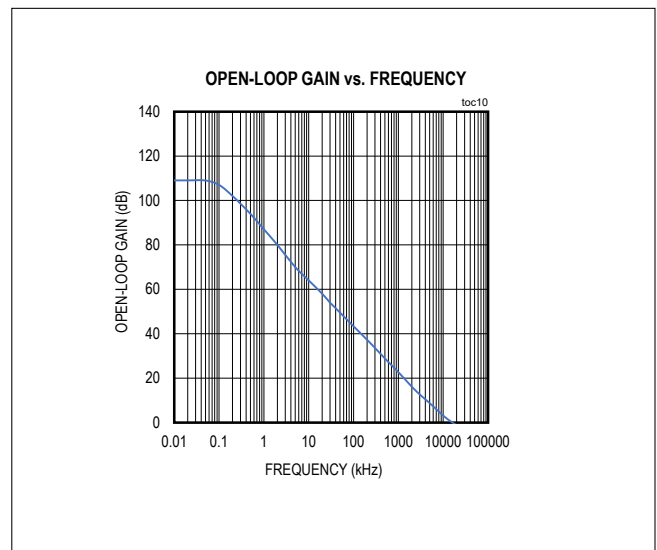
Benefits and Features

- Low 1.8V Supply Rail over the -40°C to +125°C Range
- 1.7V Supply Rail over 0°C to +70°C Range
- 15MHz Unity-Gain Bandwidth
- Low 12.7nV/ $\sqrt{\text{Hz}}$ Input Voltage-Noise Density
- Low 50 μ V (max) Input Offset Voltage at +25°C
- 500fA Low Input Bias Current
- 750 μ A Quiescent Current
- <1 μ A Supply Current in Shutdown
- Low 105dB Total Harmonic Distortion

Applications

- Notebooks
- 3G/4G Handsets
- Portable Medical Instruments
- Battery-Operated Devices
- Analog-to-Digital Converter Buffers
- Transimpedance Amplifiers
- General-Purpose Signal Processing

Typical Operating Characteristic



Absolute Maximum Ratings (Note 1)

Supply Voltage (V_{DD} to V_{SS})	-0.3V to +6V	Continuous Power Dissipation ($T_A = +70^\circ\text{C}$)	
SHDN	-0.3V to +6V	(derate 10.50mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$)	840 mW
All Other Pins	($V_{SS} - 0.3\text{V}$) to ($V_{DD} + 0.3\text{V}$)	Junction-to-Ambient Thermal Resistance (θ_{JA})	95.15 $^\circ\text{C}/\text{W}$
Output Short-Circuit Duration to V_{DD} or V_{SS}	Continuous	Junction-to-Case Thermal Resistance (θ_{JC})	70 $^\circ\text{C}/\text{W}$
Continuous Input Current (any pins)	$\pm 20\text{mA}$	Operating Temperature Range	-40 $^\circ\text{C}$ to +125 $^\circ\text{C}$
Differential Input Voltage	$\pm 6\text{V}$	Junction Temperature	+150 $^\circ\text{C}$
		Storage Temperature Range	-65 $^\circ\text{C}$ to +150 $^\circ\text{C}$
		Lead Temperature (soldering, 10s)	+300 $^\circ\text{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to www.maximintegrated.com/thermal-tutorial.

Electrical Characteristics

($V_{DD} = 3.3\text{V}$, $V_{SS} = 0\text{V}$, $V_{IN+} = V_{IN-} = V_{DD}/2$, $R_{LOAD} = 10\text{k}\Omega$ to $V_{DD}/2$, $V_{SHDN} = V_{DD}$, $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$, unless otherwise noted. Typical values are at $+25^\circ\text{C}$.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
POWER SUPPLY						
Supply Voltage Range	V_{DD}	Guaranteed by PSRR	1.8		5.5	V
		Over $0^\circ\text{C} < T_A < +70^\circ\text{C}$	1.7		5.5	
Power-Supply Rejection Ratio	PSRR	$V_{CM} = V_{DD}/2$	82	95		dB
Quiescent Current	I_{DD}	$R_{LOAD} = \text{no load}$		750	1200	μA
Shutdown Supply Current	I_{SHDN}				1	μA
Shutdown Input Low	V_{IL}				0.5	V
Shutdown Input High	V_{IH}		1.3			V
Output Leakage Current in Shutdown				100		μA
Shutdown Input Bias Current	I_{IL}/I_{IH}				1	μA
Shutdown Turn-On Time	T_{SHDN}	$T_A = +25^\circ\text{C}$ (Note 3)		14.4	18.9	μs
		$-40^\circ\text{C} < T_A < +125^\circ\text{C}$ (Note 3)			26.7	
Turn-On Time	T_{ON}	$T_A = +25^\circ\text{C}$ (Note 3)		9.7	15.2	μs
		$-40^\circ\text{C} < T_A < +125^\circ\text{C}$ (Note 3)			18.4	
DC CHARACTERISTICS						
Input Common-Mode Range	V_{CM}	Guaranteed by CMRR test	$V_{SS} - 0.1$		$V_{DD} + 0.1$	V
Common-Mode Rejection Ratio	CMRR	$V_{CM} = V_{SS} - 0.1\text{V}$ to $V_{DD} + 0.1\text{V}$	75	90		dB
Input Offset Voltage (Note 3)	V_{OS}	$T_A = +25^\circ\text{C}$		10	50	μV
		$-40^\circ\text{C} < T_A < +125^\circ\text{C}$ after calibration			100	
		$-40^\circ\text{C} < T_A < +125^\circ\text{C}$			500	
Input Offset Voltage Drift (Note 3)	TC V_{OS}			0.8	5	$\mu\text{V}/^\circ\text{C}$

Electrical Characteristics (continued)

($V_{DD} = 3.3V$, $V_{SS} = 0V$, $V_{IN+} = V_{IN-} = V_{DD}/2$, $R_{LOAD} = 10k\Omega$ to $V_{DD}/2$, $V_{SHDN} = V_{DD}$, $T_A = -40^{\circ}C$ to $+125^{\circ}C$, unless otherwise noted. Typical values are at $+25^{\circ}C$.) (Note 2)

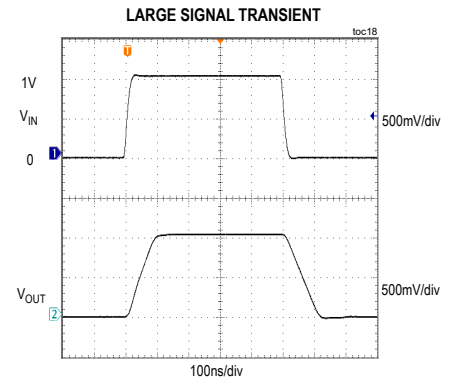
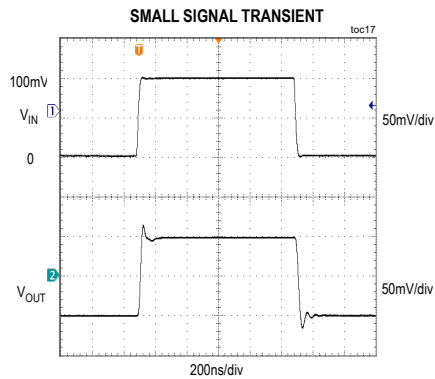
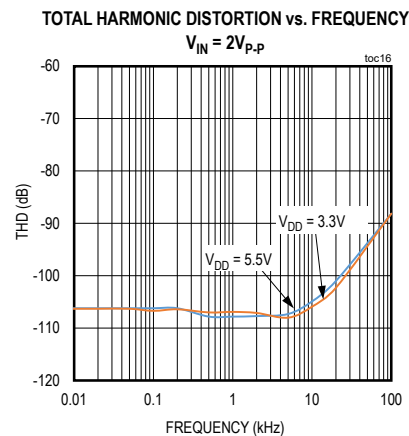
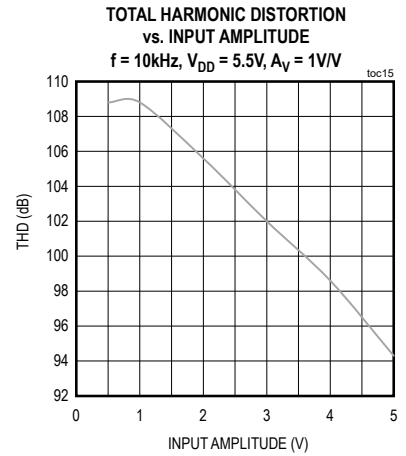
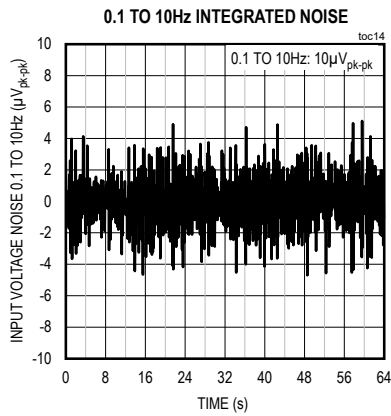
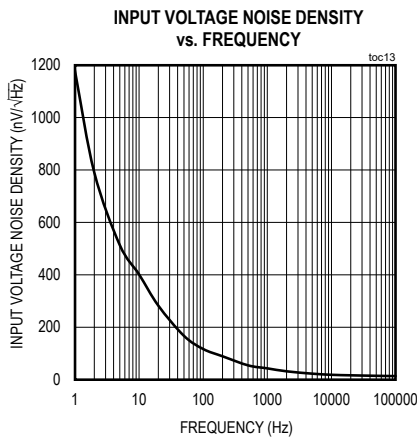
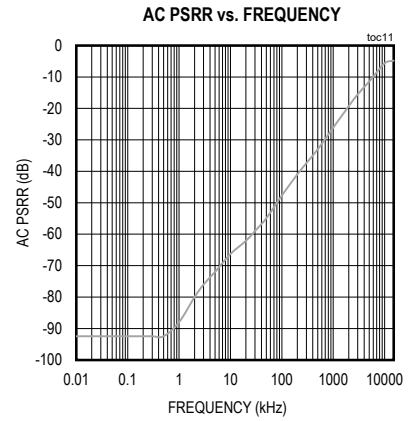
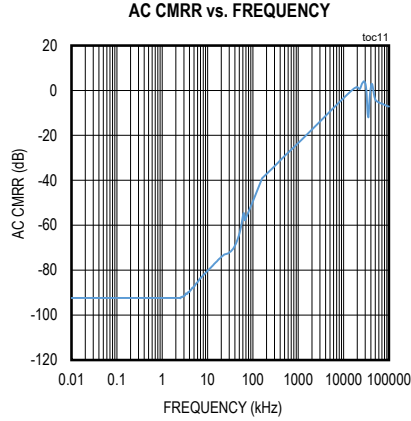
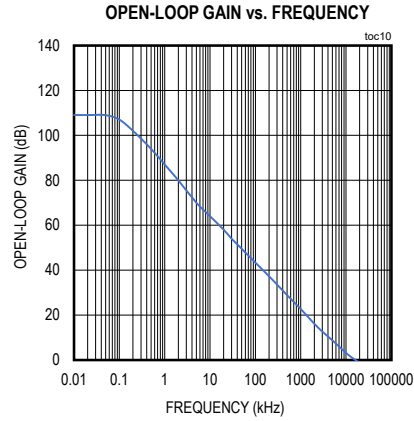
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Input Bias Current (Note 3)	I_B	$T_A = +25^{\circ}C$			0.01	0.5	μA
		$-40^{\circ}C < T_A < +85^{\circ}C$				10	
		$-40^{\circ}C < T_A < +125^{\circ}C$				100	
Open-Loop Gain	A_{VOL}	$400mV \leq V_{OUT} \leq V_{DD} - 400mV$, $R_{LOAD} = 10k\Omega$		100	115		dB
		$400mV \leq V_{OUT} \leq V_{DD} - 400mV$, $R_{LOAD} = 600\Omega$		91	100		
		$400mV \leq V_{OUT} \leq V_{DD} - 400mV$, $R_{LOAD} = 32\Omega$			80		
Output Short-Circuit Current		To V_{DD} or V_{SS}			50		mA
Output Voltage Low	V_{OL}	$V_{OUT} - V_{SS}$	$R_{LOAD} = 10k\Omega$ to $V_{DD}/2$			20	mV
			$R_{LOAD} = 600\Omega$ to $V_{DD}/2$			50	
			$R_{LOAD} = 32\Omega$ to $V_{DD}/2$	400	700		
Output Voltage High	V_{OH}	$V_{DD} - V_{OUT}$	$R_{LOAD} = 10k\Omega$ to $V_{DD}/2$			10	mV
			$R_{LOAD} = 600\Omega$ to $V_{DD}/2$			40	
			$R_{LOAD} = 32\Omega$ to $V_{DD}/2$	400	800		
AC CHARACTERISTICS							
Input Voltage Noise Density	e_n	$f = 10kHz$			12.7		nV/\sqrt{Hz}
Input Voltage Noise		$0.1Hz \leq f \leq 10Hz$			10		μV_{P-P}
Input Capacitance	C_{IN}				0.4		pF
Gain-Bandwidth Product	GBW				15		MHz
Slew Rate	SR	$A_V = 1V/V$, $V_{OUT} = 2V_{P-P}$, 10% to 90%			7		$V/\mu s$
Capacitive Loading	C_{LOAD}	No sustained oscillation, $A_V = 1V/V$			300		pF
Total Harmonic Distortion +Noise	THD+N	$V_{OUT} = 2V_{P-P}$, $A_V = +1V/V$ $R_{LOAD} = 10k\Omega$	$f = 10kHz$		-105		dB
Settling Time		To 0.01%, $V_{OUT} = 2V_{PK-PK}$, $A_V = -1V/V$, $C_{LOAD} = 30pF$			1.7		μs
Output Transient Recovery Time		$\Delta V_{OUT} = 0.2V$, $V_{DD} = 3.3V$, $A_V = 1V/V$, $R_S = 20\Omega$, $C_{LOAD} = 1nF$			1		μs

Note 2: All devices are 100% production tested at $T_A = +25^{\circ}C$. Specifications over temperature are guaranteed by design.

Note 3: Guaranteed by design.

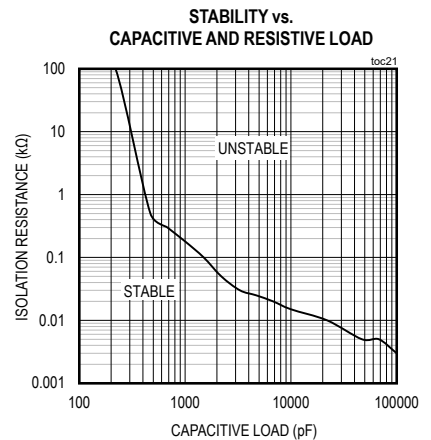
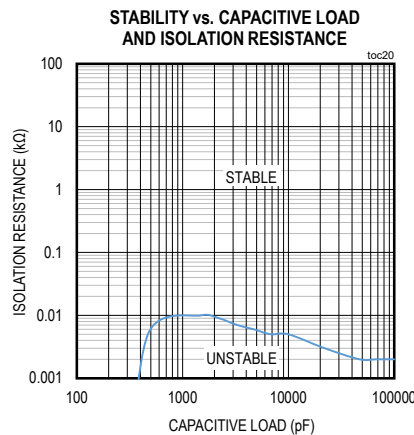
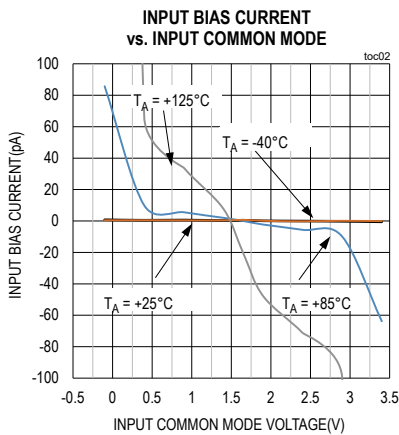
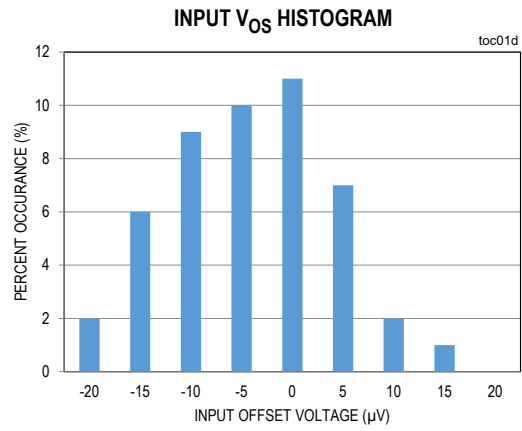
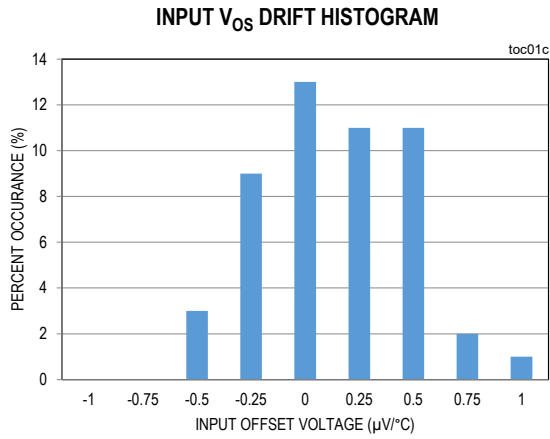
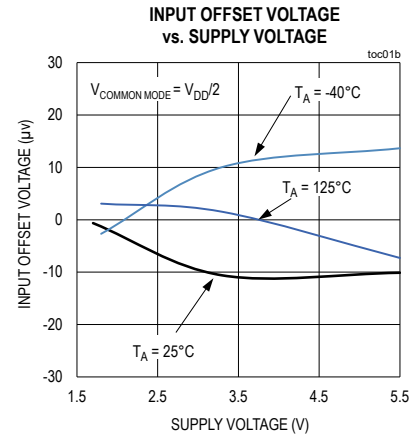
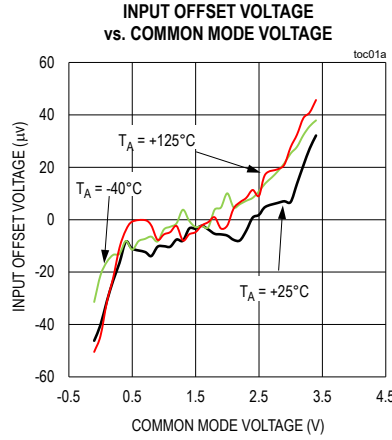
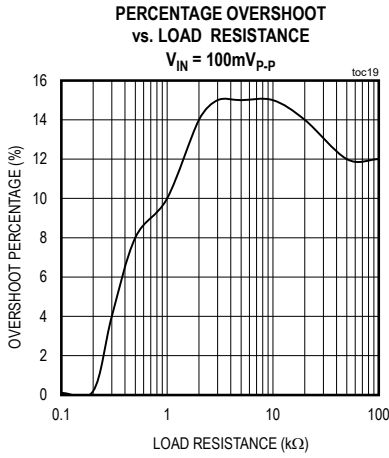
Typical Operating Characteristics

(TA = +25°C, unless otherwise noted.)



Typical Operating Characteristics (continued)

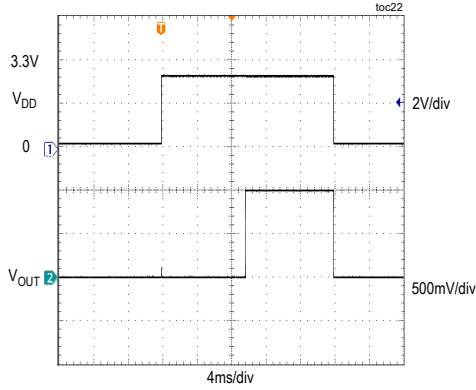
(T_A = +25°C, unless otherwise noted.)



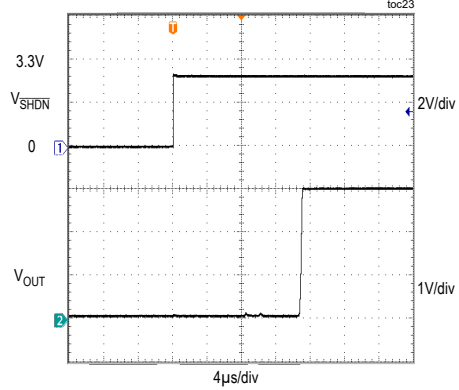
Typical Operating Characteristics (continued)

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

POWER-UP TIME

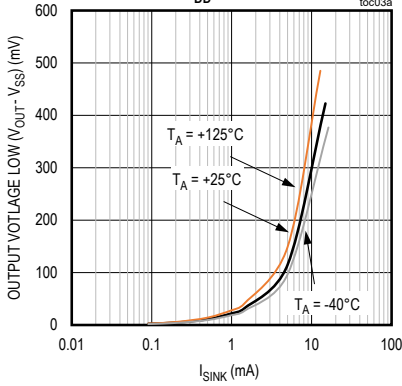


TURN-ON TIME FROM SHUTDOWN



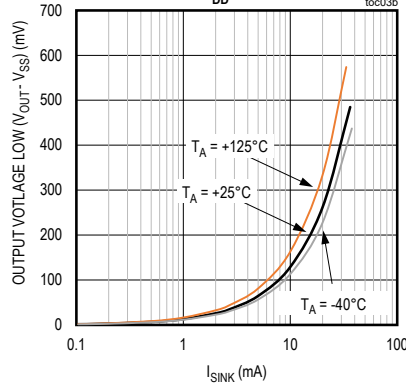
OUTPUT VOLTAGE LOW vs. OUTPUT SINK CURRENT

$V_{DD} = 1.8\text{V}$



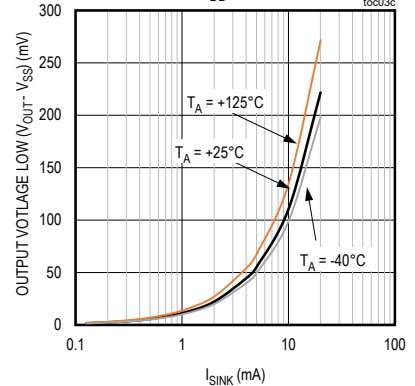
OUTPUT VOLTAGE LOW vs. OUTPUT SINK CURRENT

$V_{DD} = 3.3\text{V}$



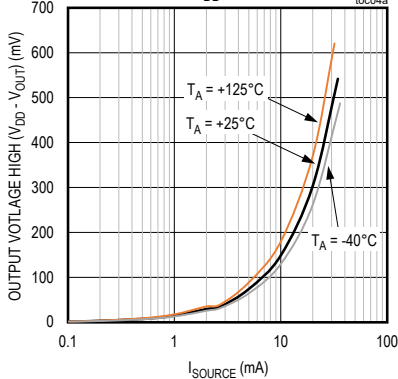
OUTPUT VOLTAGE LOW vs. OUTPUT SINK CURRENT

$V_{DD} = 5\text{V}$



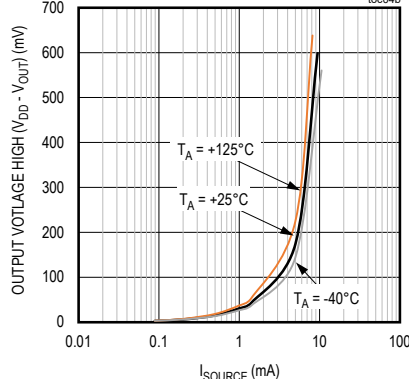
OUTPUT VOLTAGE HIGH vs. OUTPUT SOURCE CURRENT

$V_{DD} = 1.8\text{V}$



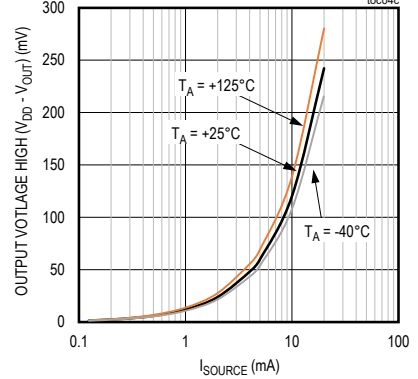
OUTPUT VOLTAGE HIGH vs. OUTPUT SOURCE CURRENT

$V_{DD} = 3.3\text{V}$



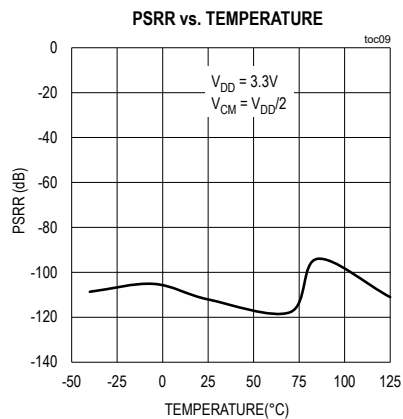
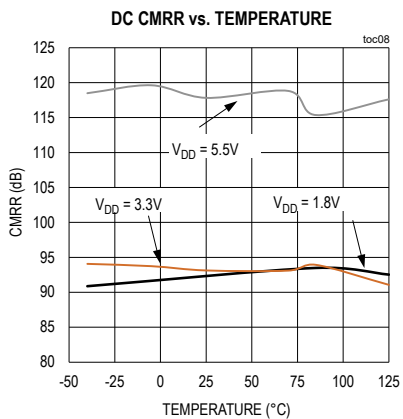
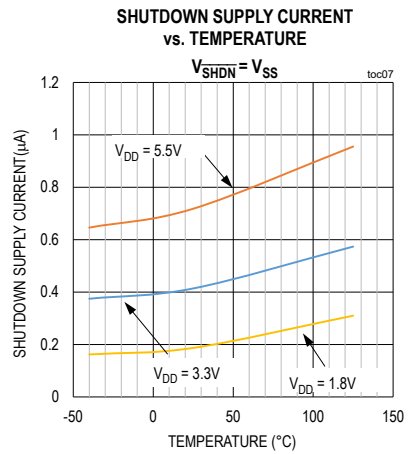
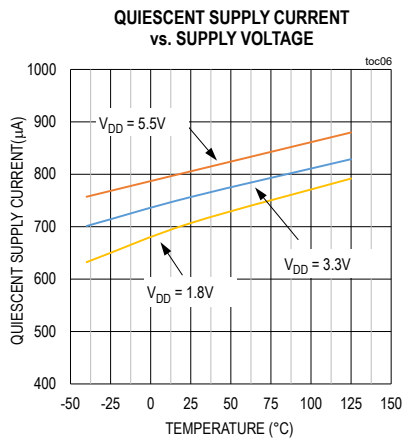
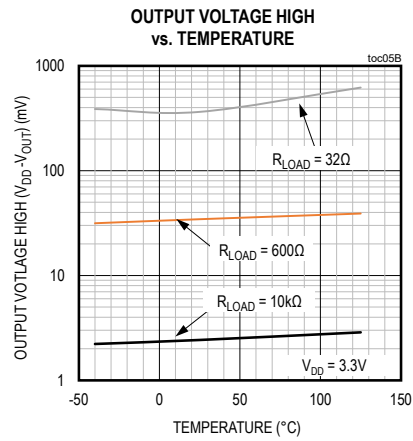
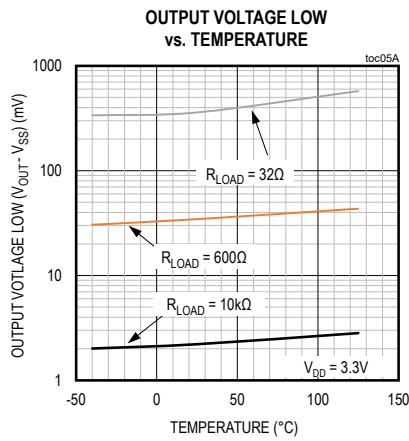
OUTPUT VOLTAGE HIGH vs. OUTPUT SOURCE CURRENT

$V_{DD} = 5\text{V}$

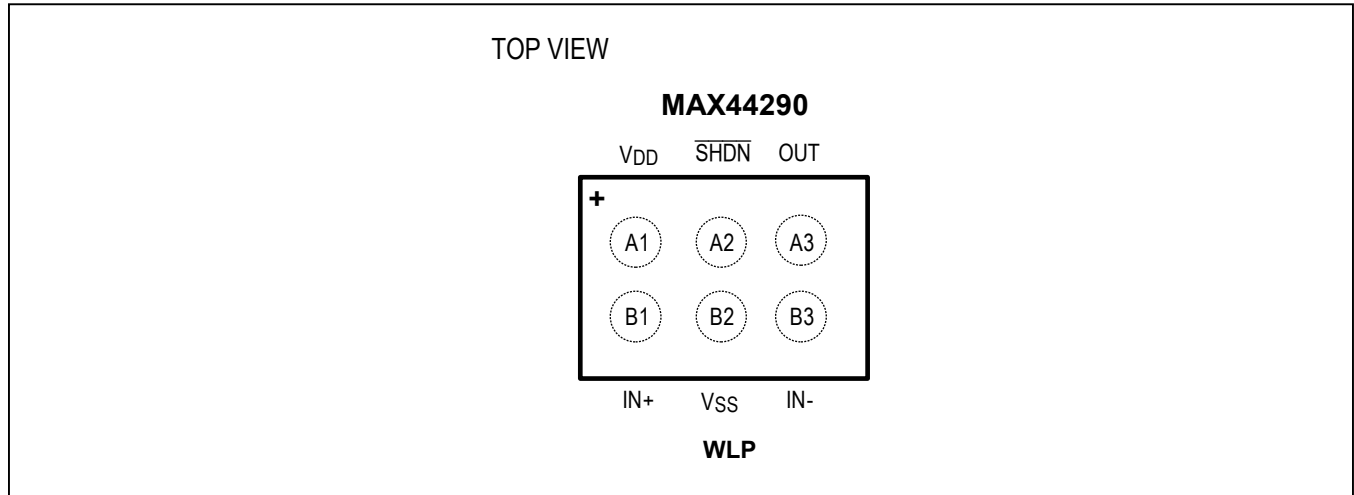


Typical Operating Characteristics (continued)

(TA = +25°C, unless otherwise noted.)



Pin Configuration



Pin Description

PIN	NAME	TYPE	FUNCTION
A1	V _{DD}	Power	Positive Supply Voltage
A2	SHDN	Analog Input	Active-Low Shutdown Input. Connect to V _{DD} for normal operation.
A3	OUTA	Analog Output	Output
B1	IN+	Analog Input	Positive Input
B2	V _{SS}	Power	Negative Supply Voltage
B3	IN-	Analog Input	Negative Input

Detailed Description

The MAX44290 is a high-speed, low-power operational amplifier (op amp) ideal for signal-processing applications due to the device’s high-precision and low-noise CMOS inputs. The device self-calibrates on power-up to eliminate the effects of temperature and power-supply variation.

The device also features a low-power shutdown mode that greatly reduces quiescent current while the device is not operational and recovers in 30µs. The device features autocalibration at a power-up event. The calibration routine takes 10ms.

Crossover Distortion

The device features a low-noise integrated charge pump that creates an internal voltage rail 1V above V_{DD}, which powers the input differential pair of PMOS transistors, as shown in Figure 1. Such a unique architecture eliminates crossover distortion common in traditional CMOS input architecture (Figure 2), especially when used in a noninverting configuration, such as for Sallen-Key filters.

The charge pump operating frequency lies well above the unity-gain frequency of the amplifier. Because of its high-frequency operation and ultra-quiet circuitry, the charge pump generates little noise, does not require external components, and is entirely transparent to the user.

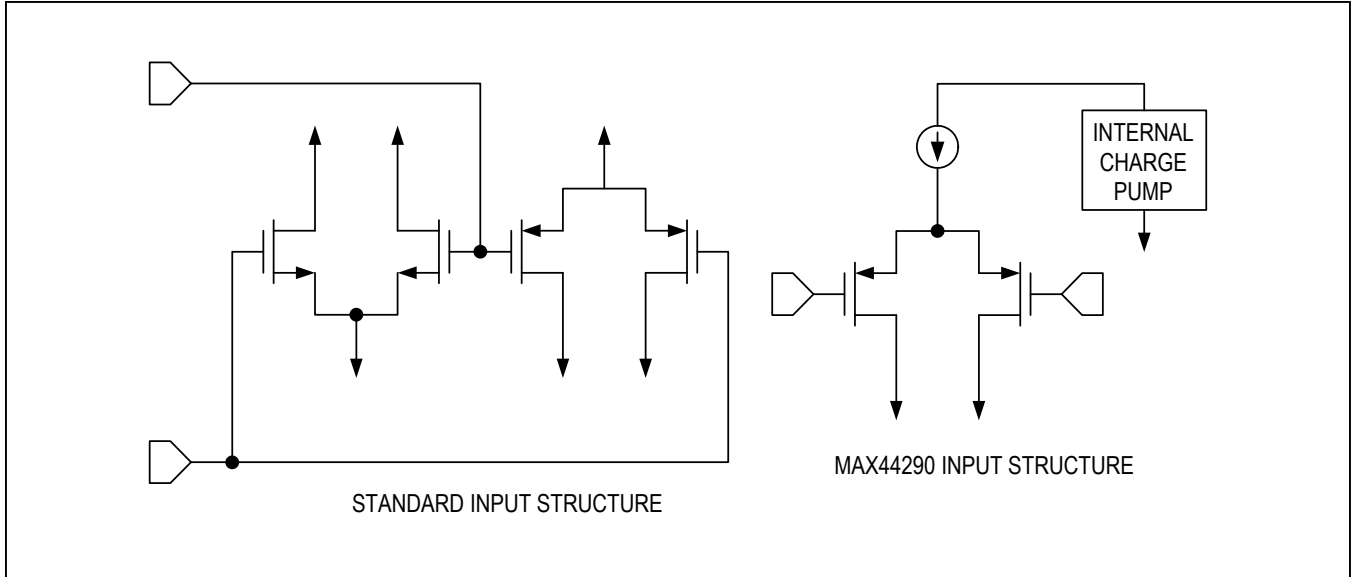


Figure 1. Comparing the Input Structure of the MAX44290 to Standard Op Amps

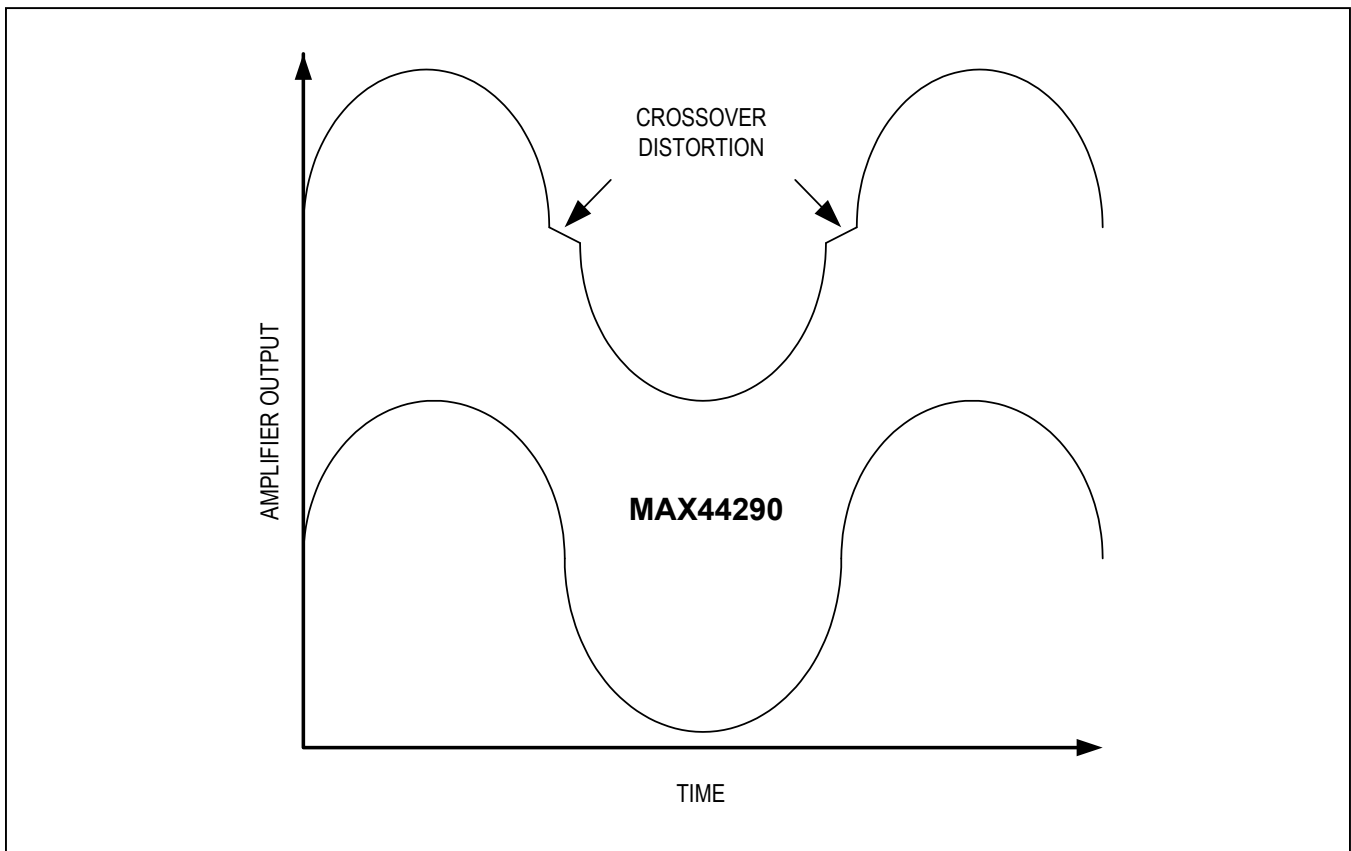


Figure 2. Crossover Distortion of Typical Amplifiers

Applications Information

Power-Up Autotrim

The device features an automatic trim that self-calibrates the input offset voltage (V_{OS}) to less than $50\mu V$ of input offset voltage on power-up. This self-calibration feature allows the device to eliminate input offset voltage effects due to power supply and operating temperature variation simply by cycling its power. The auto-trim sequence takes approximately 10ms to complete and is triggered by an internal power-on-reset (POR) circuitry. During this time, the inputs and outputs are at high impedance and left unconnected.

Shutdown Operation

The MAX44290 features an active-low shutdown mode that puts both inputs and outputs into high impedance and substantially lowers the quiescent current to less than $1\mu A$. Putting the output into high impedance allows multiple outputs to be multiplexed onto a single output

line without the additional external buffers. The device does not self-calibrate when exiting shutdown mode and retains its power-up trim settings. Figure 3 shows how the device recovers from shutdown in under $30\mu s$.

The shutdown logic levels of the device are independent of supply, allowing the shutdown feature of the device to operate off a 1.8V or 3.3V microcontroller, regardless of supply voltage.

Rail-to-Rail Input/Output

The input voltage range of the device extends $100mV$ above V_{DD} and below V_{SS} . The wide input common-mode voltage range allows the op amp to be used as a buffer and as a differential amplifier in a variety of signal processing applications. Output voltage high/low is only $50mV$ above V_{SS} and below V_{DD} , allowing maximum dynamic range in single-supply applications. The high output current and capacitance drive capability of the device make it ideal as an ADC driver and a line driver.

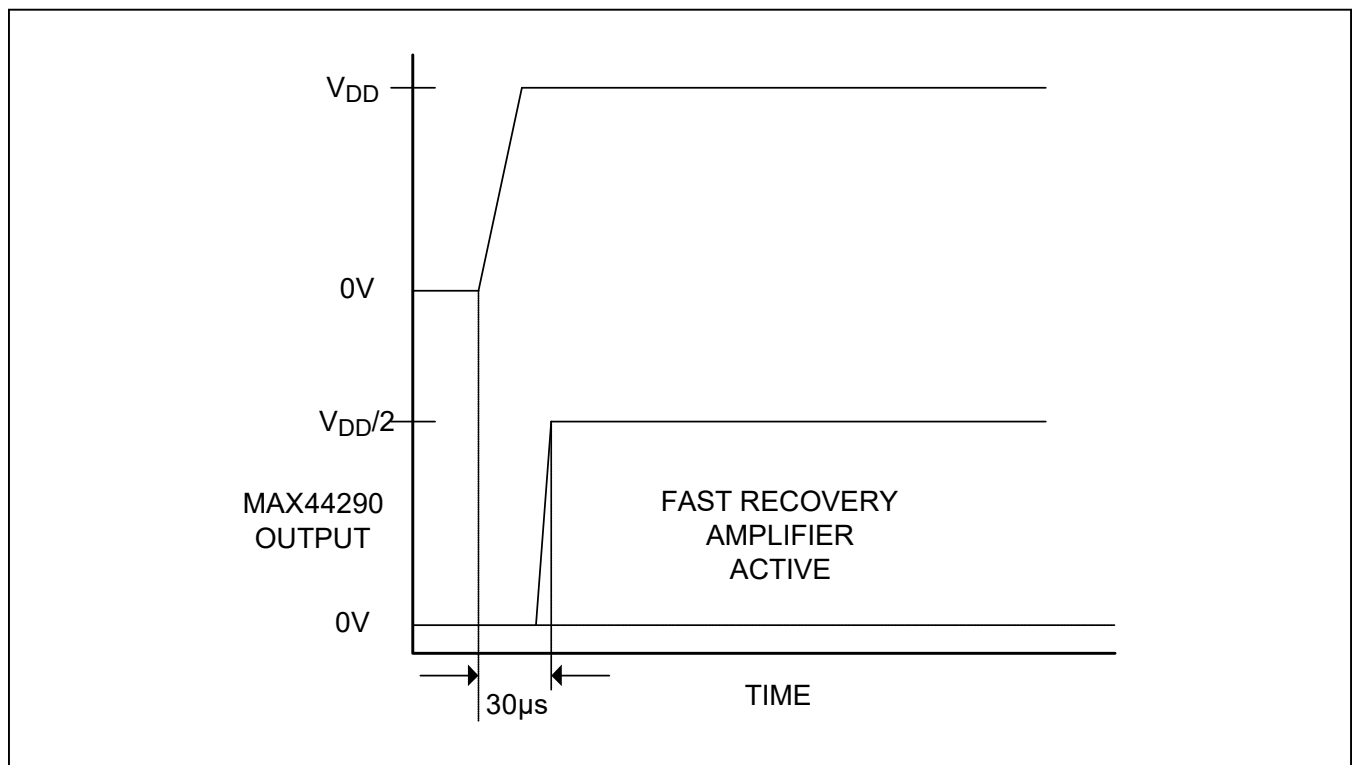


Figure 3. Shutdown Input Operation

Input Bias Current

The MAX44290 features a high-impedance CMOS input stage and a specialized ESD structure that allows low-input bias current operation at low-input, common-mode voltages. Low-input bias current is useful when interfacing with high-impedance sensors. It is also beneficial for designing transimpedance amplifiers for photodiode sensors. This makes the device ideal for ground-referenced medical and industrial sensor applications.

Driver for Interfacing with the MAX11645 ADC

The device's tiny size and low noise makes it a good fit for driving 12- to 16-bit resolution ADCs in space-constrained applications. The [Typical Application Circuits](#) show the MAX44290 amplifier output connected to a lowpass filter driving the MAX11645 ADC. The MAX11645 is part of a family of 3V and 5V, 12-bit and 10-bit, 2-channel ADCs.

The MAX11645 offers sample rates up to 94ksps and measures two single-ended inputs or one differential input. These ADCs dissipate 670µA at the maximum sampling rate, but just 6µA at 1ksps and 0.5µA in shutdown. Offered in ultra-tiny, 1.9mm x 2.2mm WLP and 8-pin µMAX packages, the MAX11645 ADCs are an ideal fit to pair with the MAX44290 in portable applications where higher resolution is required. Refer to the MAX1069 (14-bit) and MAX1169 (16-bit) ADC families.

$$I_{\text{LOAD}} = \frac{V_{\text{IN}}}{(R3 + R4)} \times \left(1 + \left(\frac{2 \times R4}{R5} \right) \right)$$

AND

$$\frac{R2}{R1} = \frac{R4 + R5}{R3}$$

High-Impedance Source/Sensor Preamp Application

High-impedance sources, such as pH sensors and photodiodes, in applications require negligible input leakage currents to the input transimpedance/buffer structure. The MAX44290 benefits with clean and precise signal conditioning due to its input structure.

The device interfaces to both current-output sensors (photodiodes), and high-impedance voltage sources (piezoelectric sensors). For current output sensors, a transimpedance amplifier is the most noise-efficient method for converting the input signal to a voltage. High-value feedback resistors are commonly chosen to create large gains, while feedback capacitors help stabilize the amplifier by cancelling any poles introduced in the feedback loop by the highly capacitive sensor or cabling. A combination of low-current noise and low-voltage noise is important for these applications. Care must be taken to calibrate out photodiode dark current if DC accuracy is important. The high bandwidth and slew rate also allow AC signal processing in certain medical photodiode sensor applications such as pulse-oximetry. For voltage-output sensors, a noninverting amplifier is typically used to buffer and/or apply a small gain to the input voltage signal. Due to the extremely high impedance of the sensor output, a low input bias current with minimal temperature variation is very important for these applications.

Typical Application Circuits

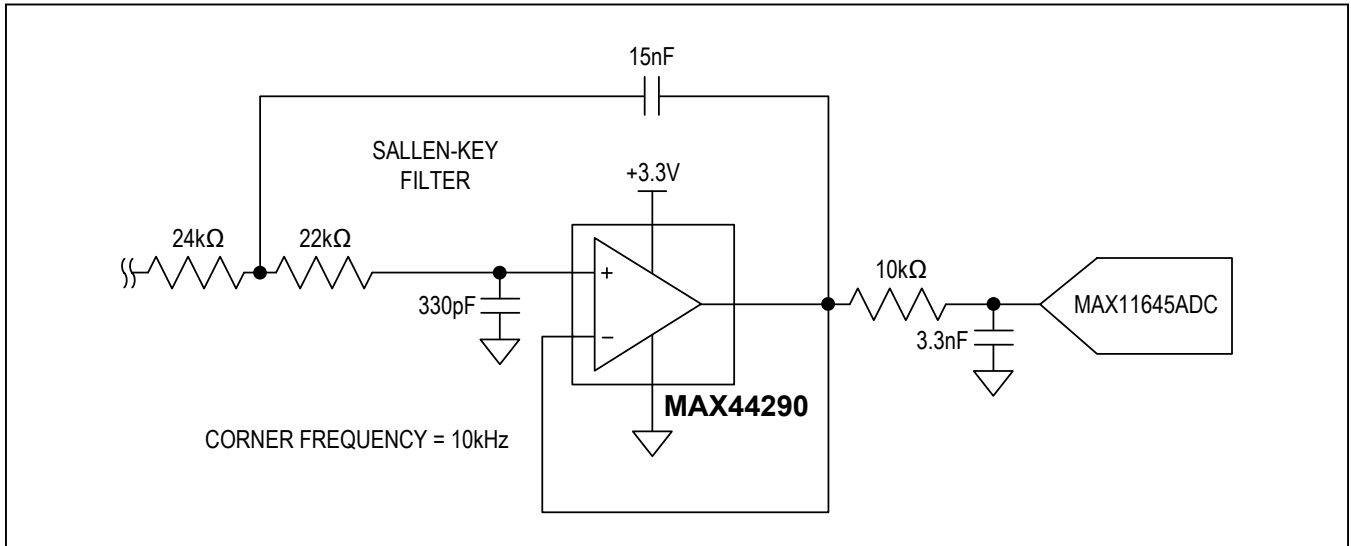


Figure 4. Sallen-Key Filter

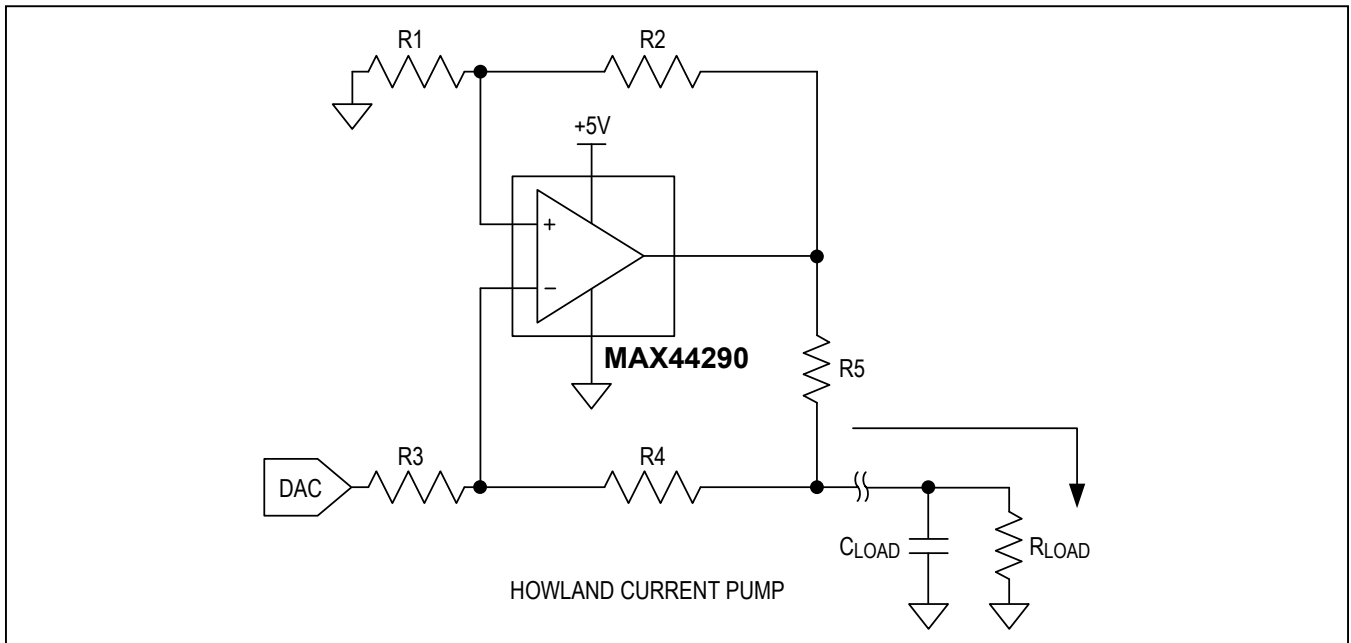


Figure 5. Howland Current Pump

Ordering Information

PART NUMBER	TEMP RANGE	PIN-PACKAGE	PACKAGE COMMENTS
MAX44290ANT+	-40°C to +125°C	6 WLP	0.4mm pitch, 0.5mm (max) height

+Denotes a lead(Pb)-free/RoHS-compliant package.

Chip Information

PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
6 WLP	N60C1+1	21-100055	Refer to Application Note 1891

Pin 1 Indicator

Marking

see Note 7

COMMON DIMENSIONS

A	0.50 MAX
A1	0.19 ±0.03
A2	0.28 REF
A3	0.040 BASIC
b	∅0.27 ±0.03
D	0.838 ±0.025
E	1.238 ±0.025
D1	0.40 BASIC
E1	0.80 BASIC
e	0.40 BASIC
SD	0.20 BASIC
SE	0.00 BASIC

DEPOPULATED BUMPS:
NONE

NOTES:

1. Terminal pitch is defined by terminal center to center value.
2. Outer dimension is defined by center lines between scribe lines.
3. All dimensions in millimeter.
4. Marking shown is for package orientation reference only.
5. Tolerance is ± 0.02 unless specified otherwise.
6. All dimensions apply to PbFree (+) package codes only.
7. Front - side finish can be either Black or Clear.

- DRAWING NOT TO SCALE -

TITLE PACKAGE OUTLINE 6 BUMPS WLP PKG. 0.4 mm PITCH, N60C1+1		
APPROVAL	DOCUMENT CONTROL NO. 21-100055	REV. A 1/1

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	12/15	Initial release	—
1	4/20	Added package outline drawing	13
2	5/20	Updated <i>Electrical Characteristics</i> table	2

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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