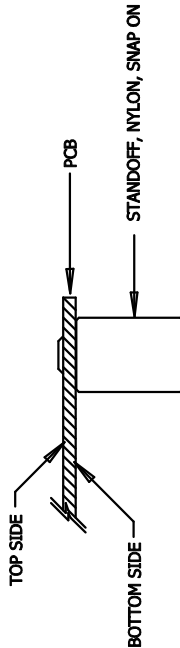
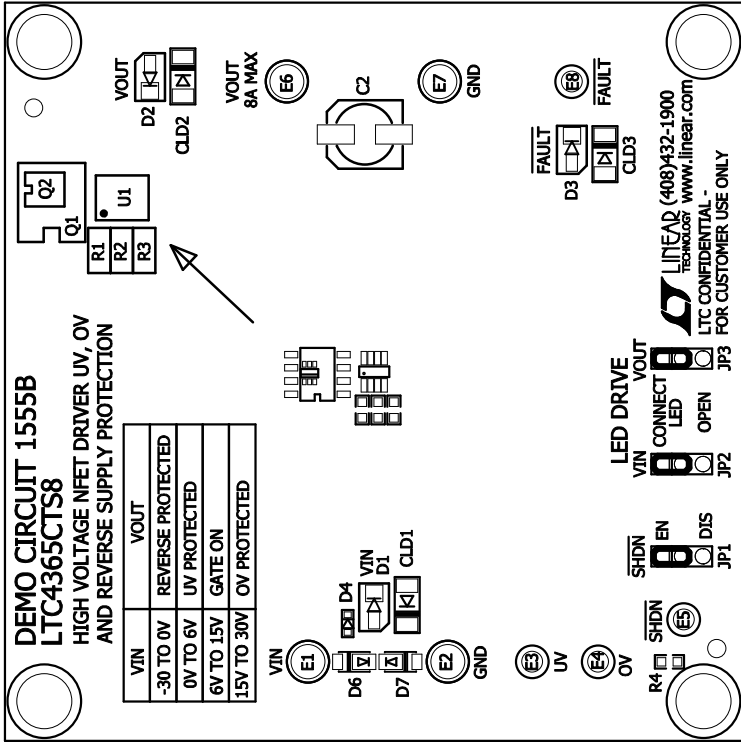
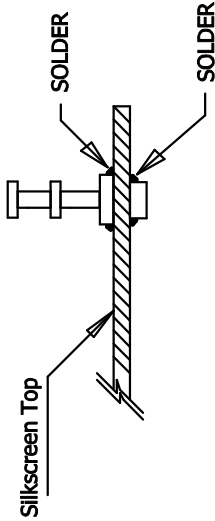


REVISION HISTORY			
ECO	REV	DESCRIPTION	DATE
—	1	PRODUCTION	09/25/12

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS AS SHOWN.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. PARTS TO OMIT WILL BE SPECIFIED ON THE BILLS OF MATERIAL. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
5. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
6. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 deg C.

TURRETS



APPROVALS		LINEAR TECHNOLOGY		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 WWW.LINEAR.COM LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
PCB DES.	R2B	TITLE: TOP, ASSEMBLY DRAWING		HIGH VOLTAGE NFET DRIVER UV, OV AND REVERSE SUPPLY PROTECTION	
APP ENG.	Vlad Ostretov	SIZE		IC NO. LTC4365CTS8	
		N/A		REV. 1	
		SCALE = NONE		FILENAME: 1555B-Rev1.PCB	
		SHT 1 OF 2			