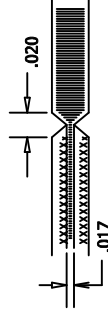
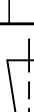



A cross-sectional diagram of a four-layer printed circuit board (PCB). The layers are labeled from top to bottom: L1 - TOP, L2 - GND, L3 - GND, and L4 - BOTTOM. The diagram is oriented with 'TOP SIDE' at the bottom and 'BOTTOM SIDE' at the top. Arrows point from the labels to the corresponding layers.

1. FAB PER IPC-A-600
2. MATERIAL - EPOXY FIBERGLASS, NEMA GRADE FR-4
 - FINISHED THICKNESS TO BE 0.062" \pm /- .005"
 - TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE \pm /-0.003"
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPT BOTH SIDES, COLOR GREEN.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
187	4	+	YES	+/-3mil
10	22	×	YES	+/-3mil
70	2	□	NO	+/-3mil
63	4	◇	YES	+/-3mil
31	9	⊠	YES	+/-3mil
94	4	⊠	YES	+/-3mil

UNLESS OTHERWISE SPECIFIED			
DIMENSIONS ARE IN INCHES			
TOLERANCES:			
0.00" = + 0.01"			
0.000" = + 0.005"			
INTERPRET DIM AND TOL PER ASME Y14.5M-1994			
THIRD ANGLE PROJECTION			
APPROVALS			
PCB DES.	R'B	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 WWW.LINEAR.COM LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
APP ENG.	DILLIAN R.	TITLE: FABRICATION DRAWING	
		HIGH VOLTAGE NFET DRIVER UV, OV AND REVERSE SUPPLY PROTECTION	
		SIZE	REV
		N/A	3
		IC NO. LTC4365CTS8	
		DEMO CIRCUIT 1555A	
SCALE = NONE		FILENAME:	SHT 1 OF 1
		DC1555A-Rev3.PCB	