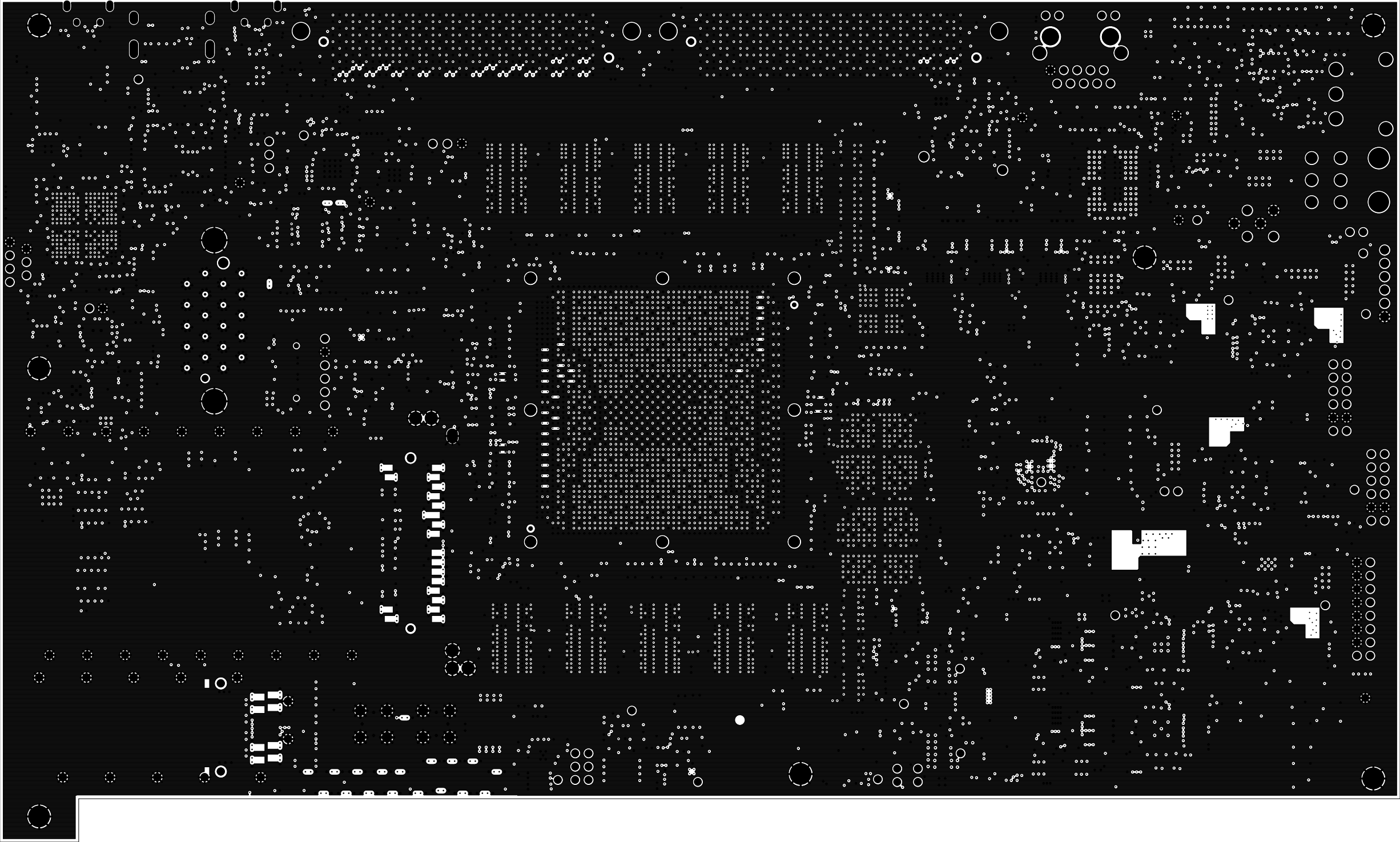
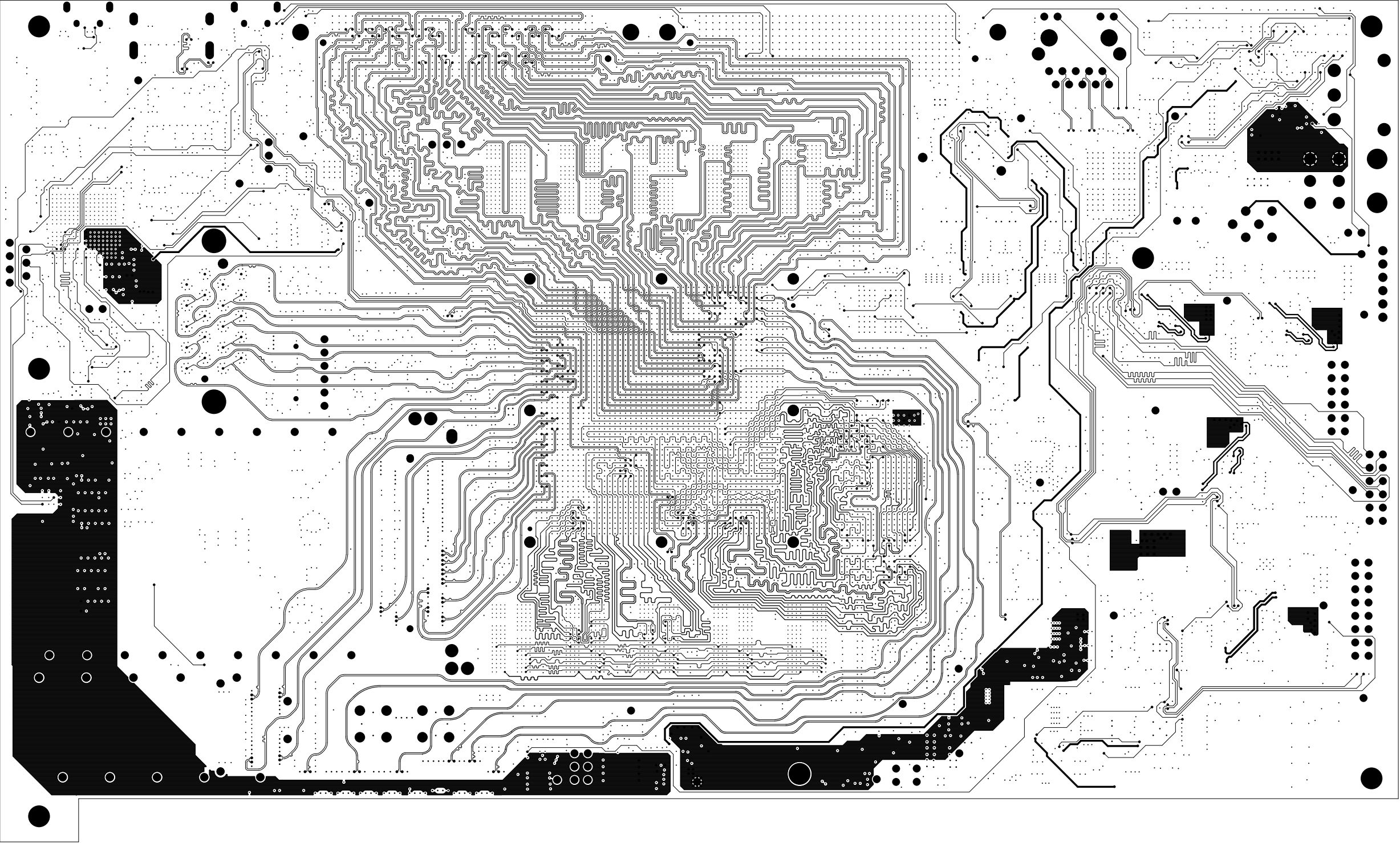


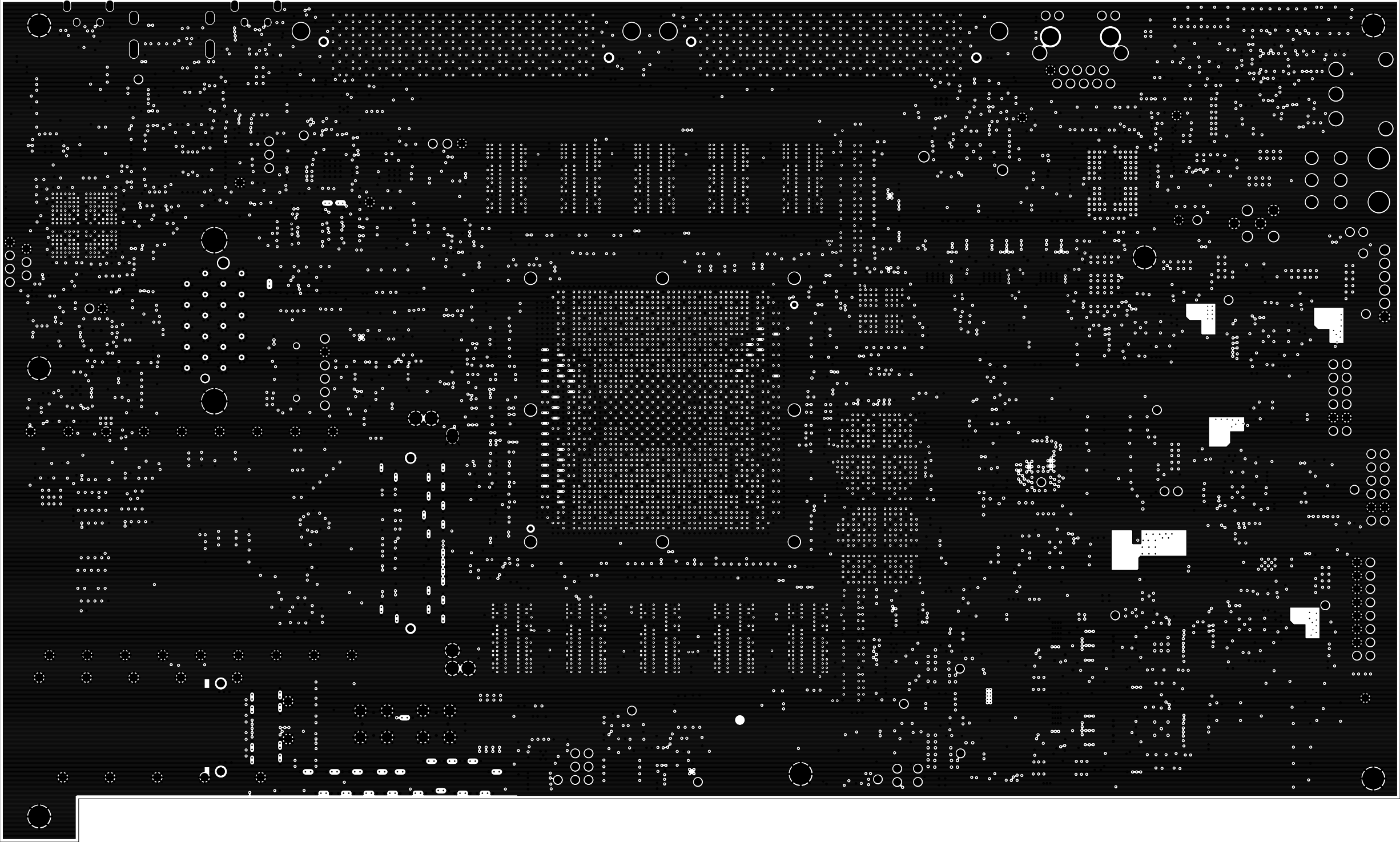
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 1 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: TOP SIDE	



ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 2 OF 28 LAYER: 02_GND1	XILINX DOC USE ONLY REVISION: 01

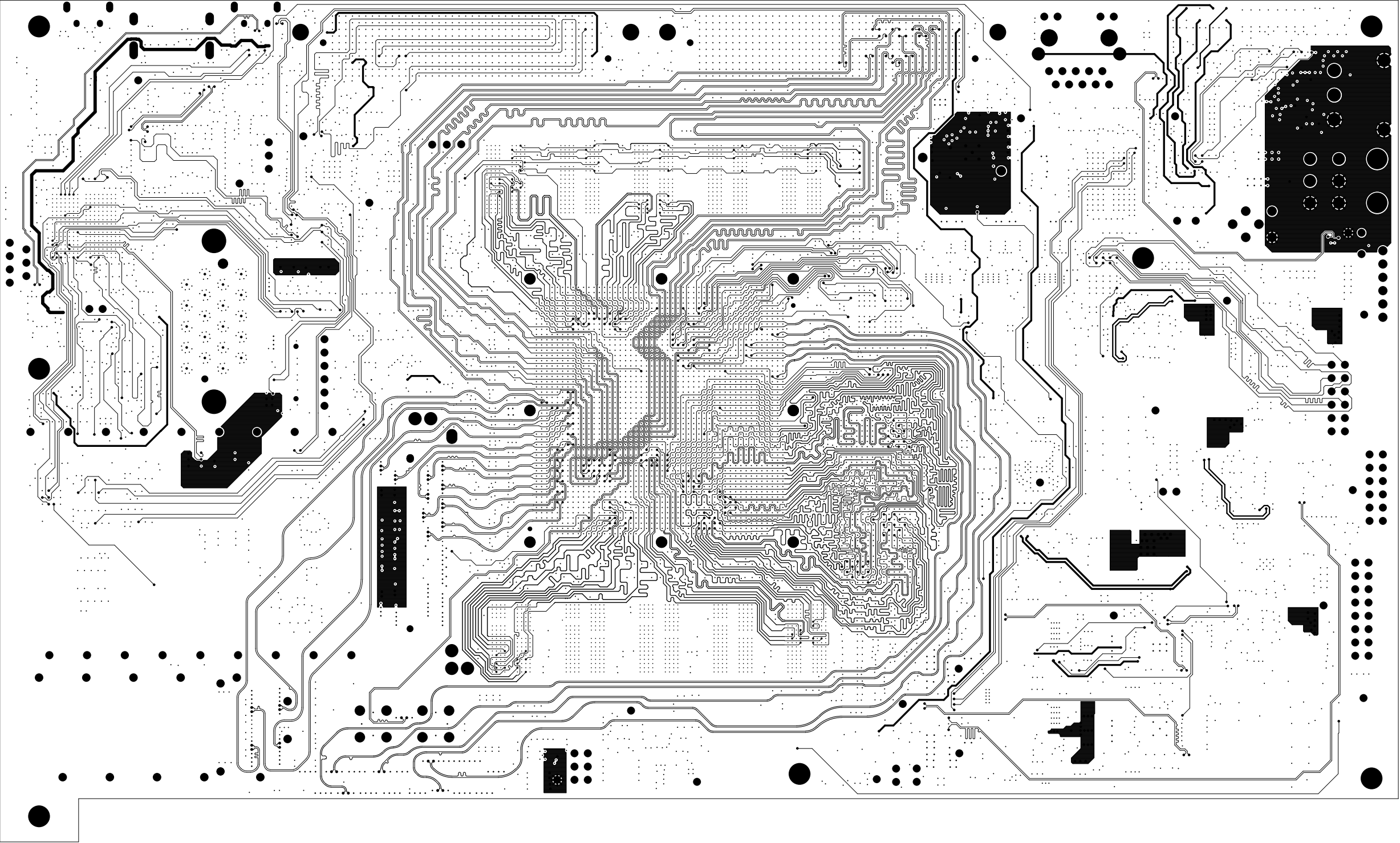


ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 3 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 03_SIG1	

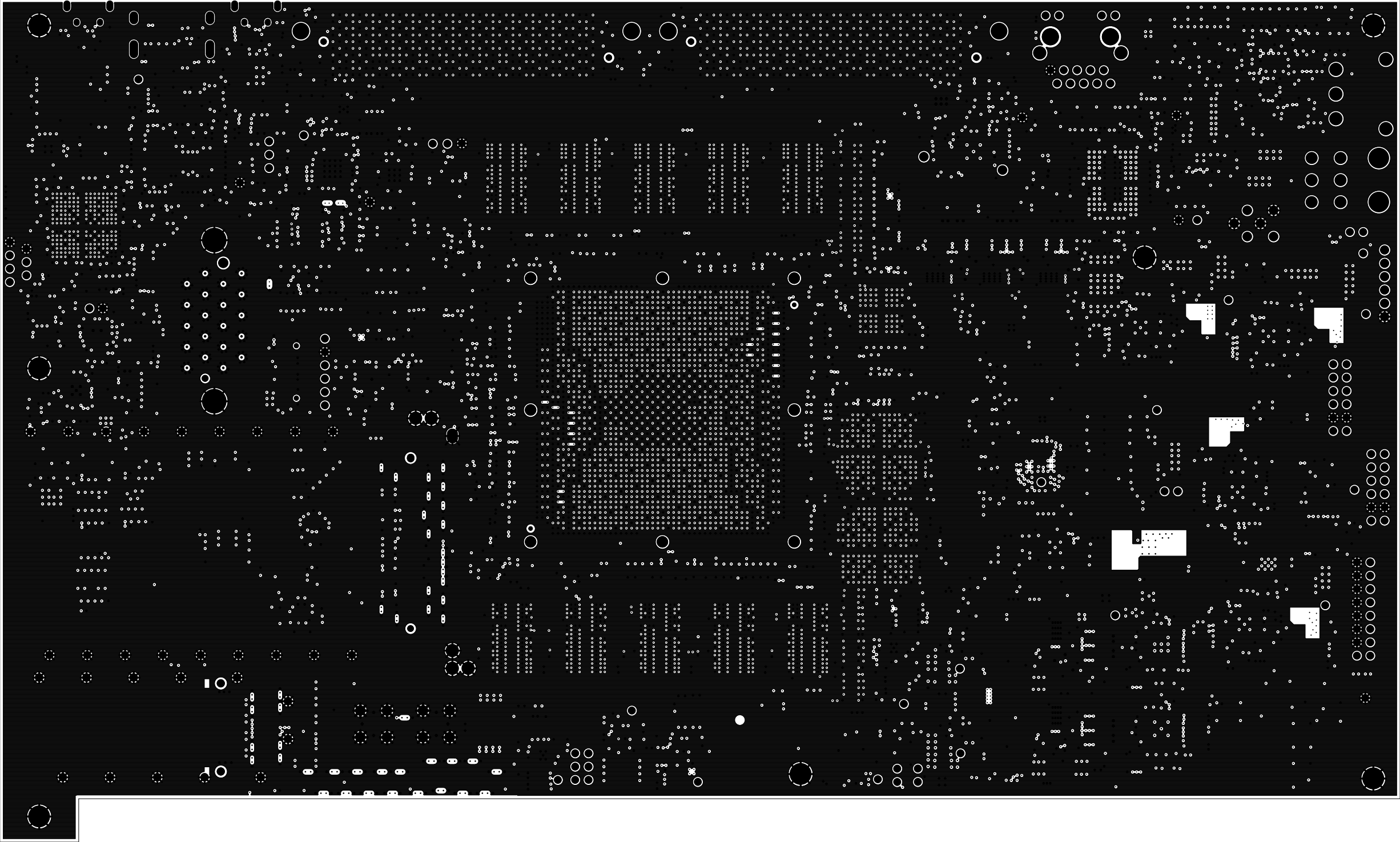


ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 4 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 04_GND2	

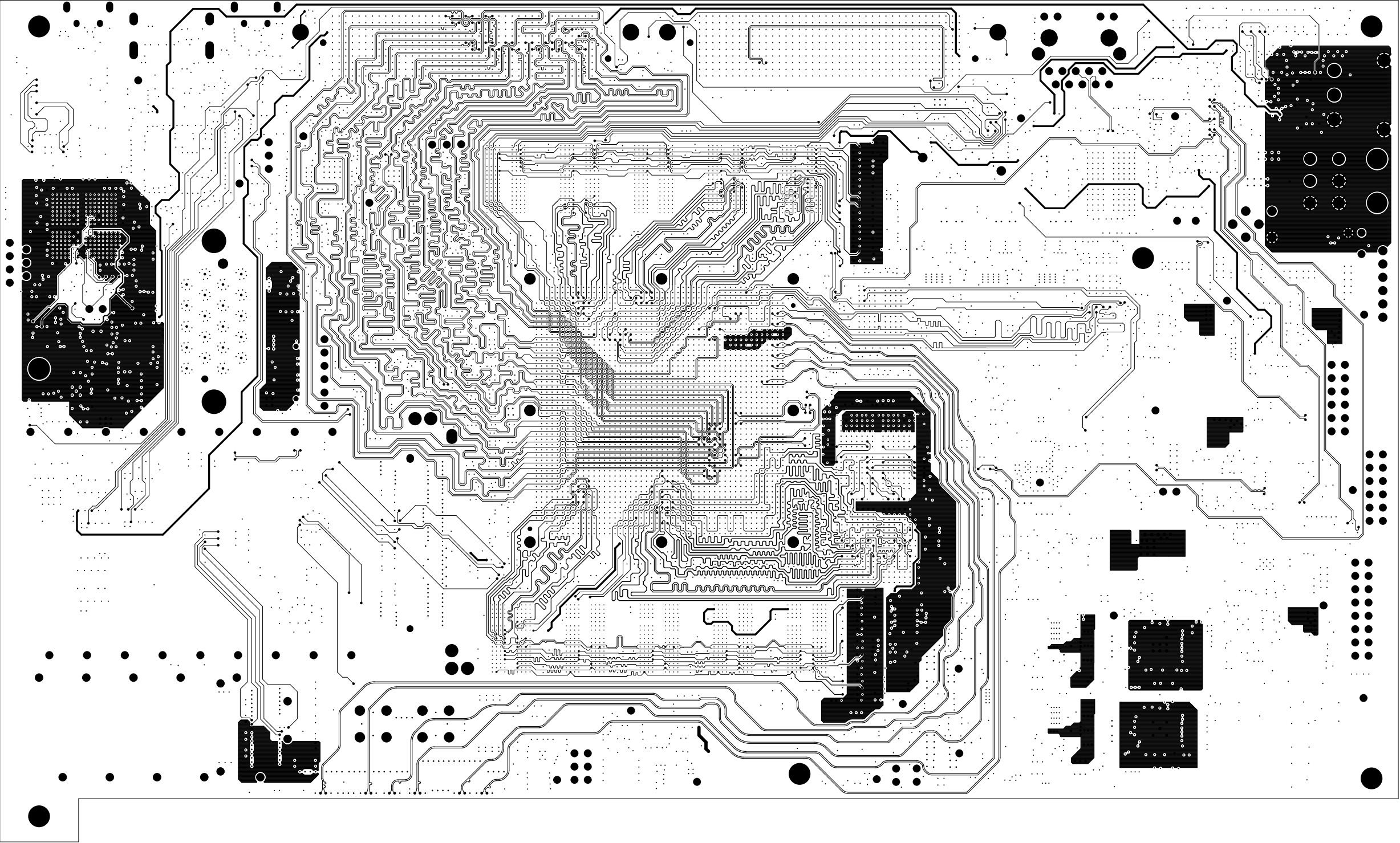




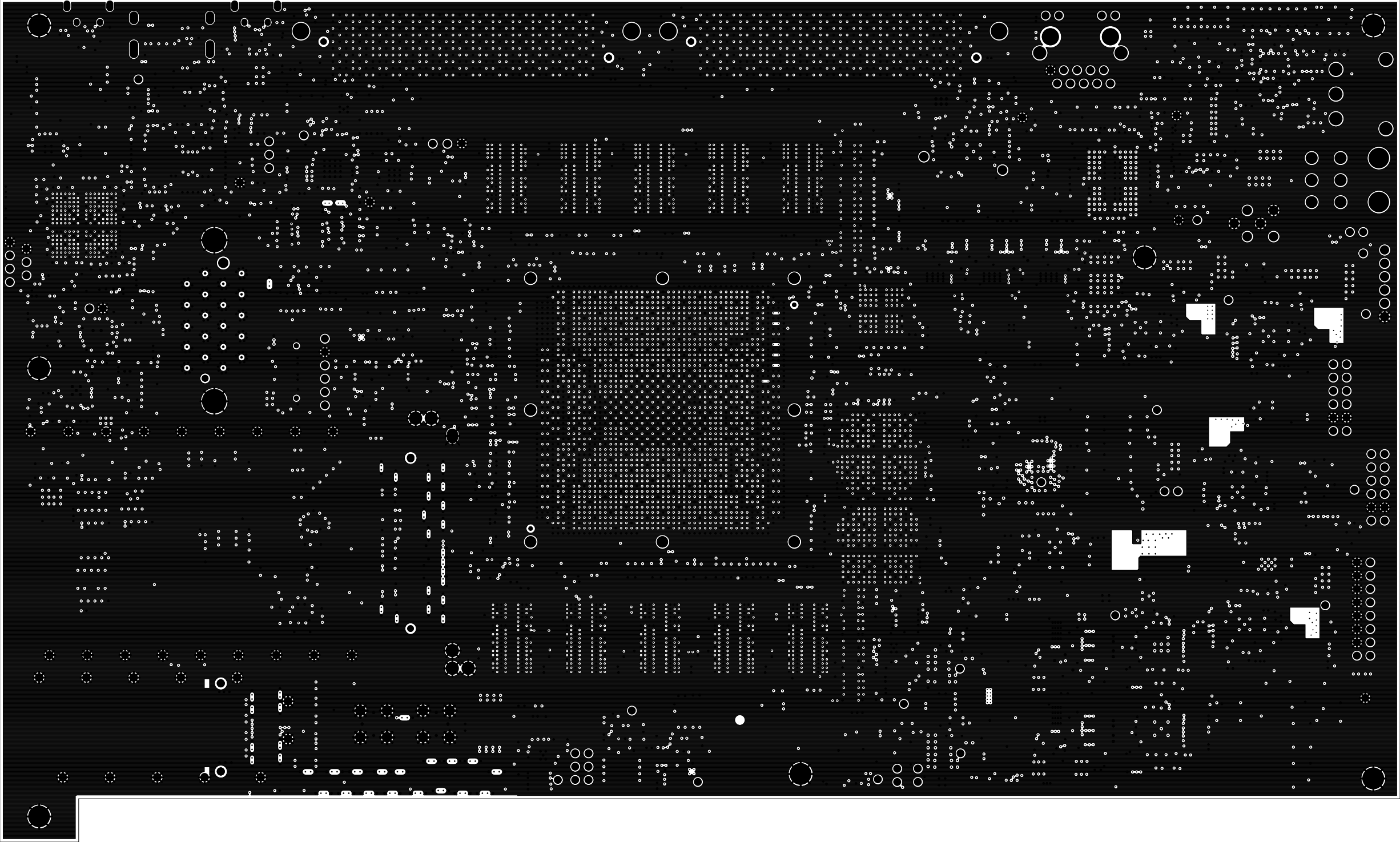
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 5 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 05_SIG2	



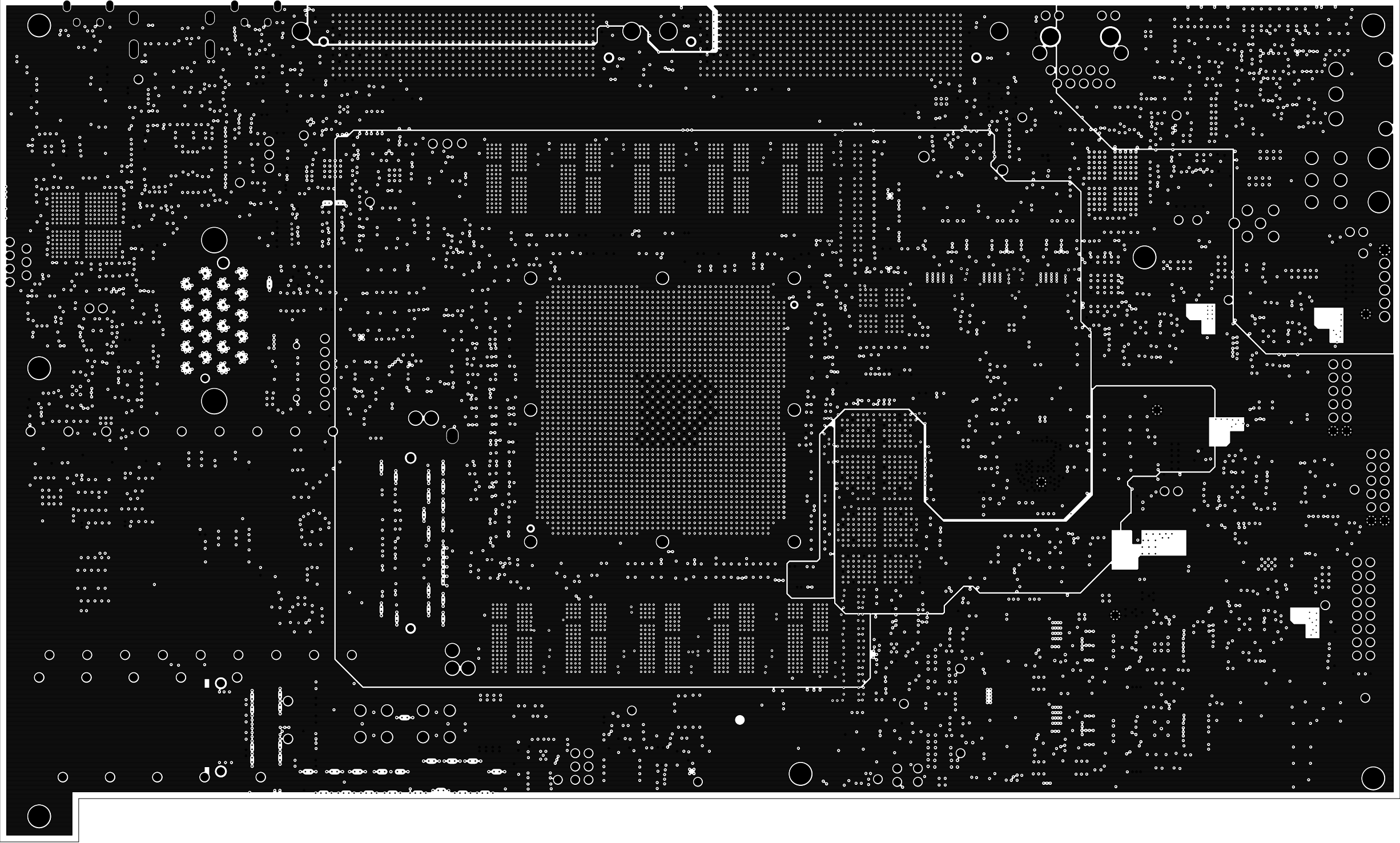
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 6 OF 28 LAYER: 06_GND3	XILINX DOC USE ONLY REVISION: 01



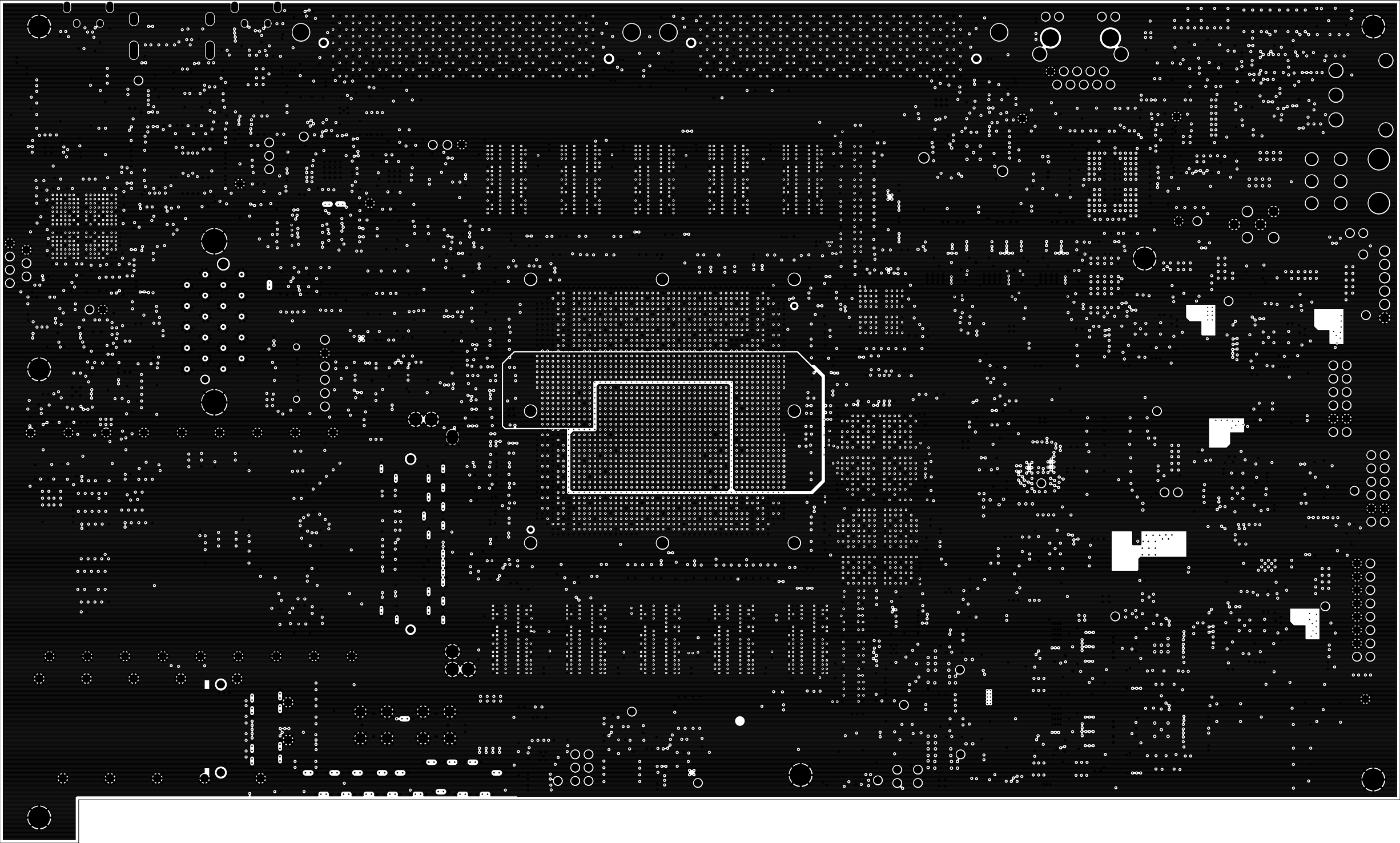
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB #        1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET    7    OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER:     07_SIG3	



ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 8 OF 28 LAYER: 08_GND4	XILINX DOC USE ONLY REVISION: 01

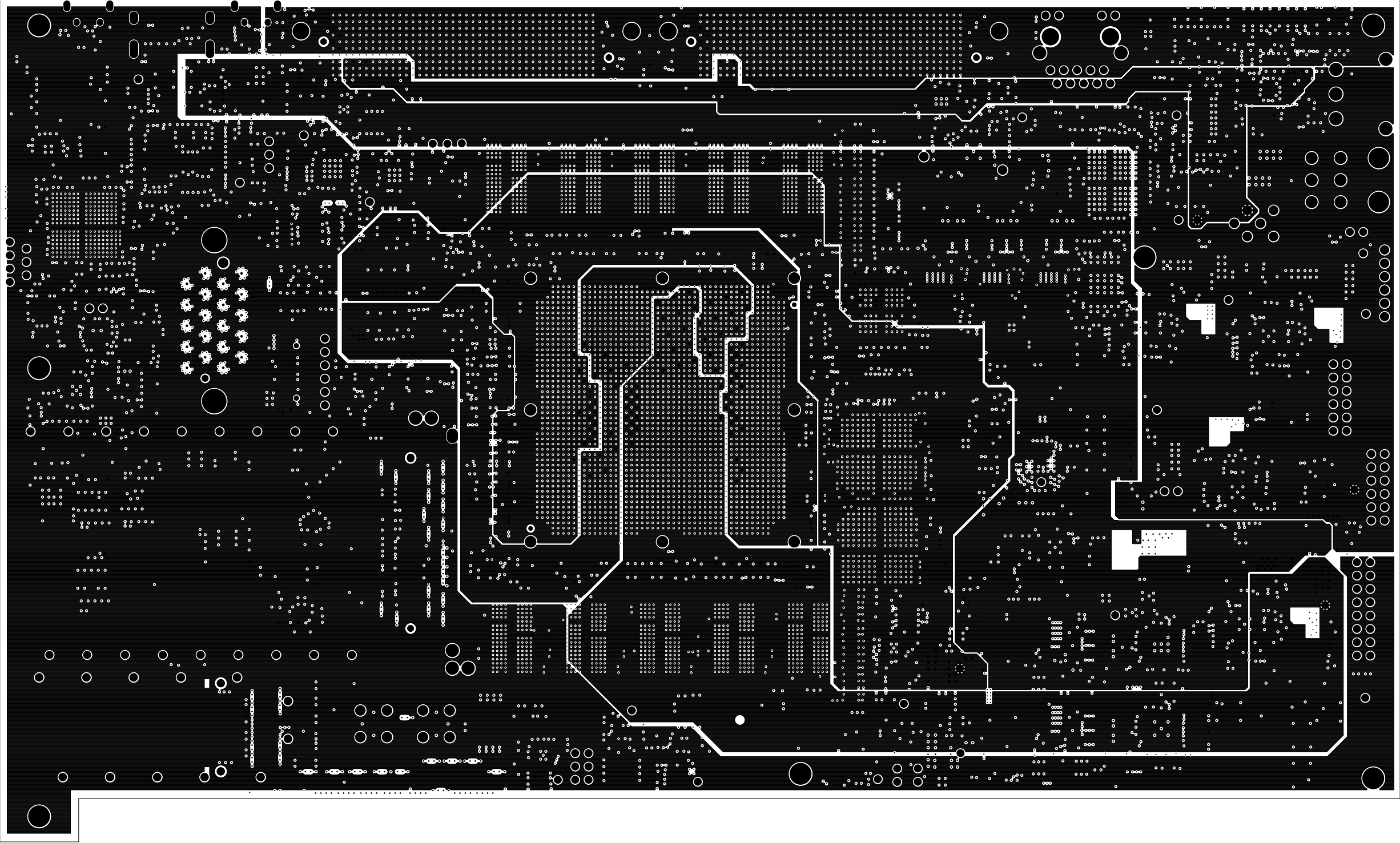


ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 9 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 09_PWR1	

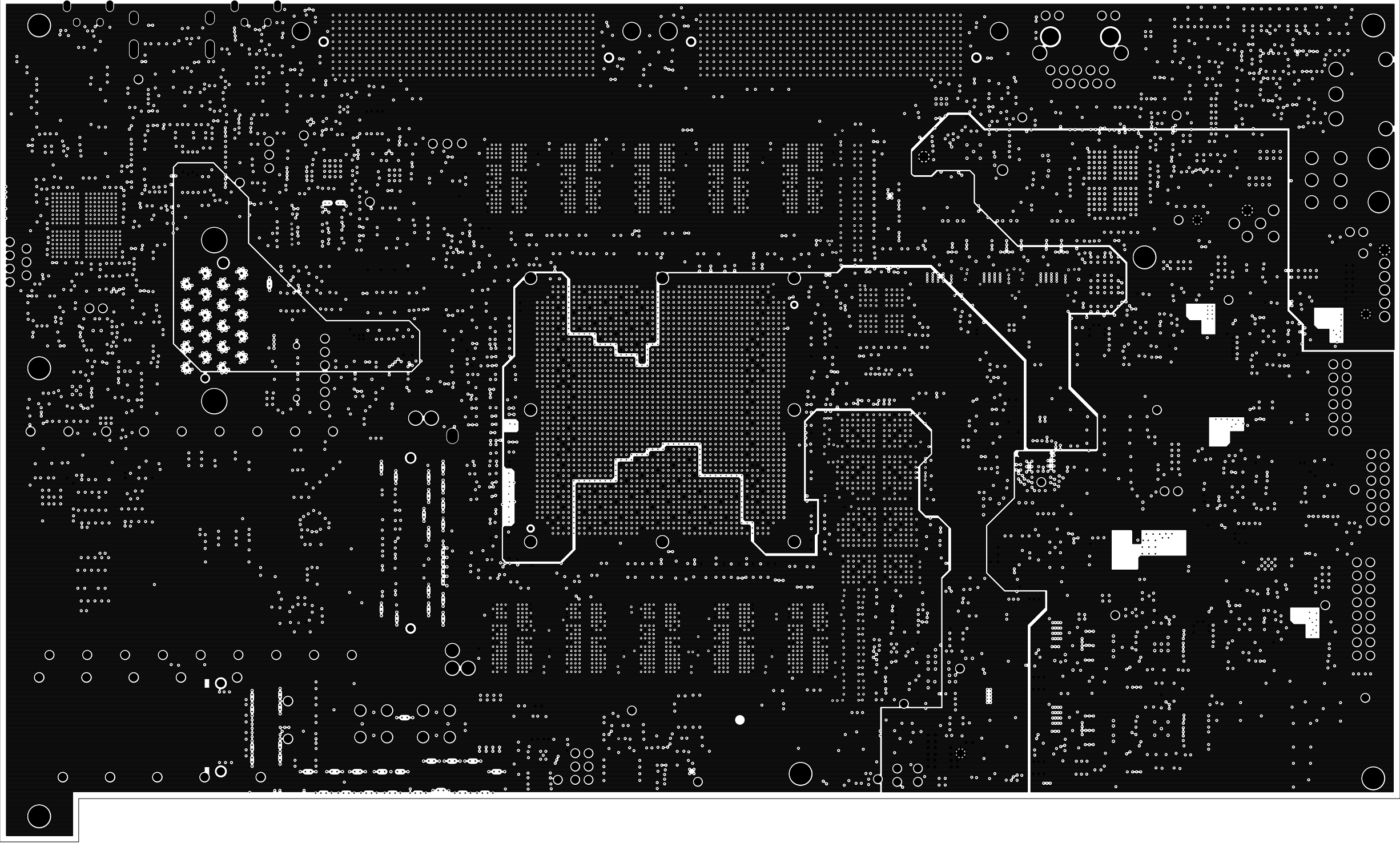


ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 10 OF 28 LAYER: 10_GND5	XILINX DOC USE ONLY REVISION: 01

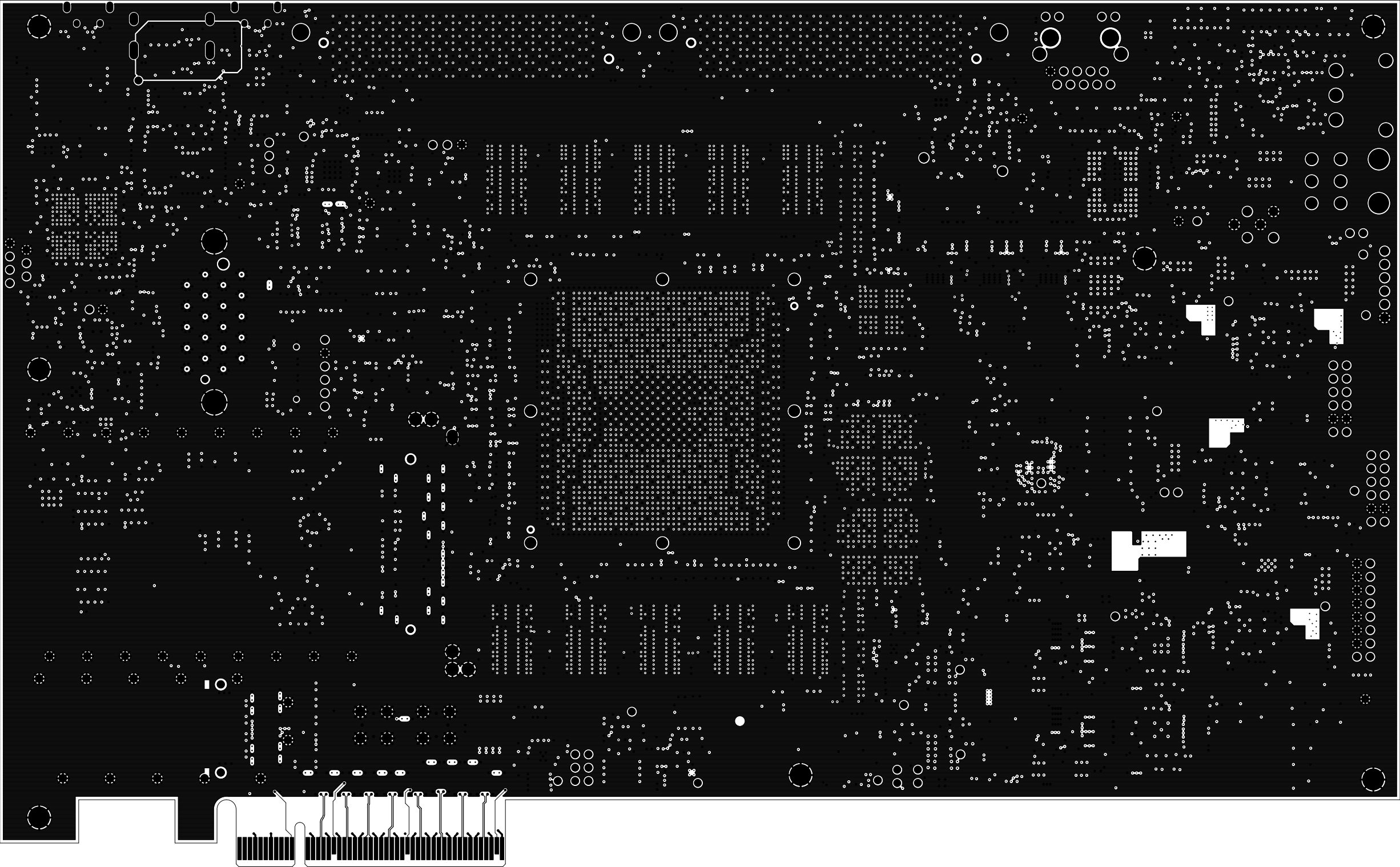




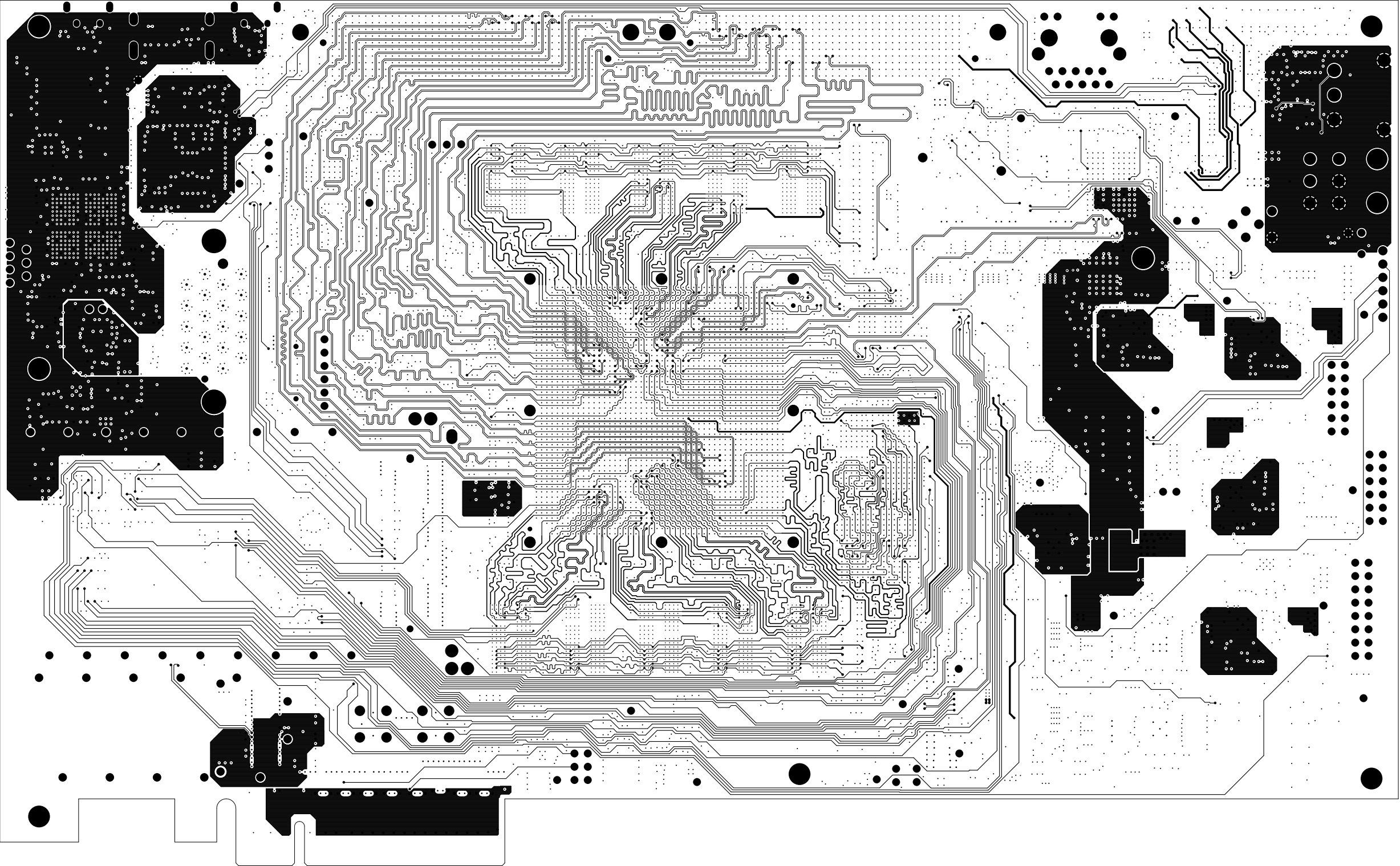
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 11 OF 28 LAYER: 11_PWR2	XILINX DOC USE ONLY REVISION: 01



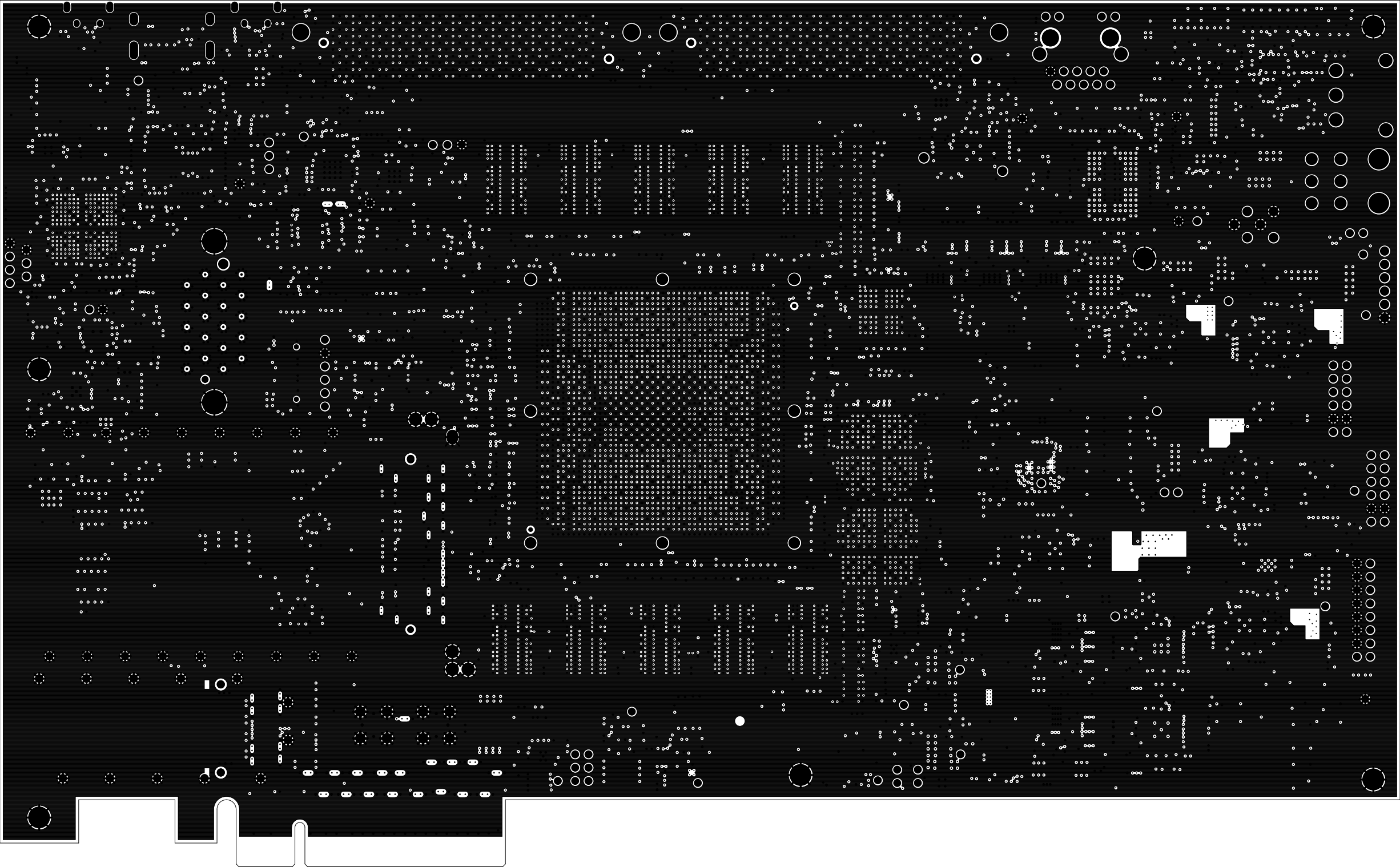
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 12 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 12_PWR3	



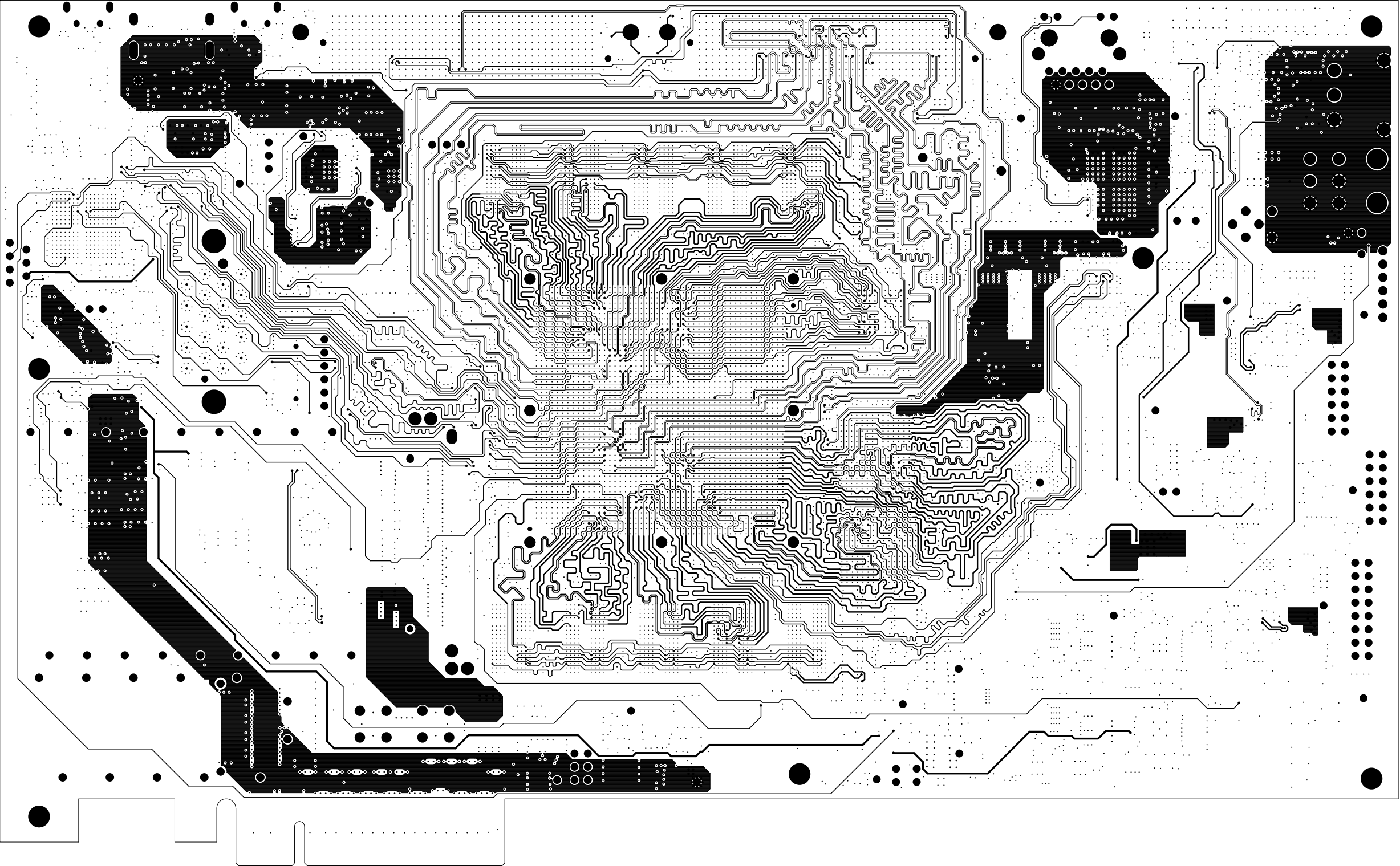
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 13 OF 28 LAYER: 13_GND6	XILINX DOC USE ONLY REVISION: 01



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PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
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SHEET 14 OF 28 LAYER: 14_SIG4	XILINX DOC USE ONLY REVISION: 01

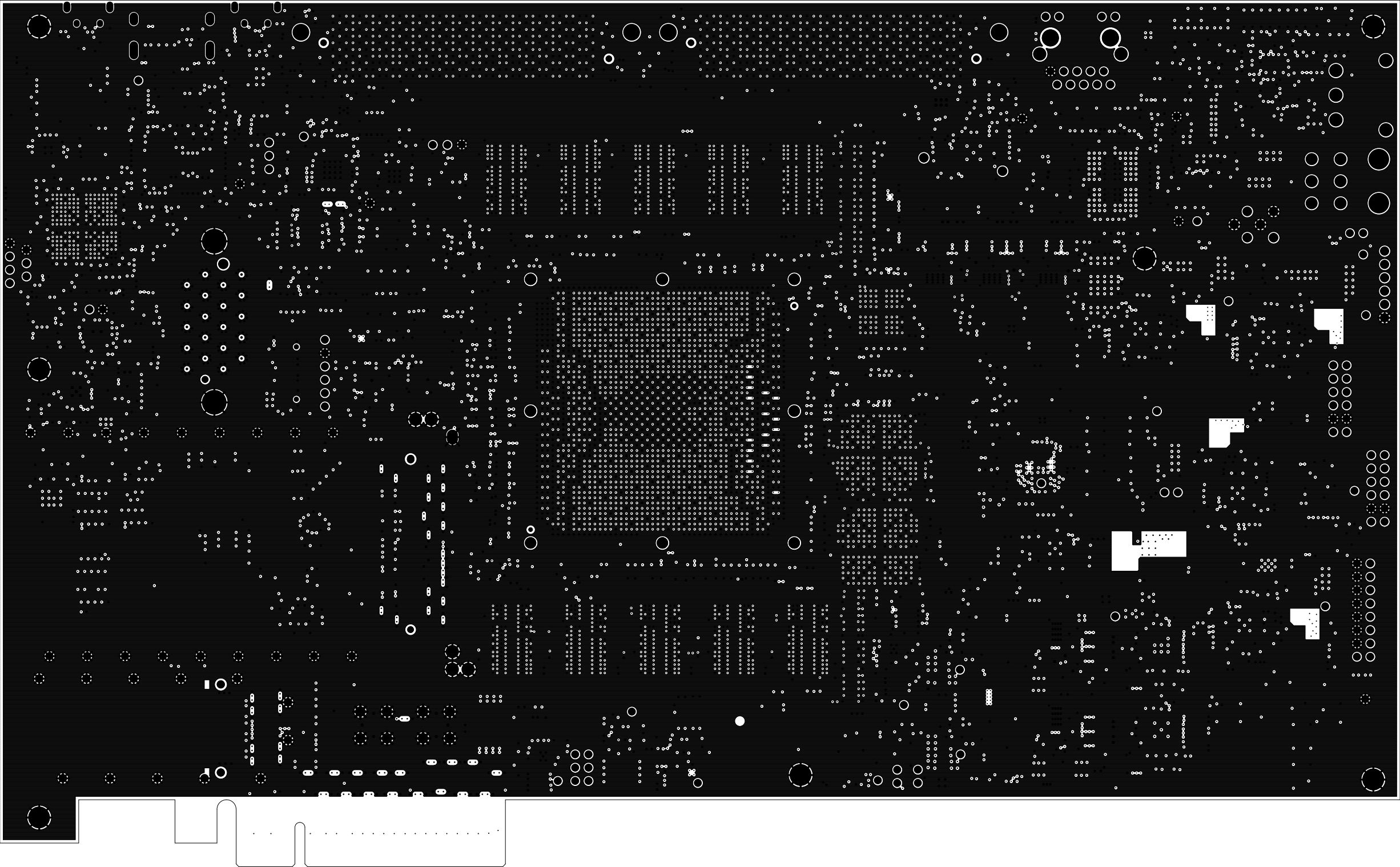


ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 15 OF 28 LAYER: 15_GND7	XILINX DOC USE ONLY REVISION: 01

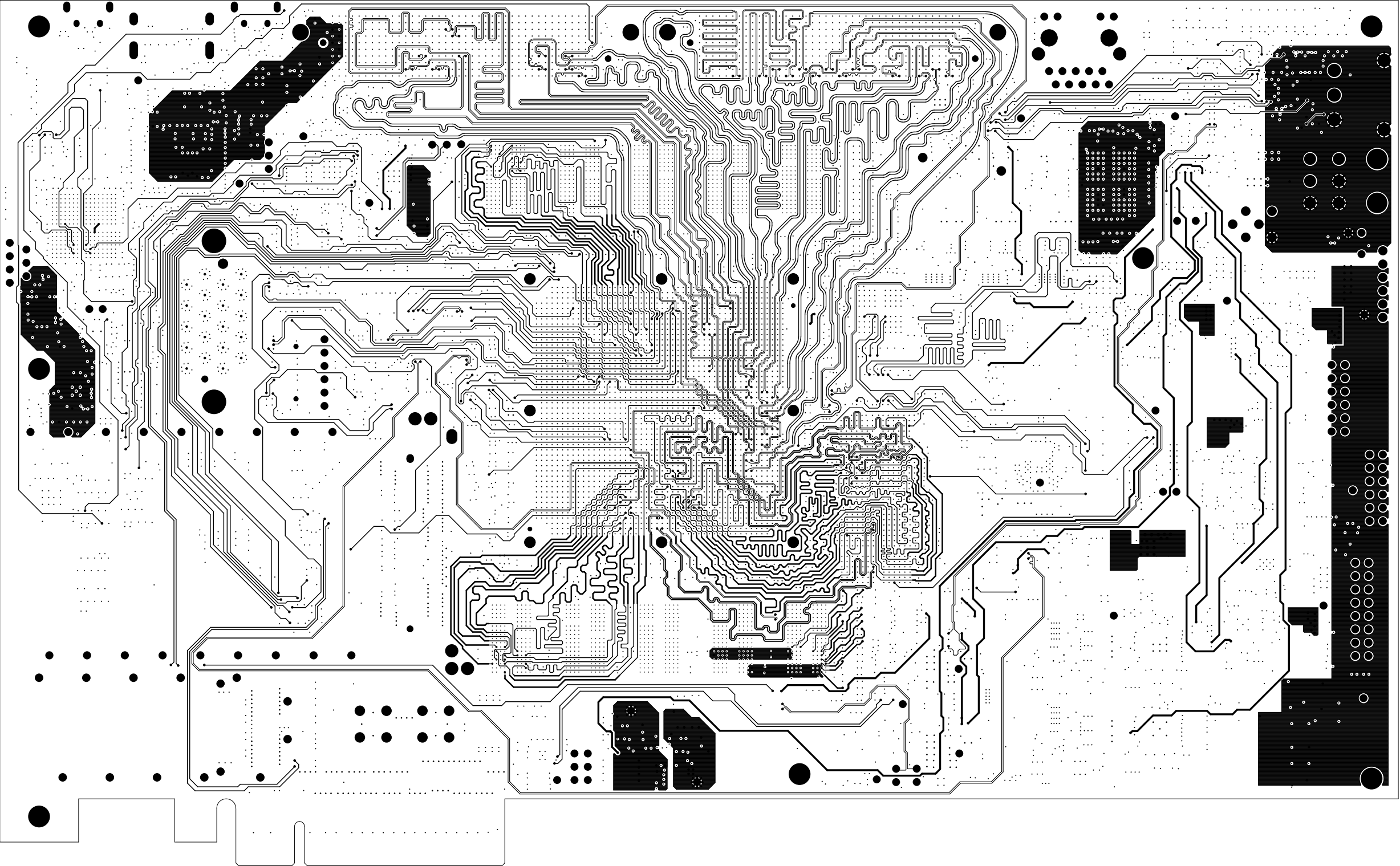


ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 16 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 16_SIG5	

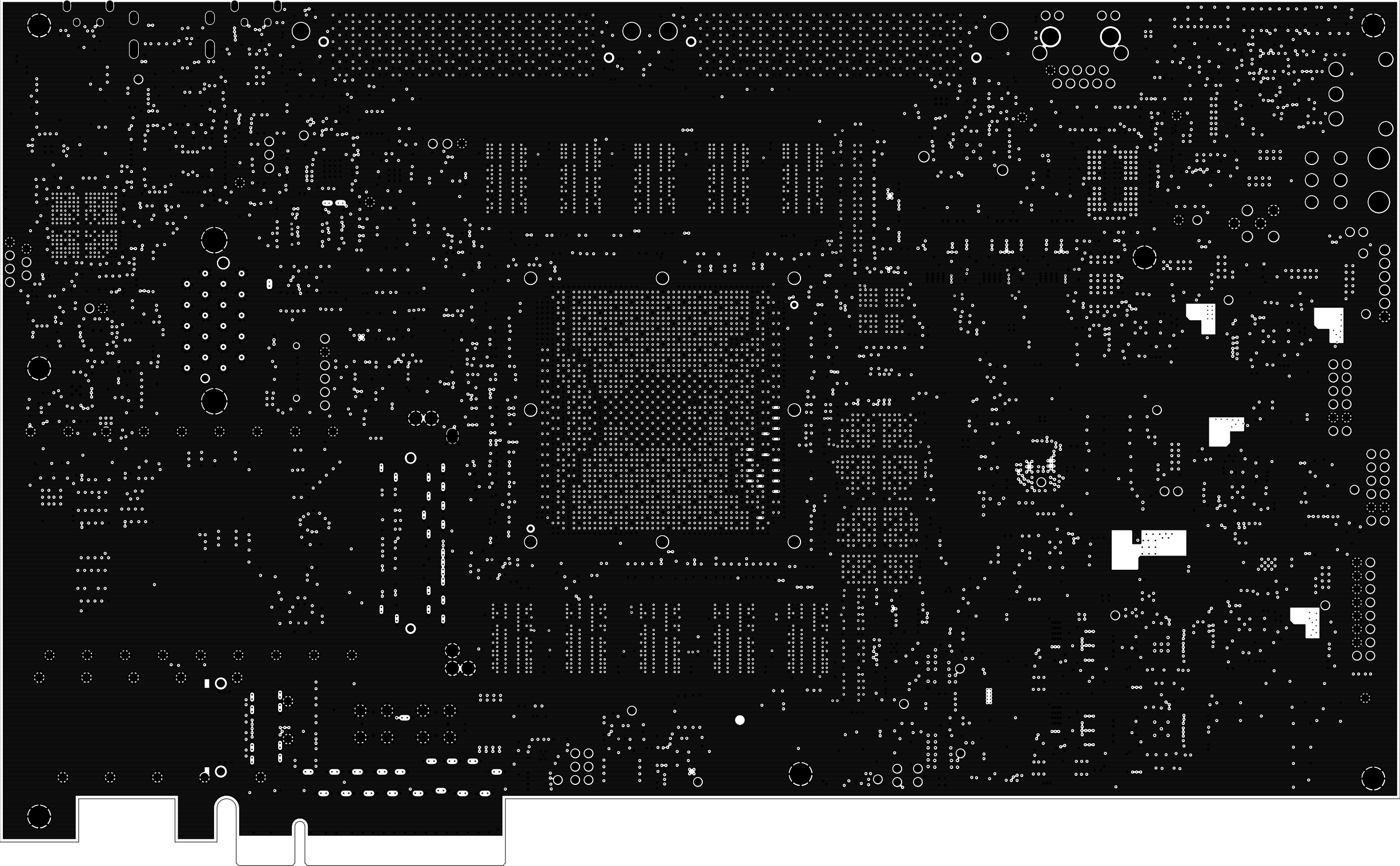




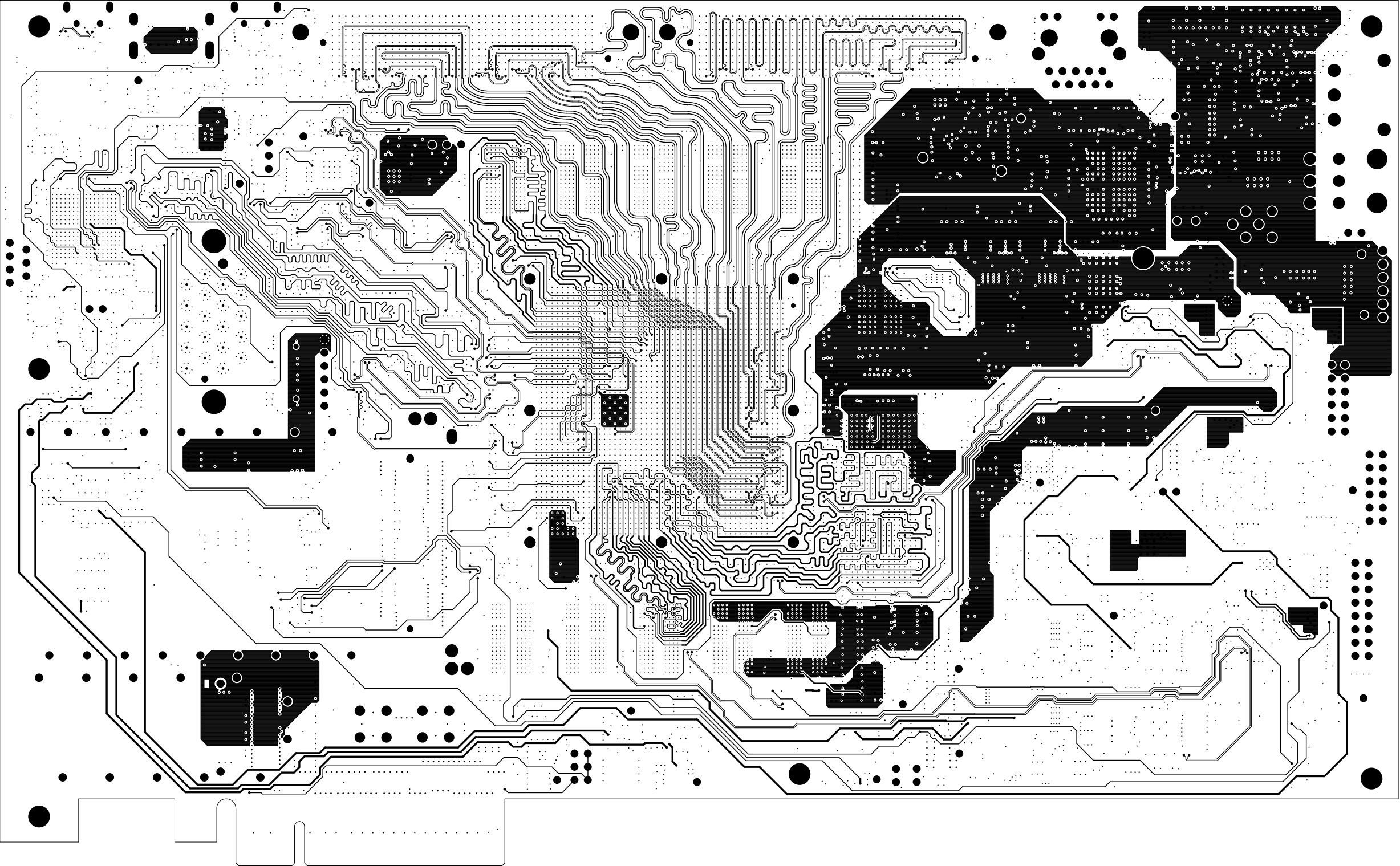
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 17 OF 28 LAYER: 17_GND8	XILINX DOC USE ONLY REVISION: 01



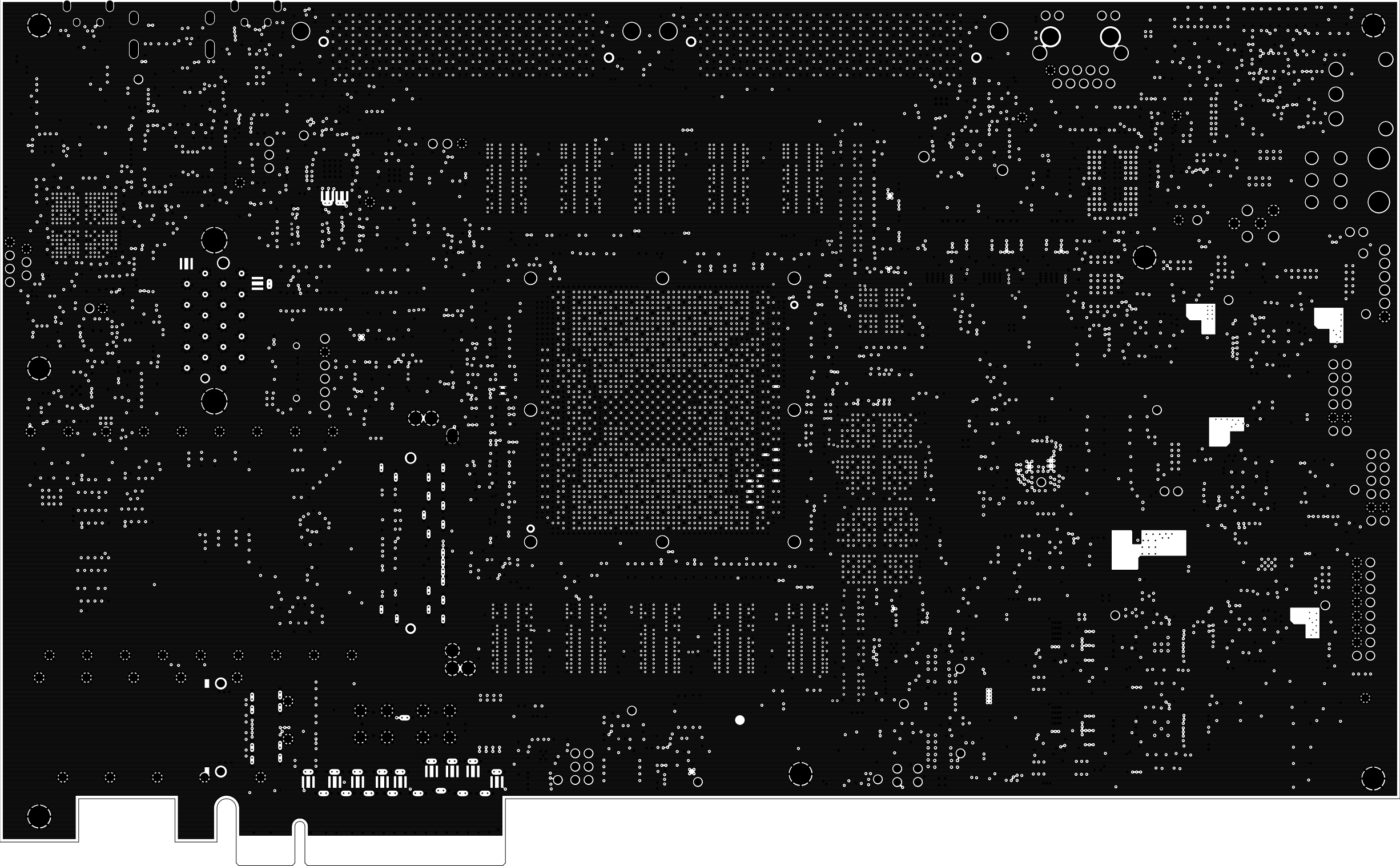
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 18 OF 28 LAYER: 18_SIG6	XILINX DOC USE ONLY REVISION: 01



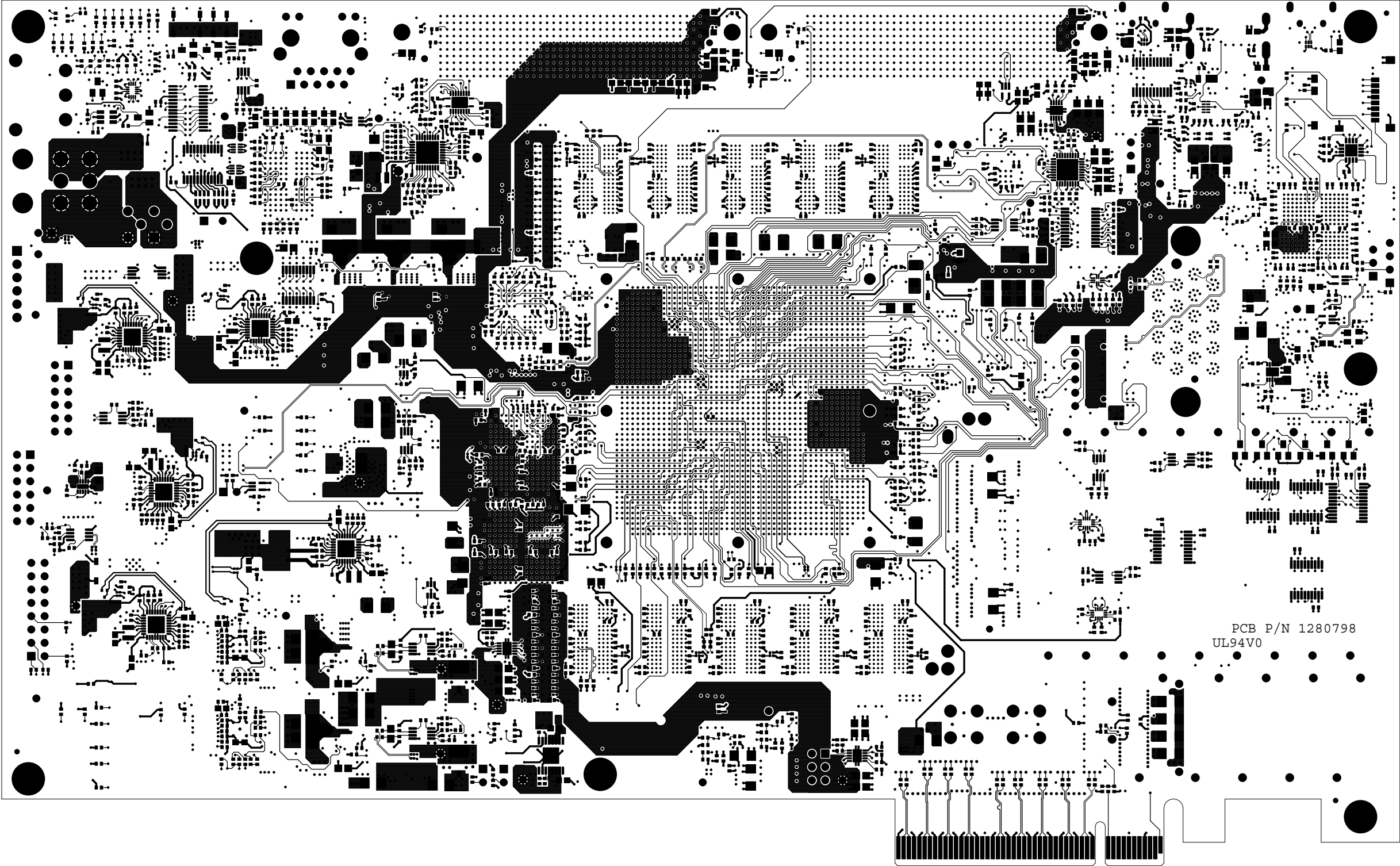
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 19 OF 28 LAYER: 19_GND9	XILINX DOC USE ONLY REVISION: 01



ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 20 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 20_SIG7	



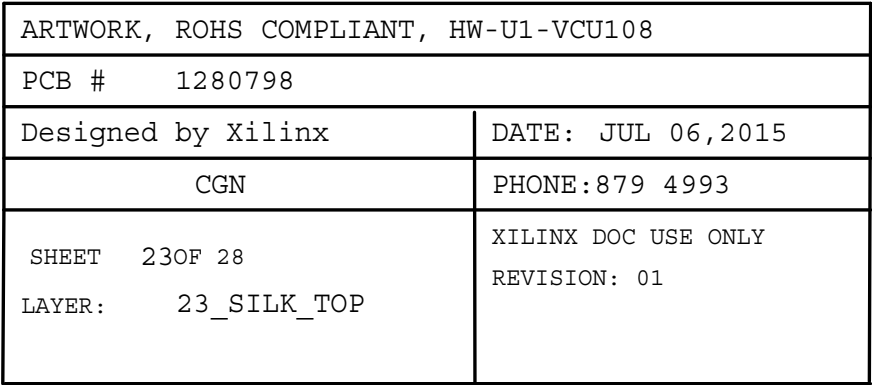
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 21 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 21_GND10	

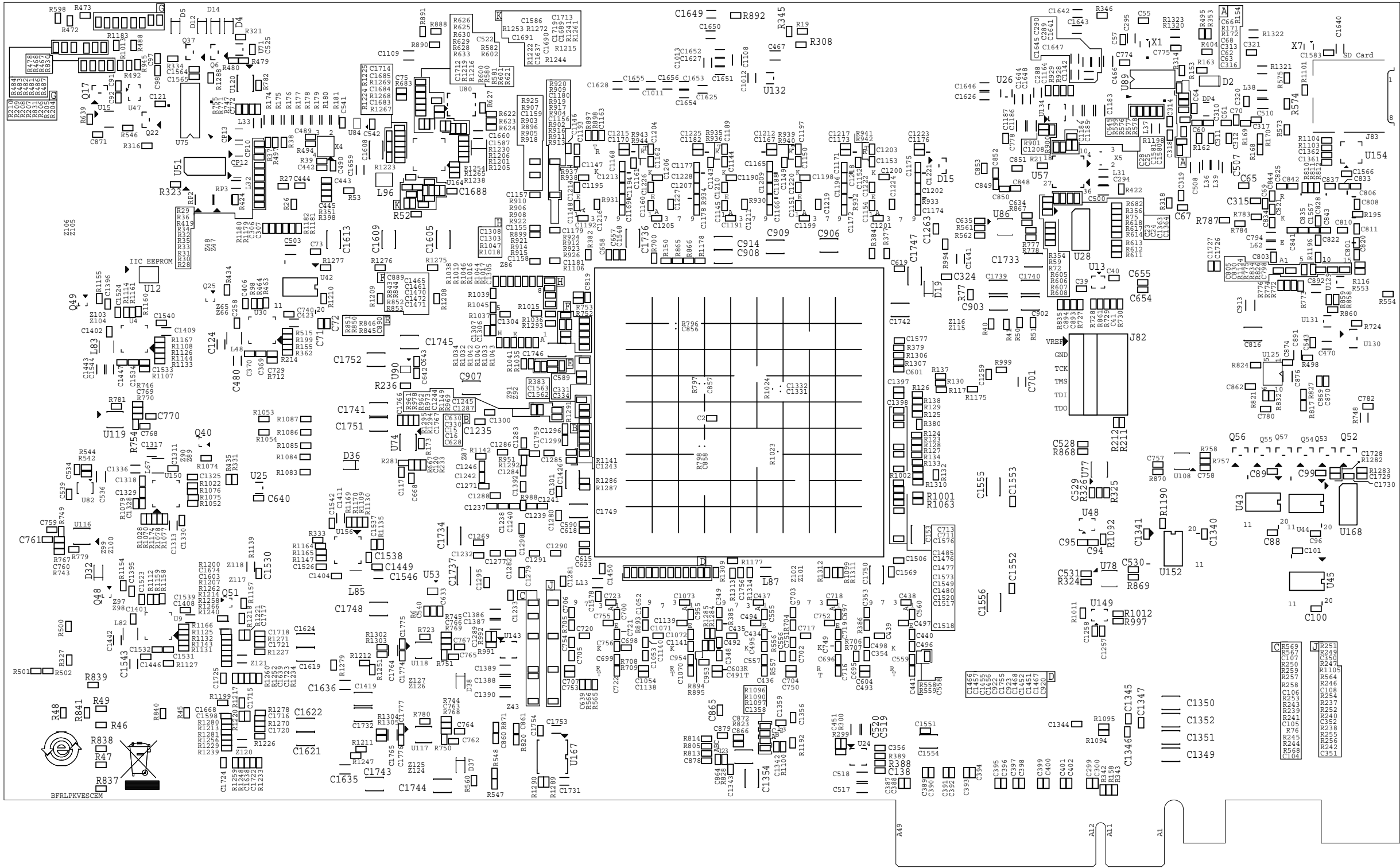


PCB P/N 1280798  
UL94V0

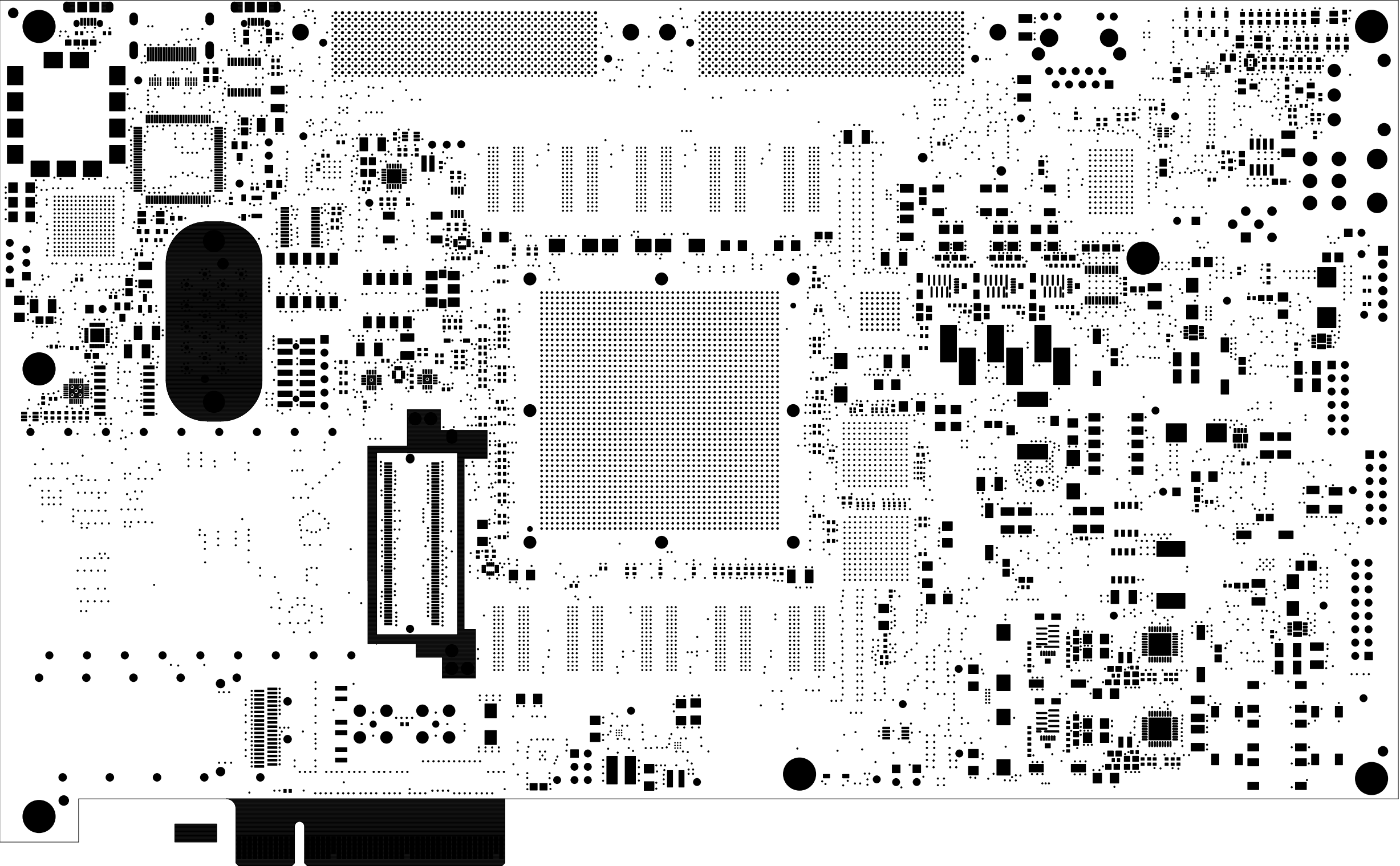
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 22 OF 28 LAYER: 22_BOTTOM	XILINX DOC USE ONLY REVISION: 01



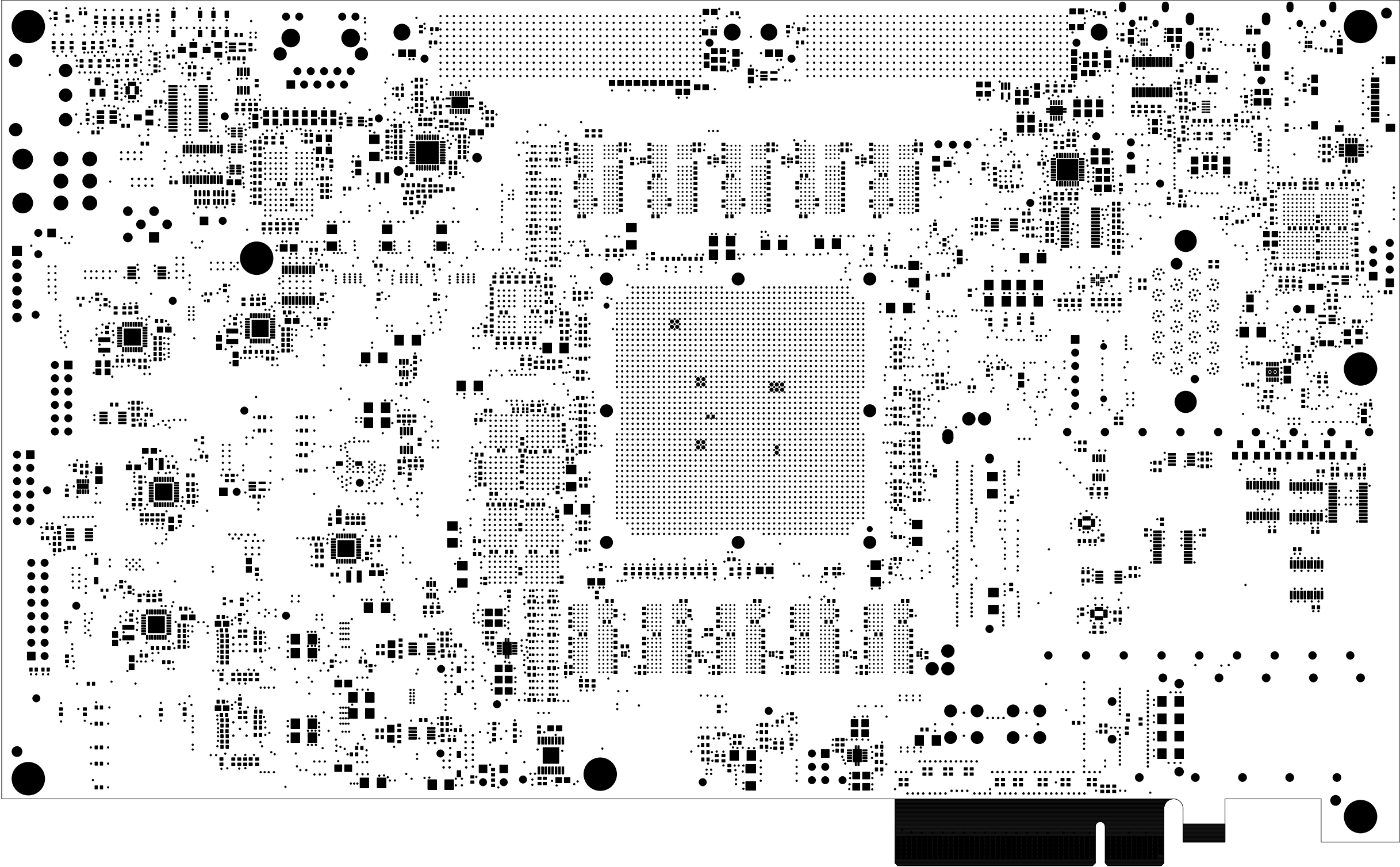




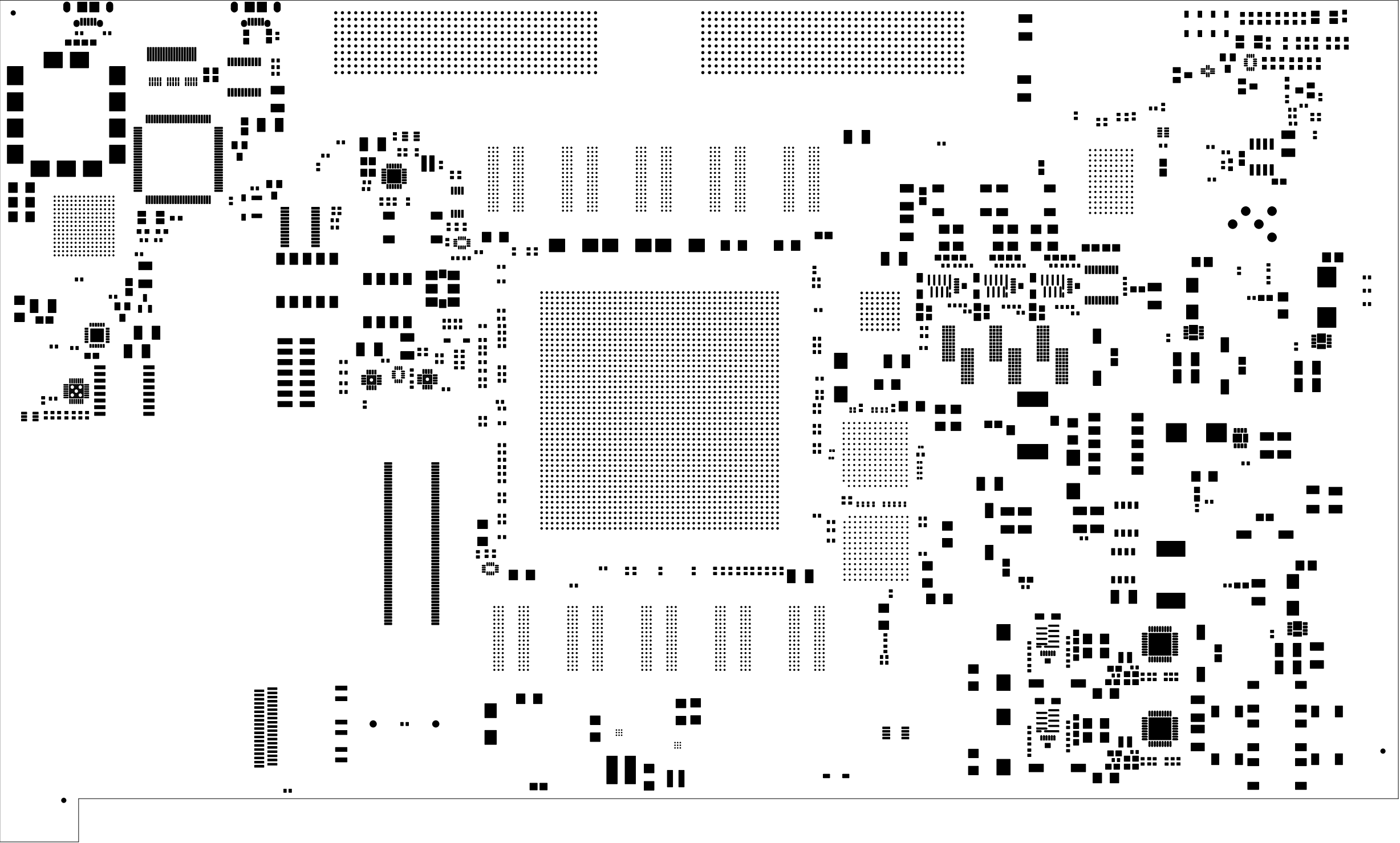
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 24 OF 28 LAYER: 24_SILK_BOT	XILINX DOC USE ONLY REVISION: 01



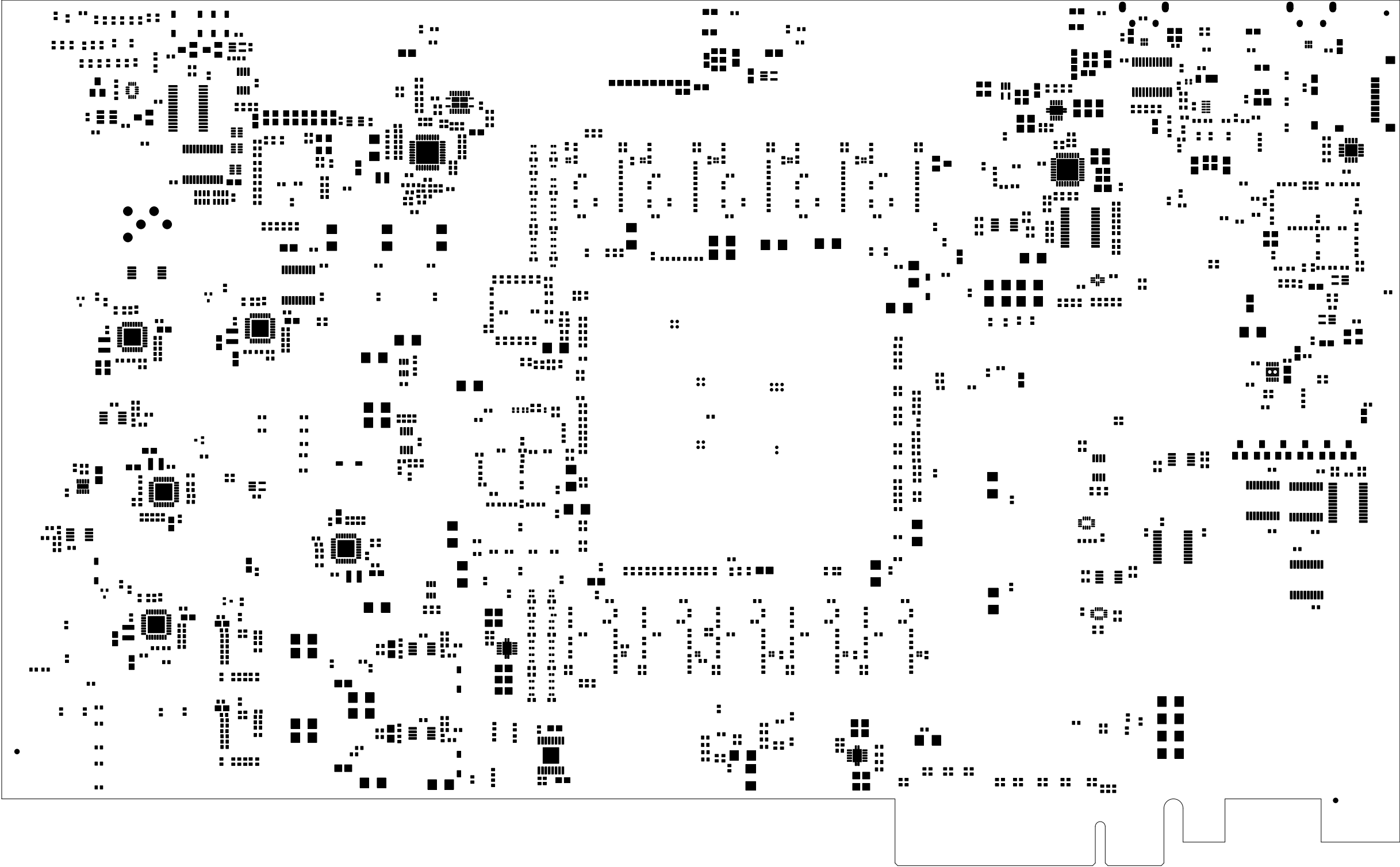
ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 25 OF 28	XILINX DOC USE ONLY REVISION: 01
LAYER: 25_SMASK_TOP	



ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 26 OF 28 LAYER: 26_SMASK_BOT	XILINX DOC USE ONLY REVISION: 01



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PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 27 OF 28 LAYER: 27_PMASK_TOP	XILINX DOC USE ONLY REVISION: 01



ARTWORK, ROHS COMPLIANT, HW-U1-VCU108	
PCB # 1280798	
Designed by Xilinx	DATE: JUL 06,2015
CGN	PHONE:879 4993
SHEET 28 OF 28 LAYER:28_PMASK_BOT	XILINX DOC USE ONLY REVISION: 01