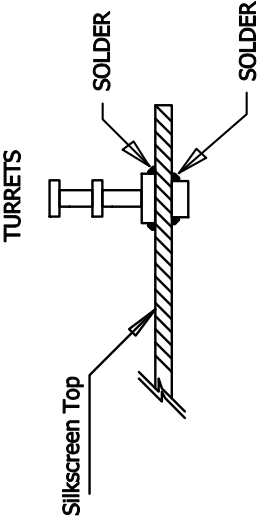



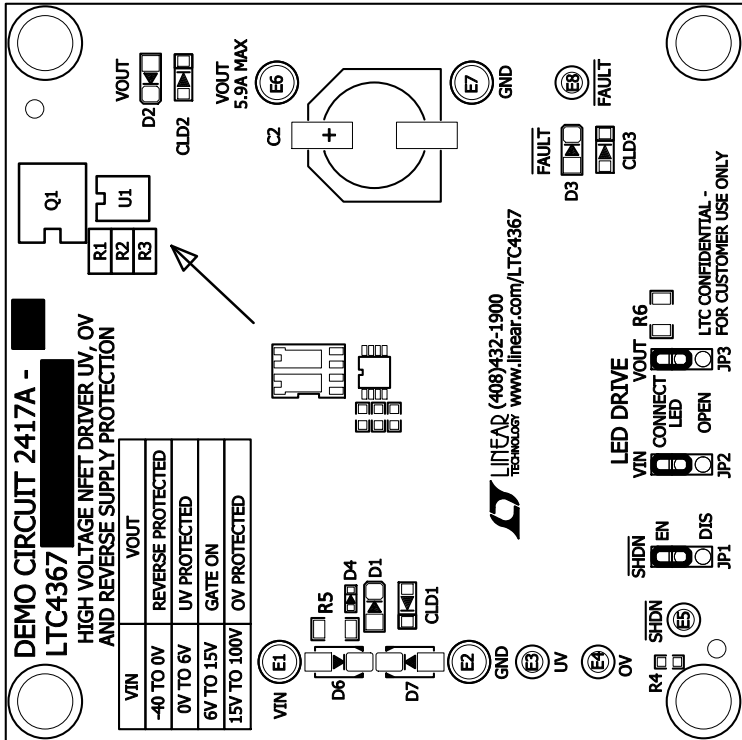
REVISION HISTORY			
ECO	REV	DESCRIPTION	DATE
—	1	1st Prototype	May 27, 2015
	2	Production	Oct 15, 2015

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS AS SHOWN.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. PARTS TO OMIT WILL BE SPECIFIED ON THE BILLS OF MATERIAL. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
5. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
6. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD. MAXIMUM SOLDER TEMPERATURE IS 240 deg C.



APPROVALS		 <b>LINEAR TECHNOLOGY</b>				1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY			
PCB DES.	R8	TITLE: TOP, ASSEMBLY DRAWING				HIGH VOLTAGE NFET DRIVER UV, OV AND REVERSE SUPPLY PROTECTION			
APP ENG.	Vlad Ostrerov					SIZE	IC NO. LTC4367IMS8/LTC4367IMS8-1	REV.	
						N/A	DEMO CIRCUIT 2417A	2	
SCALE = NONE		FILENAME: 2417A-Rev1.PCB				SHT 1 OF 2			



BOARD ASSEMBLY

U1		
DC2417A-A	LTC4367IMS8	
DC2417A-B	LTC4367IMS8-1	

