

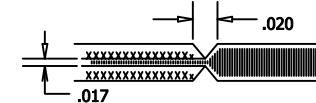
SIZE	QTY	SYM	PLATED	TOL
187	4	+	YES	+/-3mil
10	57	X	YES	+/-3mil
70	2	□	NO	+/-3mil
63	4	◇	YES	+/-3mil
31	9	⊗	YES	+/-3mil
94	4	⊠	YES	+/-3mil


REVISION HISTORY

ECO	REV	DESCRIPTION	APPR	DATE
—	1	1st Prototype	Vlad O	Jun 01, 2015
	2	Production	Vlad O	Oct 15, 2015

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS		 <div> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY </div>	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.1X" = ±0.01" 0.1XX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	R/B		
		APP ENG.	Vlad Ostrov	TITLE: FABRICATION DRAWING	
				HIGH VOLTAGE NFET DRIVER UV, OV AND REVERSE SUPPLY PROTECTION	
				SIZE N/A	IC NO. LTC4367IMS8/LTC4367IMS8-1 DEMO CIRCUIT 2417A
				REV 2	
		SCALE = NONE		FILENAME: 2417A-Rev1.PCB	SHT 1 OF 1