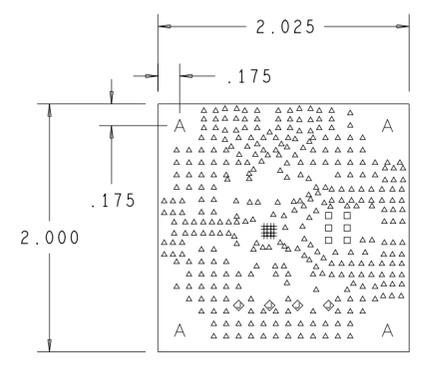


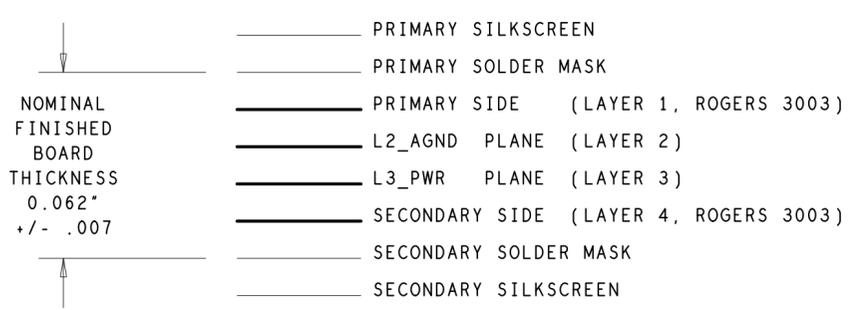
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	04/08	R.L.
B	ENGINEERING CHANGES	05FEB09	R.L.



HOLE TOLERANCE
 UNLESS SPECIFIED
 PLATED: +/- .003
 NON PLATED: +/- .001

FINISHED HOLES IN MILS				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
+	11.0	PLATED	16	SEE NOTE 16
△	12.0	PLATED	329	MAX DIAM
□	45.0	PLATED	6	
◇	63.0	PLATED	4	
A	125.0	NON-PLATED	4	

4 LAYER STACKUP



CHARACTERISTIC IMPEDANCE = 50 OHMS +/- 10%
 ARTWORK LINE WIDTH FOR
 IMPEDANCE CONTROLLED LINES = 0.024"

- NOTES:
- ACCEPTABILITY; PER ANALOG DEVICES, INC. SPECIFICATION TST00115 LATEST REVISION.
 - MATERIALS; ROGERS 3003 / IS410 IN ACCORDANCE WITH IPC-L-130 (LATEST REV.). GLASS FABRIC BASE, EPOXY RESIN, FIRE RESISTANT.
 - BONDING AGENT; PREIMPREGNATED B STAGE EPOXY GLASS CLOTH IN ACCORDANCE WITH IPC-L-109 (LATEST REV.).
 - CLADDING; EXTERNAL LAYERS 1/2 OZ. COPPER, OVERPLATE TO 1 1/2 OZ. INTERNAL SIGNAL LAYERS 1 OZ. COPPER. INTERNAL PLANE LAYERS 1 OZ. COPPER.
 - SOLDER MASK; SHALL BE LIQUID PHOTOIMAGABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3.
 - SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR YELLOW.
 - U.L. RATING; 94VO MINIMUM.
- FABRICATION:
1. REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
 2. UNDIMENSIONED HOLES TO BE LOCATED WITHIN +/- .005 OF THEIR TRUE POSITION WITH RESPECT TO ARTWORK.
 3. PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN .001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION. HOLE DIAMETERS APPLY AFTER PLATING.
 4. FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL, INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
 5. MINIMUM DESIGN LINE WIDTH IS .006 INCH.
 6. MINIMUM DESIGN SPACING IS .005 INCH.
 7. BOARD/PANEL MUST MEET IPC-A-600 (LATEST REV.) CLASS 2 FOR FLATNESS.
 8. MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;
 - A. U.L. CODE
 - B. DATE CODE (STAMP)
 - C. FLAMMABILITY RATING
 - D. MFGR. LOGO - DESIGNATED AREA.
 - E. SUCCESSFUL ELECTRICAL BOARD TEST.
 - F. LOT NUMBER - DESIGNATED AREA.
 9. NON-FUNCTIONAL PADS MAY BE REMOVED FROM INNER SIGNAL LAYERS AT MFGR. DISCRETION.
 10. IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY TEAR DROP PADS TO MAINTAIN ANNULAR RING AT PAD TO CIRCUIT INTERFACE ONLY AND MUST INSURE ELECTRICAL INTEGRITY. REPAIRS PER IPC-R-700 ARE ALLOWED.
 11. MODIFICATIONS TO THE ARTWORK, OTHER THAN THOSE DESCRIBED ON THE FABRICATION DRAWING, ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
 12. SURFACE TO BE IMMERSION GOLD BOARD PLATING, .000035" GOLD OVER LOW STRESS NICKEL PLATING, .000075" MIN PER QQ-N-290, CLASS I.
 13. IPC NETLIST TESTING TO BE DONE WITH NETLIST PROVIDED PRIOR TO FABRICATION. ANY INTENTIONAL SHORTS WILL BE DEFINED IN SHORTS.TXT FILE.
 14. .011 DIA HOLES TO BE NON-CONDUCTIVE FILLED PRIOR TO OVERPLATING.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 .XXX -.005	NCDRILL		ANALOG DEVICES		ASC DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
	APPROVAL	DATE	TITLE			
MATERIAL	MDL	04/08	CHAR. BOARD			
FINISH	APPROVED		SIZE	FSCM NO	DRAWING NUMBER	REV
	MFG ENGINEER		C		09-A02008	B
DO NOT SCALE DWG			SCALE	1X1		SHEET 1 OF 1