

REVISION HISTORY

| ECO | REV | DESCRIPTION | APPROVED | DATE |
|-----|-----|-------------|----------|----------|
| — | 1 | PRODUCTION | JIM M. | 08-31-12 |

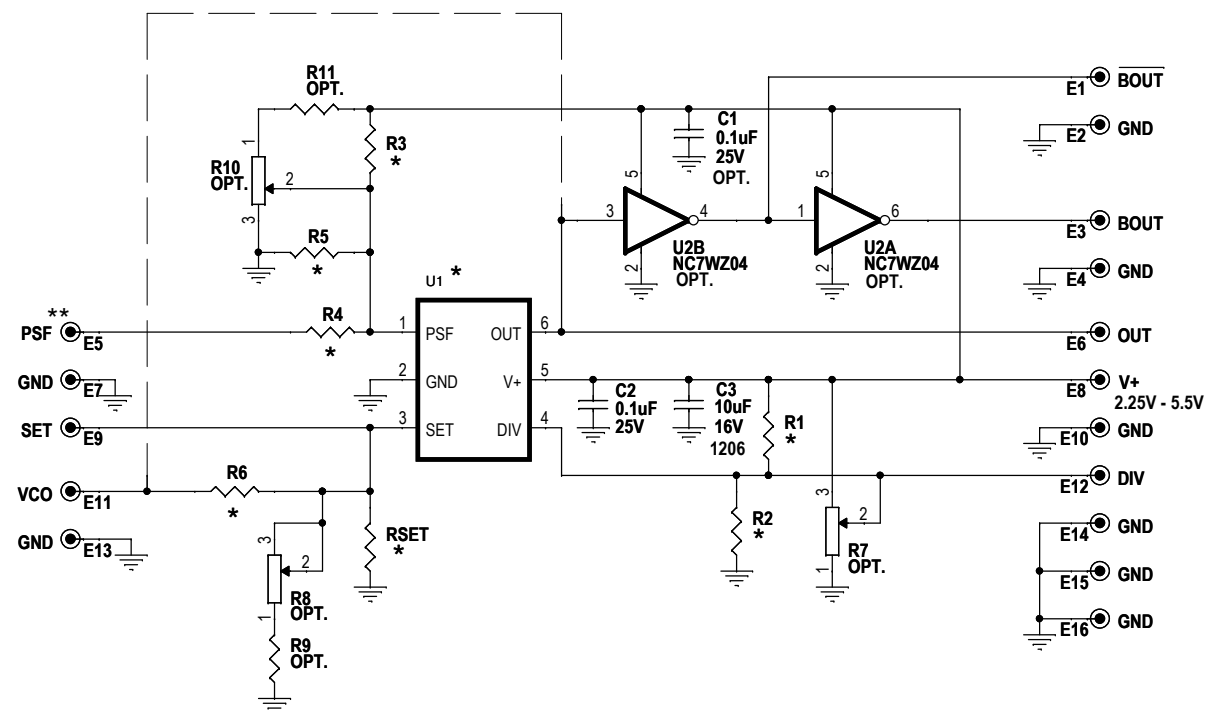
* VERSION TABLE

| ASSY TYPE | U1 | R1 | R2 | R3 | R4 | R5 | R6 | RSET | PSF |
|-----------|--------------|------|------|-------|-----|-------|------|-------|------|
| DC1562B-A | LTC6990CS6 | 1M | 681K | 100K | 0 | OPEN | 432K | 95.3K | OE |
| DC1562B-B | LTC6991CS6 | 976K | 182K | OPEN | 0 | 100K | OPEN | 768K | RST |
| DC1562B-C | LTC6992CS6-1 | 976K | 182K | 80.6K | 10K | 1.96K | OPEN | 316K | MOD |
| DC1562B-D | LTC6992CS6-2 | 976K | 182K | 51.1K | 10K | 1.74K | OPEN | 316K | MOD |
| DC1562B-E | LTC6992CS6-3 | 976K | 182K | 76.8K | 10K | 1.82K | OPEN | 316K | MOD |
| DC1562B-F | LTC6992CS6-4 | 976K | 182K | 54.9K | 10K | 1.87K | OPEN | 316K | MOD |
| DC1562B-G | LTC6993CS6-1 | 1M | 523K | OPEN | 0 | 100K | OPEN | 154K | TRIG |
| DC1562B-H | LTC6993CS6-2 | 1M | 523K | OPEN | 0 | 100K | OPEN | 154K | TRIG |
| DC1562B-I | LTC6993CS6-3 | 1M | 523K | OPEN | 0 | 100K | OPEN | 154K | TRIG |
| DC1562B-J | LTC6993CS6-4 | 1M | 523K | OPEN | 0 | 100K | OPEN | 154K | TRIG |
| DC1562B-K | LTC6994CS6-1 | 523K | 1M | OPEN | 0 | 100K | OPEN | 154K | TRIG |
| DC1562B-L | LTC6994CS6-2 | 1M | 523K | OPEN | 0 | 100K | OPEN | 154K | TRIG |
| DC1562B-M | LTC6995CS6-1 | 1M | 280K | OPEN | 0 | 100K | 191K | OPEN | RST |
| DC1562B-N | LTC6995CS6-2 | 1M | 280K | 100K | 0 | OPEN | 191K | OPEN | RST |

* STANDARD CONFIGURATIONS TABLE

| ASSY TYPE | U1 | CONFIGURED FUNCTION |
|-----------|--------------|---|
| DC1562B-A | LTC6990CS6 | 1KHz to 10KHz VCO |
| DC1562B-B | LTC6991CS6 | 1Hz (1 SEC) FIXED FREQUENCY |
| DC1562B-C | LTC6992CS6-1 | 10KHz, 0 - 100% DUTY CYCLE |
| DC1562B-D | LTC6992CS6-2 | 10KHz, 5 - 95% DUTY CYCLE |
| DC1562B-E | LTC6992CS6-3 | 10KHz, 0 - 95% DUTY CYCLE |
| DC1562B-F | LTC6992CS6-4 | 10KHz, 5 - 100% DUTY CYCLE |
| DC1562B-G | LTC6993CS6-1 | 100ms ONE SHOT, RISING EDGE TRIGGERED |
| DC1562B-H | LTC6993CS6-2 | 100ms RE-TRIGGERABLE (RISING EDGE) ONE SHOT |
| DC1562B-I | LTC6993CS6-3 | 100ms ONE SHOT, FALLING EDGE TRIGGERED |
| DC1562B-J | LTC6993CS6-4 | 100ms RE-TRIGGERABLE (FALLING EDGE) ONE SHOT |
| DC1562B-K | LTC6994CS6-1 | 100ms OUTPUT FALLING EDGE DELAYED FROM TRIGGER FALLING EDGE |
| DC1562B-L | LTC6994CS6-2 | 100ms OUTPUT RISING EDGE DELAYED FROM TRIGGER RISING EDGE |
| DC1562B-M | LTC6995CS6-1 | 1sec. POWER ON RESET |
| DC1562B-N | LTC6995CS6-2 | 1sec. POWER ON RESET |

JUMPER ADDED FOR DC1562B-M/N



** PSF = Part Specific Function, This Pin's Function Is Different Depending On The U1 Part used.

NOTE: UNLESS OTHERWISE SPECIFIED

- ALL RESISTORS ARE IN 0805.
- ALL CAPACITORS ARE IN 0805.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

| | |
|----------|--------|
| PCB DES. | A.K. |
| APP ENG. | JIM M. |
| | |
| | |

SCALE = NONE



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TITLE: SCHEMATIC

TimerBlox™

| | | |
|------|---|------|
| SIZE | IC NO. | REV. |
| N/A | LTC699X DEMO CIRCUIT 1562B-A/B/C/D/E/F/G/H/I/J/K/L | 1 |

DATE: Monday, May 06, 2013 SHEET 1 OF 1