

L1 - Component Side

SIZE	QTY	SYM	PLTD	TOL
0.035	15	□	YES	+/-0.003
0.07	2	◇	NO	+/-0.003
0.095	17	⊕	YES	+/-0.003
0.01	30	⊗	YES	+/-0.003
0.035	2	+	NO	+/-0.003

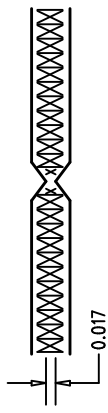
REVISIONS		
REV	DESCRIPTION	DATE

LAYER STACKUP

- ART01 = LAYER 1 (COMPONENT SIDE)
- ART02 = LAYER 2 (GND)
- ART03 = LAYER 3 (GND)
- ART04 = LAYER 4 (SOLDER SIDE)

NOTES : Unless Otherwise Specified

- FAB PER IPC-A-600.
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.  
2 OZ. COPPER FINISH ON OUTER LAYERS.  
1 OZ. COPPER FINISH ON INNER LAYERS.  
THICKNESS .062 +/- .006 TOTAL OF 4 LAYERS.  
FLAMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: DIMENSIONS SHOWN AND TOLERANCES +/-0.010  
0.000 ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, .001 INCH THICK MIN. ALL  
HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/- .003  
INCH IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.  
SILVER IMMERSON BOTH SIDES.
- SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- SCORING:



Fabrication Drawing

UNLESS OTHERWISE SPECIFIED  
DIMENSIONS ARE IN INCHES  
TOLERANCES ON ANGLE ± 1  
2 PLACE ± .01 3 PLACE ± .005  
INTERPRET DIM AND TOL  
PER ASME Y14.5M - 1994

APPROVALS		INIT		DATE	
DRAWN	CHECK				
DESIGN	Heien Zhu			12/8/05	
ENGR	Liu Yang			12/8/05	

SCALE = NONE		DES-250		SHEET 1 of 1	
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1630 McCarthy Blvd.  
Milpitas, CA 95035  
PH: (408)432-1900

TITLE: LTC4085EDE  
USB Power Manager with Ideal Diode Controller

SIZE	DEM0	DC937A	REV. A
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