

PIC18F26/46/56Q43

PIC18F26/46/56Q43 Silicon Errata and Data Sheet Clarifications

The PIC18F26/46/56Q43 devices that you have received conform functionally to the current device data sheet (DS40002171B), except for the anomalies described in this document.

The silicon issues discussed in the following pages are for silicon revisions with the Device and Revision IDs listed in the table below.

The errata described in this document will be addressed in future revisions of the PIC18F26/46/56Q43 silicon.

Note: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current.

Table 1. Silicon Device Identification

Part Number	Device ID	Revision ID	
Fait Number	Device iD	В0	
PIC18F26Q43	0x7420	0xA040	
PIC18F46Q43	0x7440	0xA040	
PIC18F56Q43	0x7460	0xA040	



Important: Refer to the Device/Revision ID section in the current "PIC18FXXQ43 Family Programming Specification" (DS40002079) for more detailed information on Device Identification and Revision IDs for your specific device.

Table 2. Silicon Issue Summary

Module	Feature	Item No.	Issue Summary	Affected Revisions B0		
ADCC	Capacitive Voltage Divider	1.1.1	CVD is only functional on PORTA[2:0] and PORTB[4:0]	Х		
Note: Only those issues indicated in the last column apply to the current silicon revision.						

Table of Contents

			. 1
1.	Silico	n Errata Issues	. 4
	1.1.	Module: Analog-to-Digital Converter with Computation (ADCC)	.4
2.	Data :	Sheet Clarifications	.5
	2.1.	None	.5
3.	APPE	NDIX A: Revision History	.6
Mic	rochip	Devices Code Protection Feature	.7
The	Micro	chip Website	.7
Pro	duct C	nange Notification Service	.7
Cus	tomer	Support	. 7
Mic	rochip	Devices Code Protection Feature	.7
Leg	al Noti	ce	. 8
Trac	demark	S	. 8
Qua	ality Ma	nagement System	. 9
Woi	ldwide	Sales and Service	10

1. Silicon Errata Issues



Notice: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current. Only the issues indicated by the bold font in the following tables apply to the current silicon revision.

1.1 Module: Analog-to-Digital Converter with Computation (ADCC)

1.1.1 Capacitive Voltage Divider (CVD)

The CVD feature is only functional on PORTA[2:0] and PORTB[4:0]. This feature is not recommended for use on any other pins.

Work around

None.

Affected Silicon Revisions

В0				
Х				

2. Data Sheet Clarifications

The following typographic corrections and clarifications are to be noted for the latest version of the device data sheet 40002171B):

Note:

Corrections are shown in bold. Where possible, the original bold text formatting has been removed for clarity.

2.1 None

There are no known data sheet clarifications as of this publication date.

APPENDIX A: Revision History

3. APPENDIX A: Revision History

Doc Rev.	Date	Comments
В	02/2020	Add working pins for CVD.
A	12/2019	Initial document release.

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