

Test Procedure for the NCP3063BSTEXG Evaluation Board

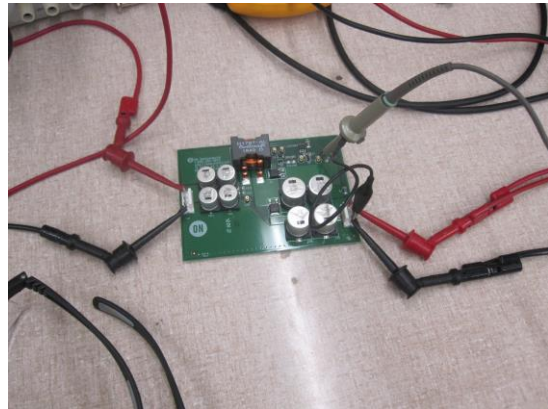
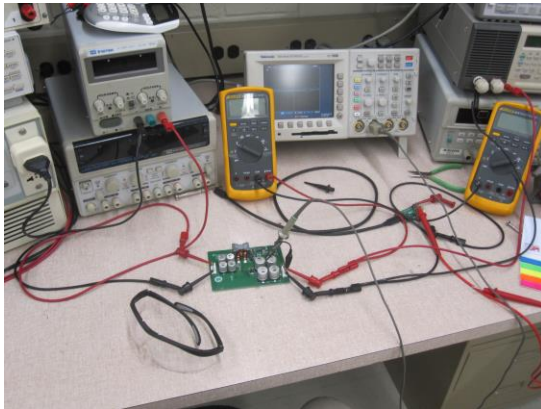
ON Semiconductor®



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Table 1: Suggested Equipment for Test

Equipment	Quantity	Voltage (V)	Current (A)	Power (W)
DC source	1	12	8	96
Multimeter	1	14V	NA	NA
Load	1	25V	3A	75W
Oscilloscope	1	25V	NA	NA



Test Procedure for Boost Demonstration Board

1. Apply a 12 V input to the part with no load attached to the demonstration board. The user should observe an output voltage of approximately 24 V.
2. Apply any load up to the nominal 2 A load and observe the load regulation and point efficiencies.
3. Test pins labeled CTP, GD, SE, and SWN should be observed for frequency control signal, ground signal, emitter of the Darlington, and switch node of the boost circuit, respectively. The user should ensure good grounding techniques when measuring critical signals.



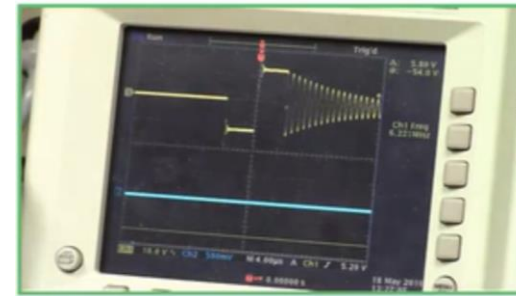
CTP



GD



SE



SWN