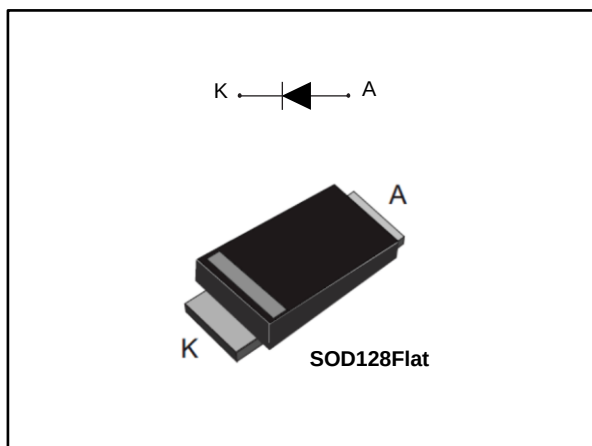


High voltage power Schottky rectifier

Datasheet - production data



Description

This high voltage Schottky barrier rectifier device is packaged in SOD128Flat and designed for high frequency miniature switched mode power supplies and for board DC to DC converters.

Table 1: Device summary

Symbol	Value
$I_{F(AV)}$	5 A
V_{RRM}	100 V
$T_j(max.)$	175 °C
$V_F(typ.)$	0.51 V

Features

- Negligible switching losses
- High junction temperature capability
- Low leakage current
- Good trade-off between leakage current and forward voltage drop
- Avalanche specification
- ECOPACK® compliant component

1 Characteristics

Table 2: Absolute ratings (limiting values at 25 °C, unless otherwise specified)

Symbol	Parameter		Value	Unit
V _{RRM}	Repetitive peak reverse voltage		100	V
I _{F(AV)}	Average forward current	T _L = 115 °C, δ = 0.5, square pulse	5	A
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms sinusoidal	125	A
		t _p = 8.3 ms sinusoidal	130	
P _{ARM}	Repetitive peak avalanche power	t _p = 10 μs, T _j = 125 °C	165	W
T _{stg}	Storage temperature range		-65 to +175	°C
T _j	Maximum operating junction temperature ⁽¹⁾		175	°C

Notes:

⁽¹⁾(dP_{tot}/dT_j) < (1/R_{th(j-a)}) condition to avoid thermal runaway for a diode on its own heatsink.

Table 3: Thermal parameters

Symbol	Parameter	Max. value	Unit
R _{th(j-l)}	Junction to lead	16	°C/W

Table 4: Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
I _R ⁽¹⁾	Reverse leakage current	T _j = 25 °C	V _R = 100 V	-	0.7	3.5	μA
		T _j = 125 °C		-	1	4	mA
		T _j = 150 °C		-		16	
V _F ⁽²⁾	Forward voltage drop	T _j = 25 °C	I _F = 2.5 A	-		0.67	V
		T _j = 125 °C		-	0.51	0.55	
		T _j = 25 °C	I _F = 5 A	-		0.76	
		T _j = 125 °C		-	0.57	0.61	

Notes:

⁽¹⁾Pulse test: t_p = 5 ms, δ < 2%

⁽²⁾Pulse test: t_p = 380 μs, δ < 2%

To evaluate the conduction losses use the following equation:

$$P = 0.49 \times I_{F(AV)} + 0.024 \times I_{F(RMS)}^2$$

For more information, please refer to the following application notes related to the power losses.

- AN604 (Calculation of conduction losses in a power rectifier)
- AN4021 (Calculation of reverse losses in a power diode)

1.1 Characteristics (curves)

Figure 1: Average forward power dissipation versus average forward current

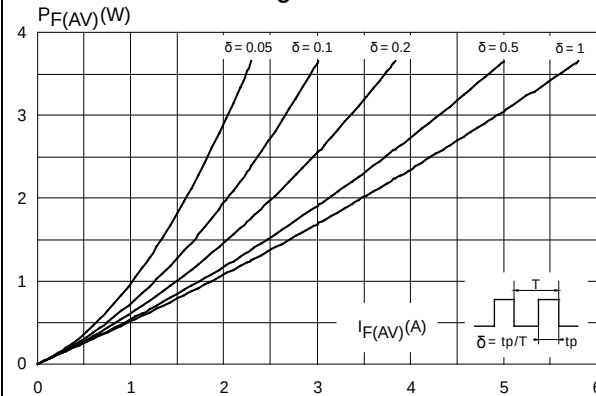


Figure 2: Average forward current versus ambient temperature ($\delta = 0.5$)

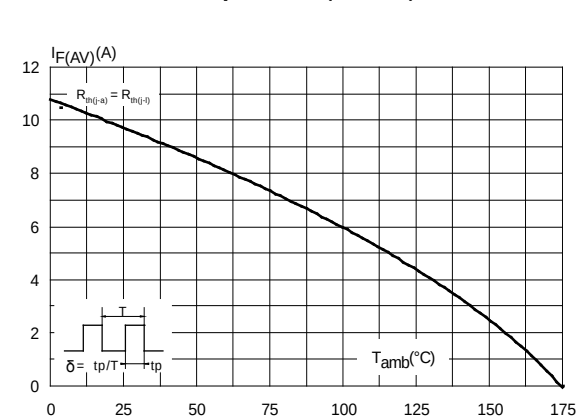


Figure 3: Normalized avalanche power derating versus pulse duration

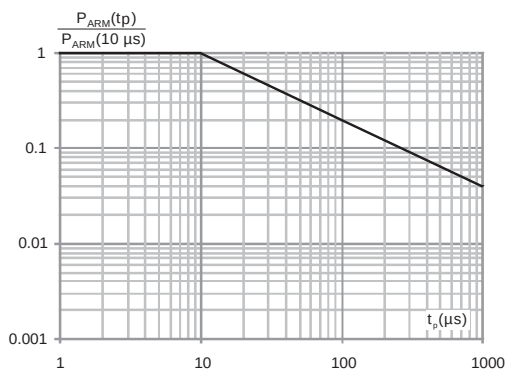


Figure 4: Relative variation of thermal impedance junction to lead versus pulse duration

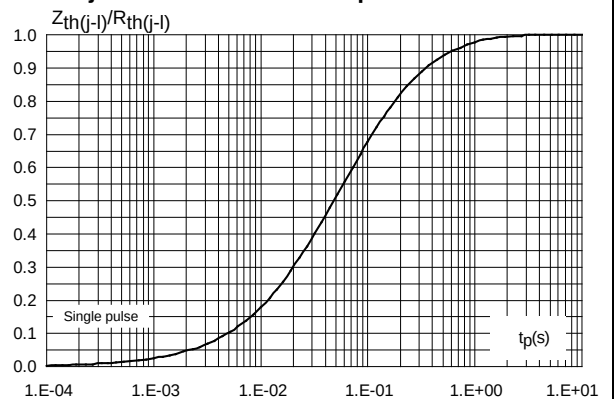


Figure 5: Reverse leakage current versus reverse voltage applied (typical values)

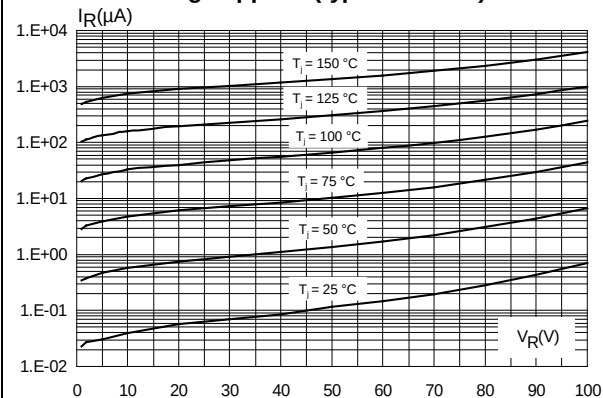


Figure 6: Junction capacitance versus reverse voltage applied (typical values)

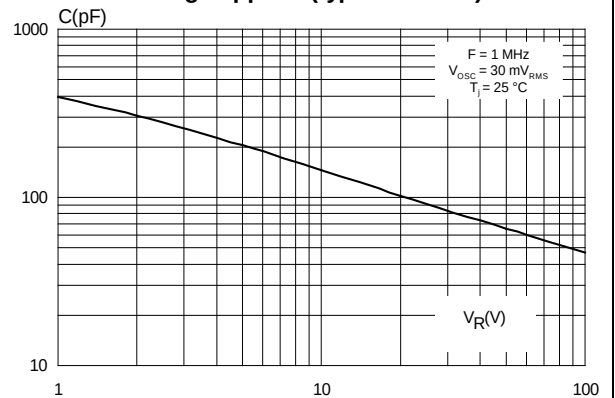
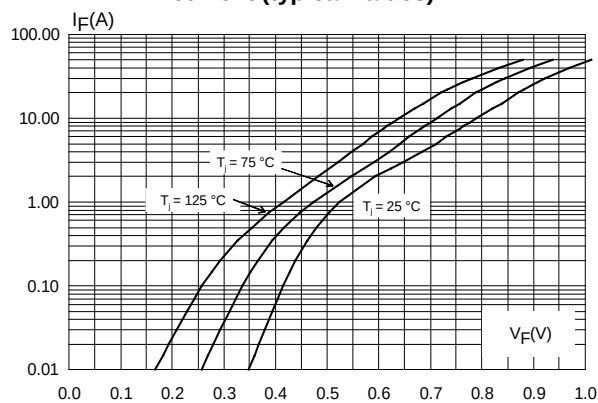
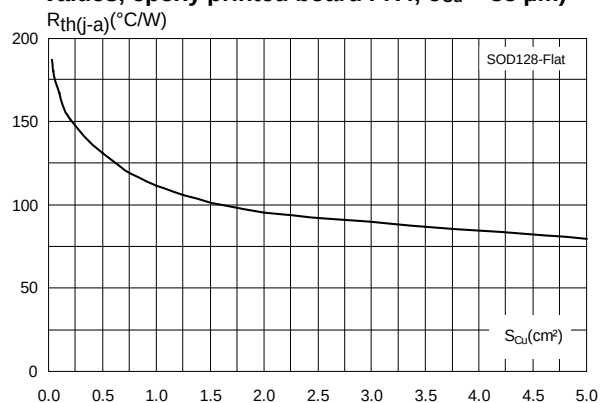


Figure 7: Forward voltage drop versus forward current (typical values)**Figure 8: Thermal resistance junction to ambient versus copper surface under each lead (typical values, epoxy printed board FR4, $e_{Cu} = 35 \mu m$)**

2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

- Epoxy meets UL94, V0
- Lead-free package

2.1 SOD128Flat package information

Figure 9: SOD128Flat package outline

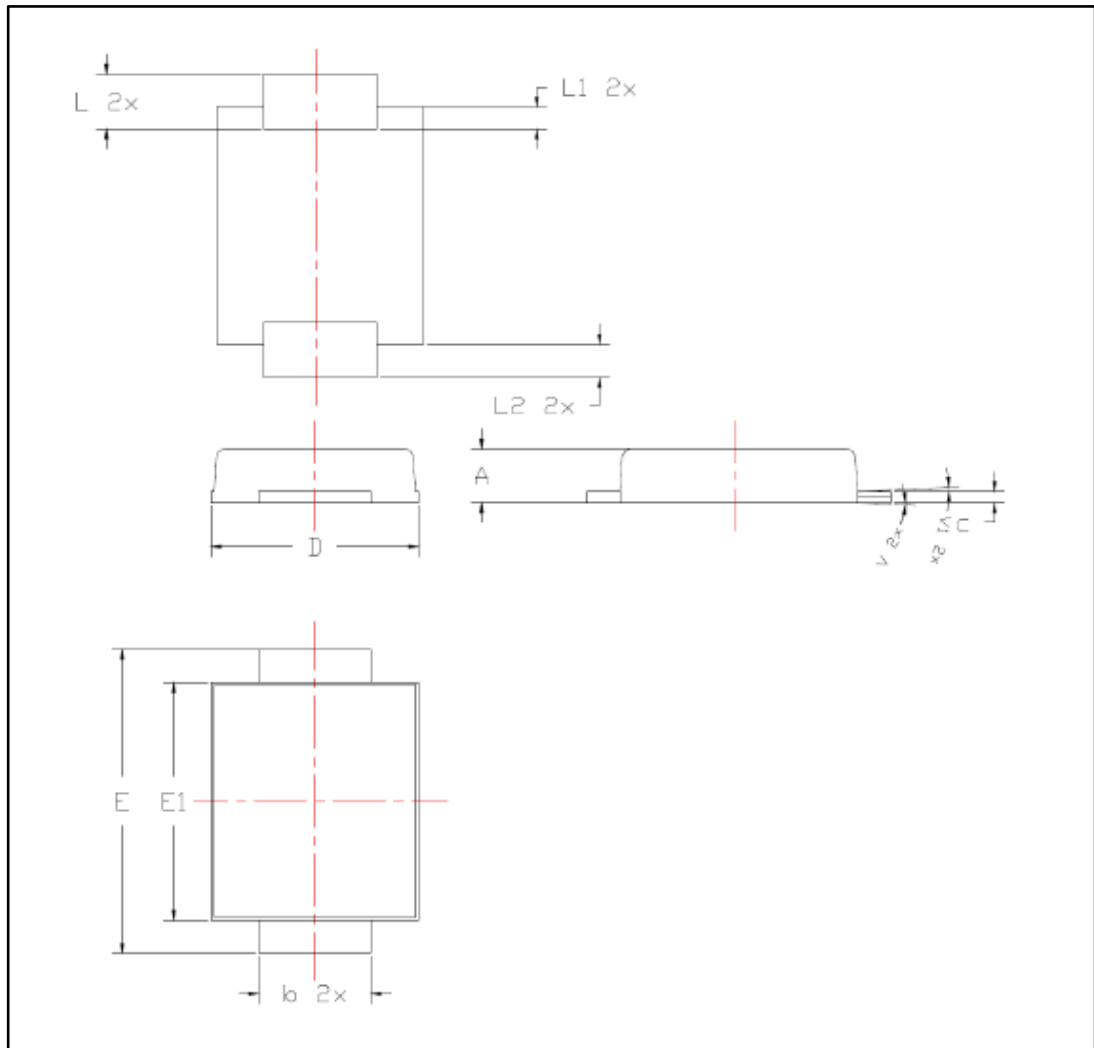
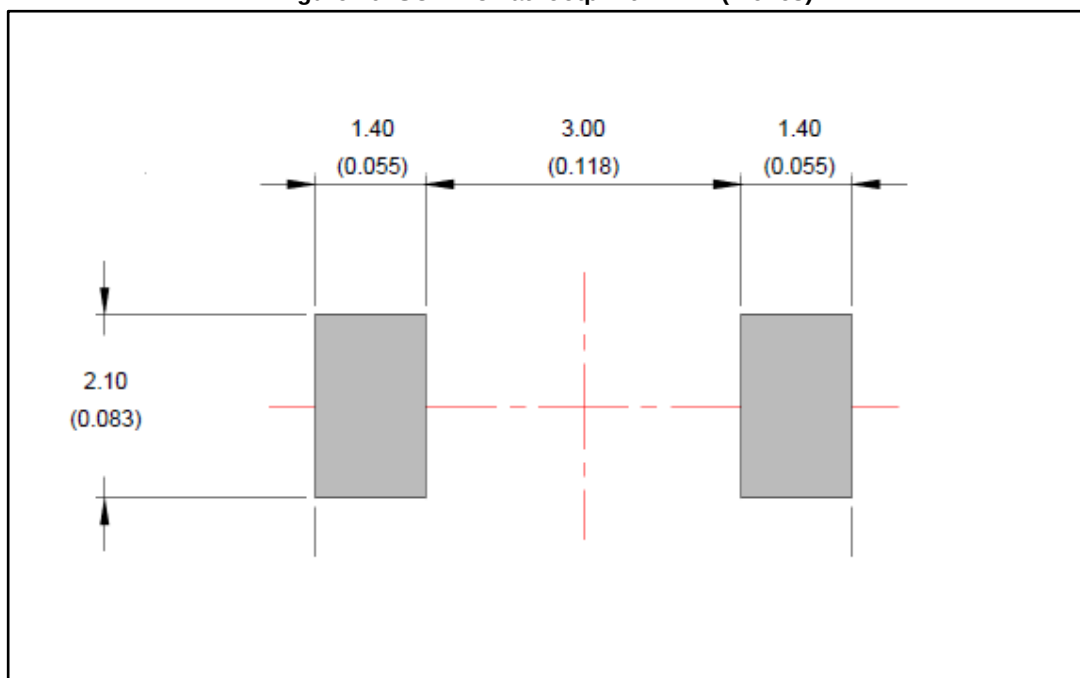


Table 5: SOD128Flat package mechanical data

Ref.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.93	1.03	0.037	0.041
b	1.69	1.81	0.067	0.071
c	0.10	0.22	0.004	0.009
D	2.30	2.50	0.091	0.098
E	4.60	4.80	0.181	0.189
E1	3.70	3.90	0.146	0.154
L	0.55	0.85	0.026	0.033
L1	0.30 typ.		0.012 typ.	
L2	0.45 typ.		0.018 typ.	

Figure 10: SOD128Flat footprint in mm (inches)



3 Ordering information

Table 6: Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STPS5H100AF	5H100	SOD128Flat	26.4 mg	3000	Tape and reel

4 Revision history

Table 7: Document revision history

Date	Revision	Changes
09-Jan-2017	1	Initial release.

IMPORTANT NOTICE – PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2017 STMicroelectronics – All rights reserved