

Stealth II Rectifier

30 A, 600 V

FFH30S60S

Description

The FFH30S60S is stealth2 rectifier with soft recovery characteristics. It is silicon nitride passivated ion-implanted epitaxial planar construction.

This device is intended for use as freewheeling of boost diode in switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

Features

- High Speed Switching, $t_{rr} < 35 \text{ ns}$ @ $I_F = 30 \text{ A}$
- High Reverse Voltage and High Reliability
- This Device is Pb-Free and is RoHS Compliant

Applications

- General Purpose
- Switching Mode Power Supply
- Boost Diode in Continuous Mode Power Factor Corrections
- Power Switching Circuits

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	600	V
Working Peak Reverse Voltage	V_{RWM}	600	V
DC Blocking Voltage	V_R	600	V
Average Rectified Forward Current @ $T_C = 102^\circ\text{C}$	$I_{F(AV)}$	30	A
Non-Repetitive Peak Surge Current 60 Hz Single Half-Sine Wave	I_{FSM}	300	A
Operating and Storage Temperature Range	T_J, T_{STG}	-65 to +175	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

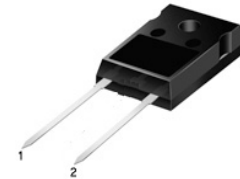
THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Maximum Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.1	$^\circ\text{C/W}$



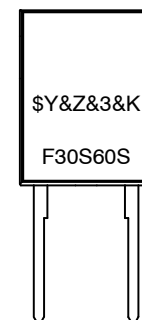
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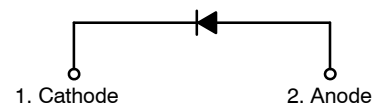


TO-247
2 LEAD
CASE 340CL

MARKING DIAGRAM



\$Y = ON Semiconductor Logo
&Z = Assembly Plant Code
&3 = Numeric Date Code
&K = Lot Code
F30S60S = Specific Device Code



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

PACKAGE MARKING AND ORDERING INFORMATION

Device	Device Marking	Package	Reel Size	Tape Width	Quantity
FFH30S60STU	F30S60S	TO-247-2L	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{FM} (Note 1)	$I_F = 30\text{ A}$ $I_F = 30\text{ A}$	$T_C = 25^\circ\text{C}$ $T_C = 125^\circ\text{C}$	–	2.1 1.6	2.6 –	V
I_{RM} (Note 1)	$V_R = 600\text{ V}$ $V_R = 600\text{ V}$	$T_C = 25^\circ\text{C}$ $T_C = 125^\circ\text{C}$	–	–	100 500	μA
t_{rr}	$I_F = 1\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	$T_C = 25^\circ\text{C}$	–	25.2	30	ns
t_{rr} I_{rr} S factor Q_{rr}	$I_F = 30\text{ A}$, $di/dt = 200\text{ A}/\mu\text{s}$, $V_R = 390\text{ V}$	$T_C = 25^\circ\text{C}$	– – – –	26 2.4 0.9 43	– – – –	ns A nC
t_{rr} I_{rr} S factor Q_{rr}	$I_F = 30\text{ A}$, $di/dt = 200\text{ A}/\mu\text{s}$, $V_R = 390\text{ V}$	$T_C = 125^\circ\text{C}$	– – – –	75.1 6.3 0.9 238	– – – –	ns A nC
W_{AVL}	Avalanche Energy ($L = 40\text{ mH}$)		7.2	–	–	mJ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = 300 μs , Duty Cycle = 2%

TEST CIRCUIT AND WAVEFORMS

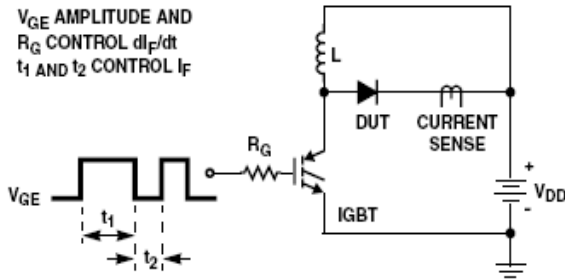


Figure 1. t_{rr} Test Circuit

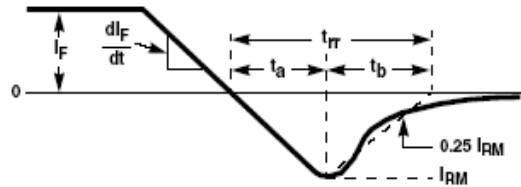


Figure 2. t_{rr} Waveforms and Definitions

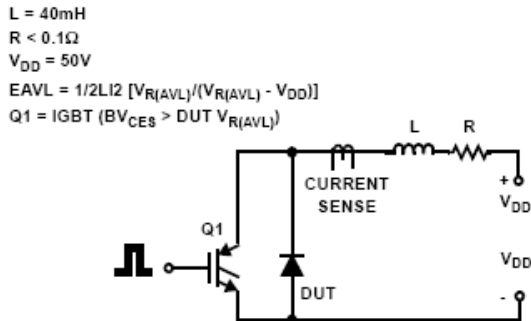


Figure 3. Avalanche Energy Test Circuit

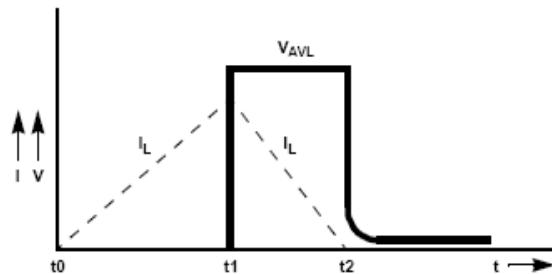


Figure 4. Avalanche Current and Voltage Definitions

TYPICAL PERFORMANCE CHARACTERISTICS

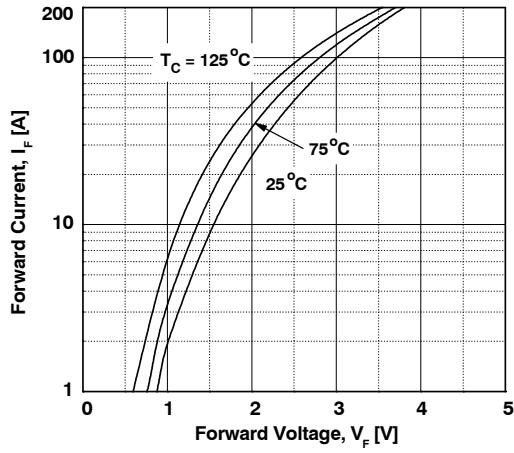


Figure 5. Typical Forward Voltage Drop vs. Forward Current

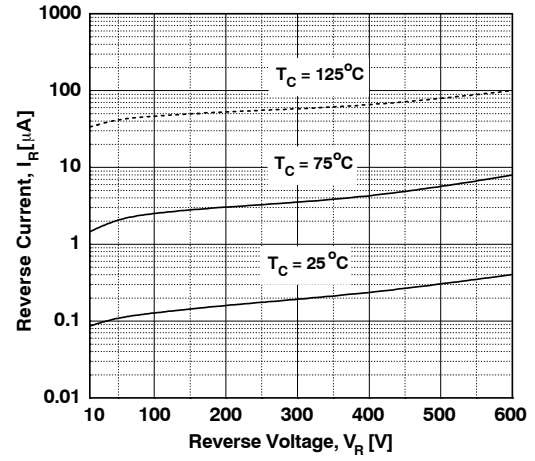


Figure 6. Typical Reverse Current vs. Reverse Voltage

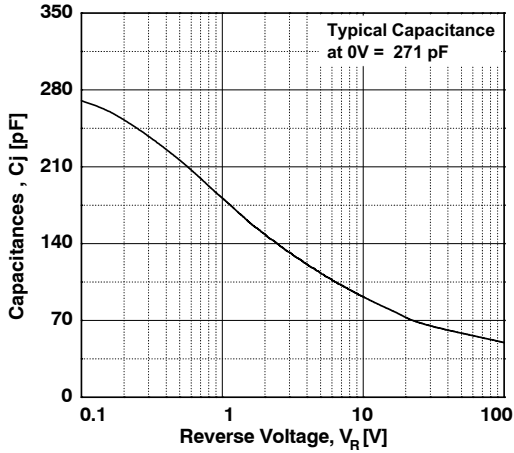


Figure 7. Typical Junction Capacitance

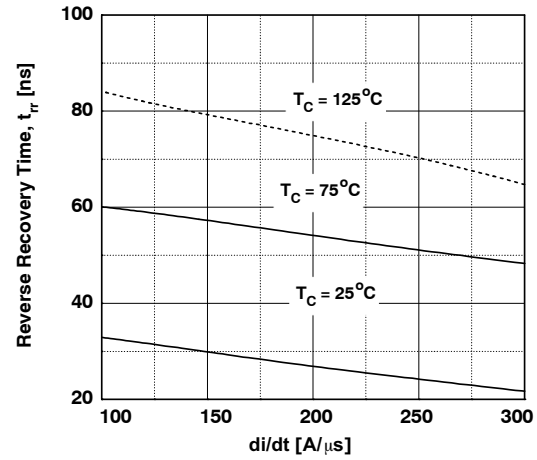


Figure 8. Typical Reverse Recovery Time vs. di/dt

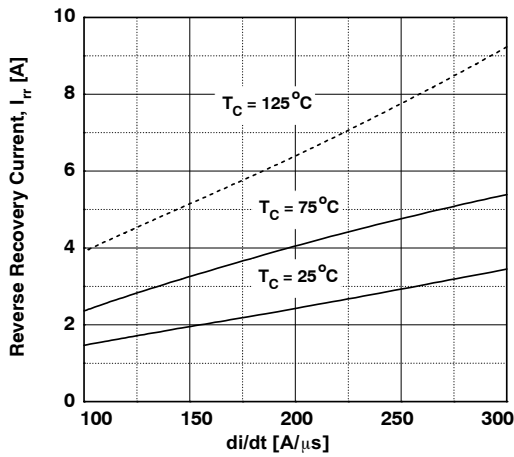


Figure 9. Typical Reverse Recovery Current vs. di/dt

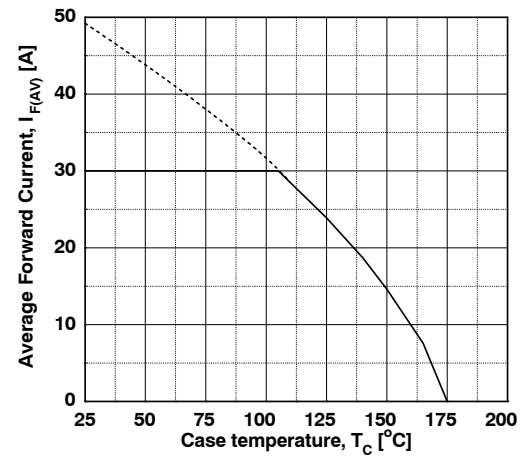


Figure 10. Forward Current Derating Curve

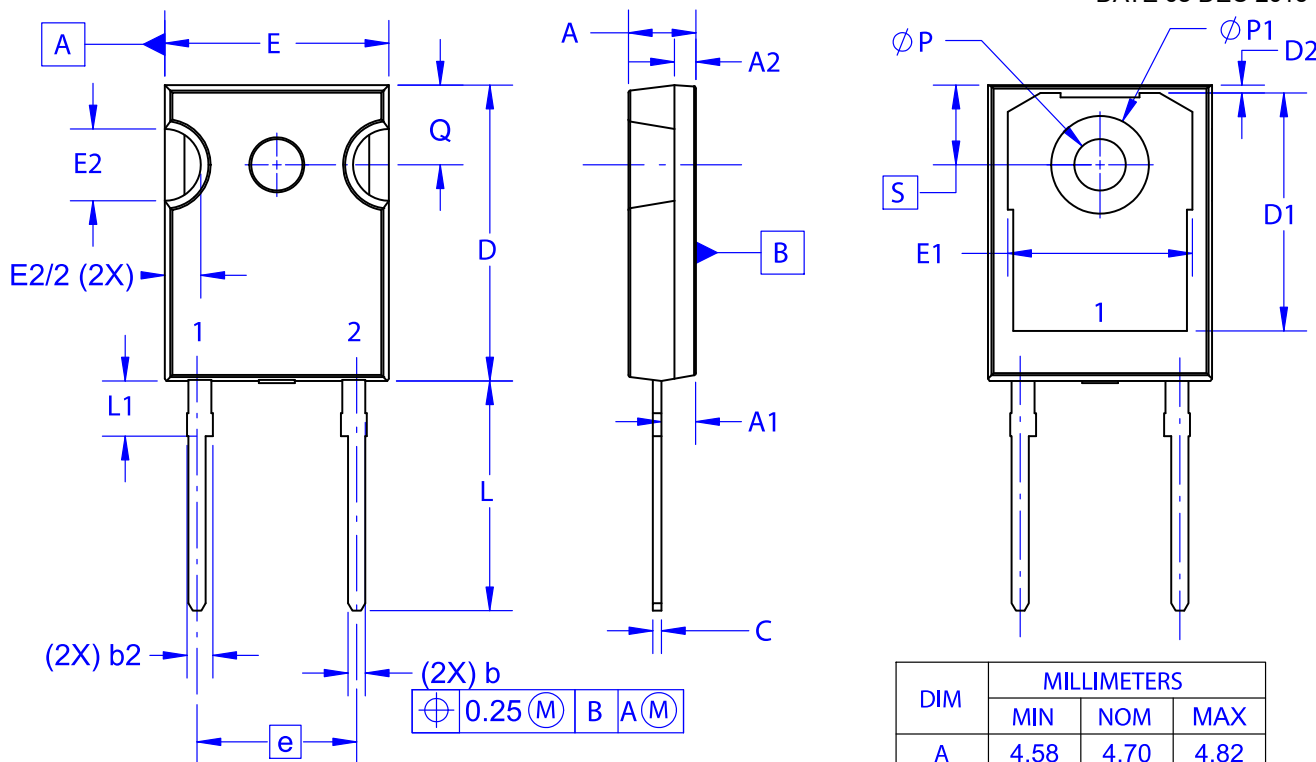
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®

ON

TO-247-2LD
CASE 340CL
ISSUE A

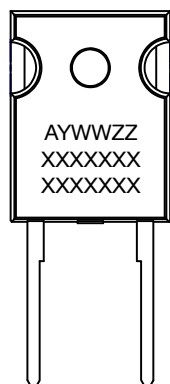
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NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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