



ON Semiconductor

Test Procedure for the NCP1622GEVB Evaluation Board

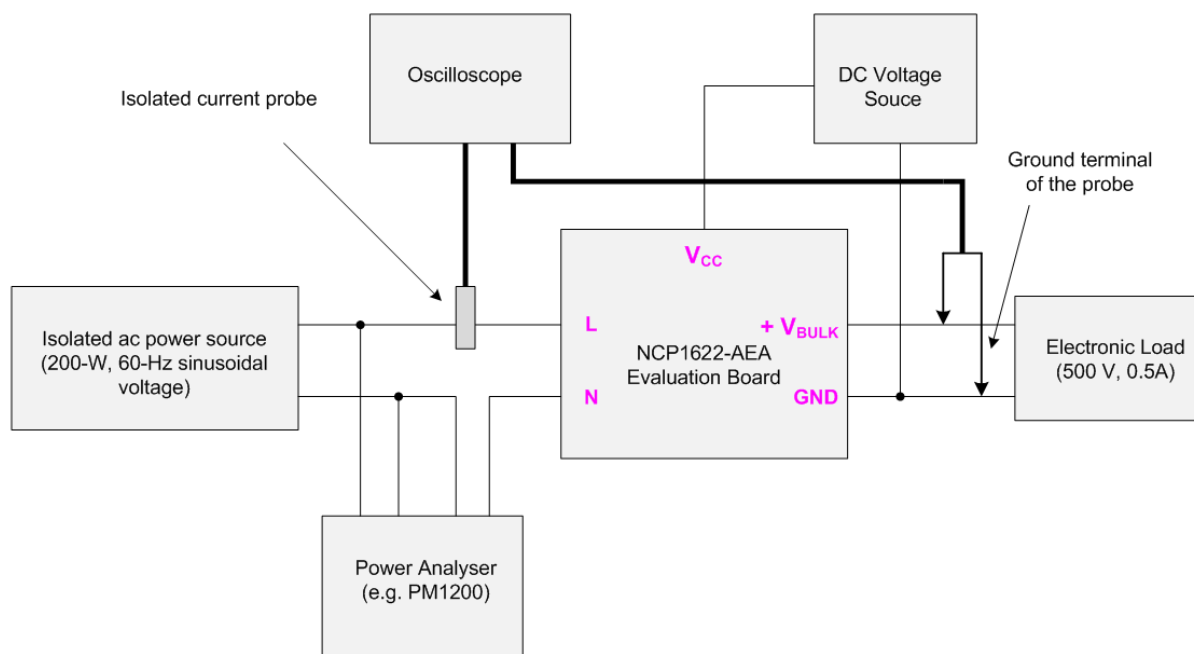


Figure 1. Set-up for board testing

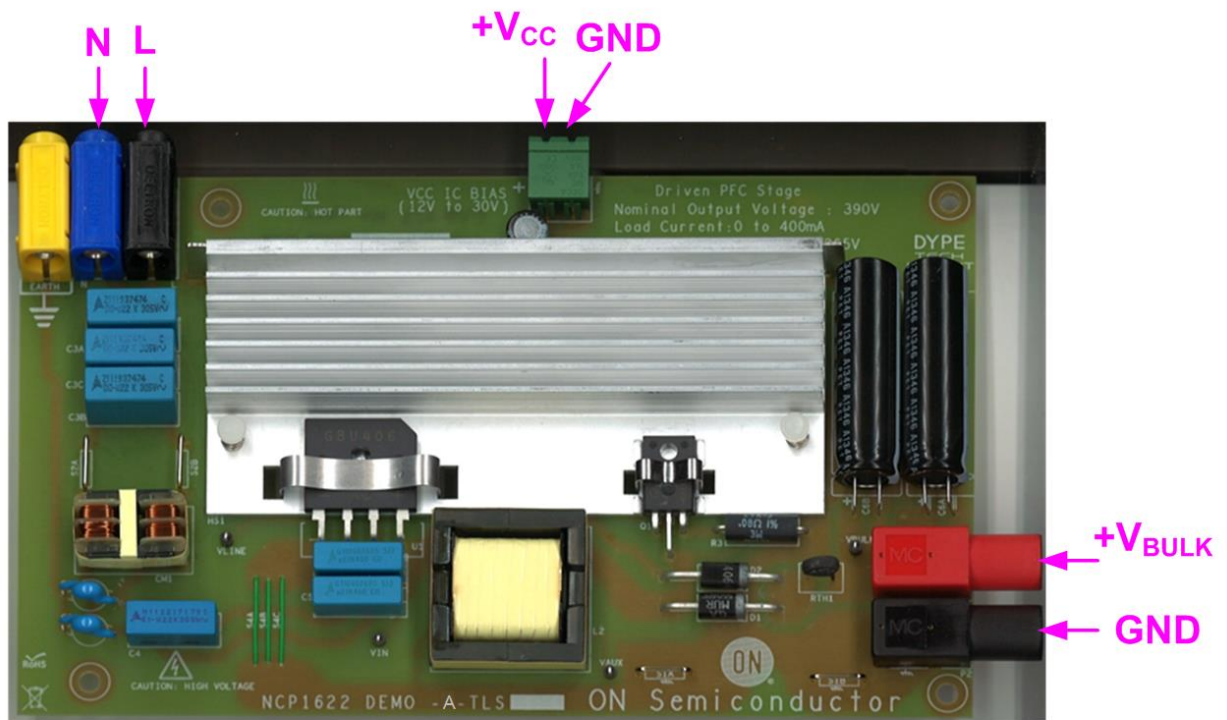


Figure 2. Tested Evaluation Board

Equipments for measurement

The board testing set-up is shown in Figure 1 and the tested board in Figure 2.

- Apply an electronic load across the output (between the “V_{BULK}” and “GND” terminals of the board). This equipment will adjust the current I_{LOAD} that loads the evaluation board.
- Apply a 200-W or more, 60-Hz, isolated ac power source to the “L” and “N” inputs of the evaluation board. This source will adjust the sinusoidal input voltage, V_{in}, that is applied to the evaluation board. The rms value of V_{in} must stay below 265 V.
- Apply a 20-V dc source with a 50-mA clamp current between the V_{CC} and GND terminals of the evaluation board.
- Place a power analyzer able to measure the power factor (“PF”) and the Total Harmonic Distortion (“THD”) of the current absorbed from the ac power source.
- Place an oscilloscope current probe on the wire connected to the “L” input of the evaluation board to monitor the input current waveform.

Measurements

- VBULK,PF and THD measurements

Parameters	Comments	Limits
V _{in,rms} = 115 V, I _{LOAD} = 0.1 A		
V _{BULK}	Voltage measured between “V _{BULK} ” and “GND”	390 V < V _{BULK} < 410 V
PF	Power Factor	> 0.930
THD	Total Harmonic Distortion	< 25%
V _{in,rms} = 115 V, I _{LOAD} = 0.4 A		
V _{BULK}	Voltage measured between “V _{BULK} ” and “GND”	390 V < V _{BULK} < 410 V
PF	Power Factor	> 0.990
THD	Total Harmonic Distortion	< 15%
V _{in,rms} = 230 V, I _{LOAD} = 0.4 A		
V _{BULK}	Voltage measured between “V _{BULK} ” and “GND”	390 V < V _{BULK} < 410 V
PF	Power Factor	> 0.950
THD	Total Harmonic Distortion	< 20%

Static OVP

Observe that when the I_{LOAD} is reduced to zero, the V_{BULK} node is still regulating within the 390-410-V range

WARNING

The board contains high-voltage and hot, live parts. It should only be handled by experienced power supply professionals. Be very cautious when manipulating or testing it. It is the responsibility of the boards users to take all the precautions to avoid electric hazards and any other pains.
