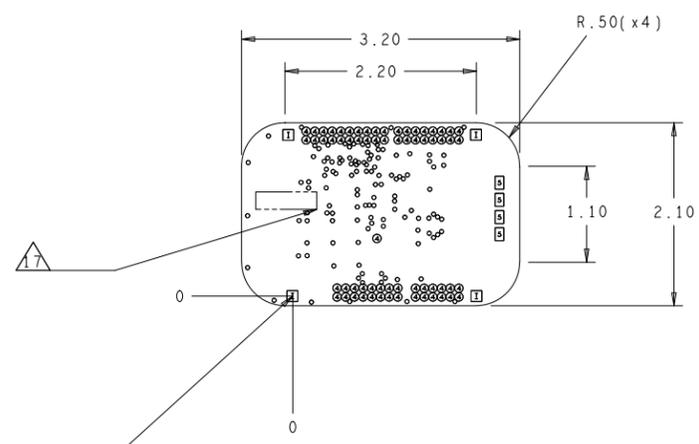


NOTES (UNLESS OTHERWISE SPECIFIED):

- THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).
- THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 260 DEGREES CELSIUS FOR 10 SECONDS.
- BASE MATERIAL - LAMINATE AND PREPREG SHALL MEET IPC-4101B-26, 83 or 98
T_g - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.
T_d - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.
- COPPER FOIL WEIGHT - SEE STACKUP DETAIL 'A'
- CHARACTERISTIC IMPEDANCE - NONE
- MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .005"/.005"
- PLATING FINISH - BOTH SIDES ENIG (ELECTROLESS NICKEL IMMERSION GOLD):
.05080-.232 MICRON (2-8 MICROINCH) OF GOLD OVER
2.540-6.350 MICRON (100-250 MICROINCH) OF NICKEL.
- ALL THROUGH HOLE VIAS MAY BE PLATED SHUT.
- SOLDERMASK - GREEN COLOR, BOTH SIDES.
MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM NXP
- SILKSCREEN - WHITE EPOXY INK, BOTH SIDES. NO SILK ON PADS.
- ELECTRICAL TEST - 100% IPCD356.
- PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED.
- DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS,
UNLESS PRIOR APPROVAL IS GIVEN IN WRITING BY NXP.
- TEARDROPS MAYBE ADDED AT THE FAB HOUSE TO ALL SIGNAL LAYERS.
- 2 SOLDER SAMPLES TO BE PROVIDED.
- BASIC GRID INCREMENT AT 1:1 IS .0001.
- SUPPLIER MARKINGS - ON SOLDER SIDE ONLY, WHERE SHOWN.
- MUST BE UL RECOGNIZED AND MUST HAVE AN ID THAT CONFORMS TO UL94V-0
- THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP (P6)
- THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C)
- ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP.
ALL HOLE LOCATION TOLERANCES ARE TO BE +/- .002 IN REFERENCE TO THE PRIMARY DATUM.
- FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURERS REQUIREMENTS.
THE ADDITION OF RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER. PANELIZATION MUST BE APPROVED BY CONTRACT MANUFACTURER.

| REVISIONS | | | | |
|-----------|-----|------------------|----------|----------|
| ZONE | REV | DESCRIPTION | DATE | APPROVED |
| | A | ORIGINAL RELEASE | 09-04-14 | A M. |
| | A1 | NXP LOGO UPDATED | 08-19-16 | A M. |
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |



| DRILL CHART: TOP to BOTTOM | | | | |
|----------------------------|-------|------------|------------|-----|
| ALL UNITS ARE IN MILS | | | | |
| FIGURE | SIZE | TOLERANCE | PLATED | QTY |
| ⊙ | 10.0 | +0.0/-10.0 | PLATED | 121 |
| ⊗ | 40.0 | +3.0/-3.0 | PLATED | 65 |
| □ | 51.0 | +3.0/-3.0 | PLATED | 4 |
| □ | 125.0 | +3.0/-3.0 | NON-PLATED | 4 |

PRIMARY DATUM
GRID ORIGIN

FINISHED Cu WEIGHT

| | | |
|--|------------------------|-------|
| | LAYER 1 COMPONENT SIDE | 1 oz |
| | LAYER 2 SOLDER SIDE | 1 oz. |

DETAIL A
LAYER STACKUP
SCALE: NONE

| | | | | |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------|---------------------------------------------------------------------------------------------------------|
| PART NO. 170-28028 | | NXP SEMICONDUCTORS | | |
| <input type="checkbox"/> COMPANY PUBLIC <input checked="" type="checkbox"/> COMPANY INTERNAL <input type="checkbox"/> COMPANY CONFIDENTIAL | | THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO NXP AND SHALL NOT BE USED FOR ENGINEERING DESIGN PROCUREMENT OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF NXP. 6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 USA | | |
| UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: DECIMALS .XX .01 .XXX .005 ANGLES D-30° ✓ RMS ALL MACHINED SURFACES BREAK ALL SHARP EDGES AND CORNERS. REMOVE BURRS. UNDERLINED DIM. NOT TO SCALE. THIRD ANGLE ORTHOGRAPHIC PROJECTION IS USED. | | APPROVALS LAURE PORTAL ANDREW MacDonald LAURE PORTAL | DATE 08-19-16 08-19-16 08-19-16 | TITLE PRINTED WIRING BOARD KIT33664EVB SIZE LAY-28028 DWG. NO. FAB-28028 REV A1 |
| SCALE 1/1 | | DO NOT SCALE DRAWING | | SHEET 1 OF 1 |