Microsemi
POWER PRODUCTS GROUP

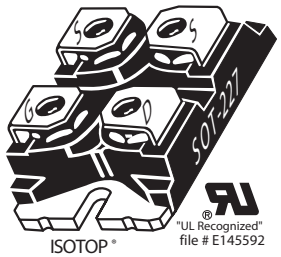
Power MOS 8 Cooler!

APT51F50J

500V, 51A, 0.075Ω Max, t_{rr} ≤310ns

N-Channel FREDFET

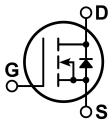
Power MOS 8™ is a high speed, high voltage N-channel switch-mode power MOSFET. This 'FREDFET' version has a drain-source (body) diode that has been optimized for high reliability in ZVS phase shifted bridge and other circuits through reduced t_{rr}, soft recovery, and high recovery dv/dt capability. Low gate charge, high gain, and a greatly reduced ratio of C_{rss}/C_{iss} result in excellent noise immunity and low switching loss. The intrinsic gate resistance and capacitance of the poly-silicon gate structure help control di/dt during switching, resulting in low EMI and reliable paralleling, even when switching at very high frequency.



ISOTOP®
"UL Recognized" file # E145592

APT51F50J

Single die FREDFET



FEATURES

- **Fast switching with low EMI**
- **Low t_{rr} for high reliability**
- **Ultra low C_{rss} for improved noise immunity**
- **Low gate charge**
- **Avalanche energy rated**
- **RoHS compliant**

TYPICAL APPLICATIONS

- **ZVS phase shifted and other full bridge**
- **Half bridge**
- **PFC and other boost converter**
- **Buck converter**
- **Single and two switch forward**
- **Flyback**

Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
I_D	Continuous Drain Current @ T_C = 25°C	51	A
	Continuous Drain Current @ T_C = 100°C	32	
I_{DM}	Pulsed Drain Current ①	230	
V_{GS}	Gate-Source Voltage	±30	V
E_{AS}	Single Pulse Avalanche Energy ②	1580	mJ
I_{AR}	Avalanche Current, Repetitive or Non-Repetitive	37	A

Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Typ	Max	Unit
P_D	Total Power Dissipation @ T_C = 25°C			480	W
$R_{\theta JC}$	Junction to Case Thermal Resistance			0.26	°C/W
$R_{\theta CS}$	Case to Sink Thermal Resistance, Flat, Greased Surface		0.15		
T_J,T_{STG}	Operating and Storage Junction Temperature Range	-55		150	°C
$V_{Isolation}$	RMS Voltage (50-60hHz Sinusoidal Waveform from Terminals to Mounting Base for 1 Min.)	2500			V
W_T	Package Weight		1.03		oz
			29.2		g
Torque	Terminals and Mounting Screws.			10	in·lbf
				1.1	N·m

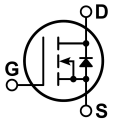
Static Characteristics T_J = 25°C unless otherwise specified APT51F50J

Symbol	Parameter	Test Conditions		Min	Typ	Max	Unit
$V_{BR(DSS)}$	Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250µA		500			V
$\Delta V_{BR(DSS)}/\Delta T_J$	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I_D = 250µA			0.60		V/°C
$R_{DS(on)}$	Drain-Source On Resistance③	V_{GS} = 10V, I_D = 37A			0.064	0.075	Ω
$V_{GS(th)}$	Gate-Source Threshold Voltage	V_{GS} = V_{DS}, I_D = 2.5mA		2.5	4	5	V
$\Delta V_{GS(th)}/\Delta T_J$	Threshold Voltage Temperature Coefficient				-10		mV/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 500V	T_J = 25°C			250	µA
		V_{GS} = 0V	T_J = 125°C			1000	
I_{GSS}	Gate-Source Leakage Current	V_{GS} = ±30V				±100	nA

Dynamic Characteristics T_J = 25°C unless otherwise specified

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
g_{fs}	Forward Transconductance	V_{DS} = 50V, I_D = 37A		55		S
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = 25V f = 1MHz		11600		pF
C_{rss}	Reverse Transfer Capacitance			160		
C_{oss}	Output Capacitance			1250		
$C_{o(cr)}$ ④	Effective Output Capacitance, Charge Related	V_{GS} = 0V, V_{DS} = 0V to 333V		725		
$C_{o(er)}$ ⑤	Effective Output Capacitance, Energy Related			365		
Q_g	Total Gate Charge	V_{GS} = 0 to 10V, I_D = 37A, V_{DS} = 250V		290		nC
Q_{gs}	Gate-Source Charge			65		
Q_{gd}	Gate-Drain Charge			130		
$t_{d(on)}$	Turn-On Delay Time	**Resistive Switching** V_{DD} = 333V, I_D = 37A R_G = 2.2Ω⑥, V_{GG} = 15V		45		ns
t_r	Current Rise Time			55		
$t_{d(off)}$	Turn-Off Delay Time			120		
t_f	Current Fall Time			39		

Source-Drain Diode Characteristics

Symbol	Parameter	Test Conditions		Min	Typ	Max	Unit
I_S	Continuous Source Current (Body Diode)	MOSFET symbol showing the integral reverse p-n junction diode (body diode) 				51	A
I_{SM}	Pulsed Source Current (Body Diode)①					230	
V_{SD}	Diode Forward Voltage	I_{SD} = 37A, T_J = 25°C, V_{GS} = 0V				1.2	V
t_{rr}	Reverse Recovery Time	I_{SD} = 37A③ V_{DD} = 100V di_{SD}/dt = 100A/µs	T_J = 25°C			310	ns
			T_J = 125°C			570	
Q_{rr}	Reverse Recovery Charge		T_J = 25°C		1.48		µC
			T_J = 125°C		3.85		
I_{rrm}	Reverse Recovery Current		T_J = 25°C		11.3		A
			T_J = 125°C		16.6		
dv/dt	Peak Recovery dv/dt	I_{SD} ≤ 37A, di/dt ≤1000A/µs, V_{DD} = 333V, T_J = 125°C				20	V/ns

① Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.

② Starting at T_J = 25°C, L = 2.31mH, R_G = 25Ω, I_{AS} = 37A.

③ Pulse test: Pulse Width < 380µs, duty cycle < 2%.

④ $C_{o(cr)}$ is defined as a fixed capacitance with the same stored charge as C_{OSS} with V_{DS} = 67% of $V_{(BR)DSS}$.

⑤ $C_{o(er)}$ is defined as a fixed capacitance with the same stored energy as C_{OSS} with V_{DS} = 67% of $V_{(BR)DSS}$. To calculate $C_{o(er)}$ for any value of V_{DS} less than $V_{(BR)DSS,}$ use this equation: $C_{o(er)}$ = -1.65E-7/V_{DS}^2 + 5.51E-8/V_{DS} + 2.03E-10.

⑥ R_G is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)

Microsemi reserves the right to change, without notice, the specifications and information contained herein.

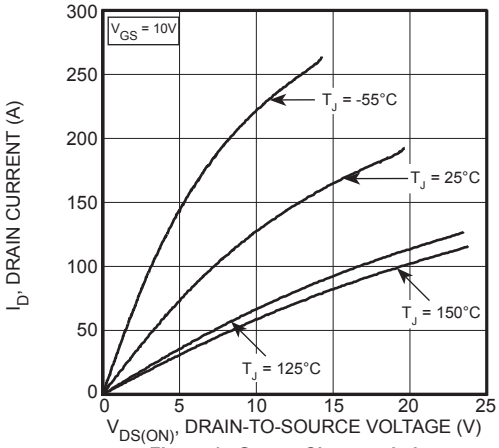


Figure 1, Output Characteristics

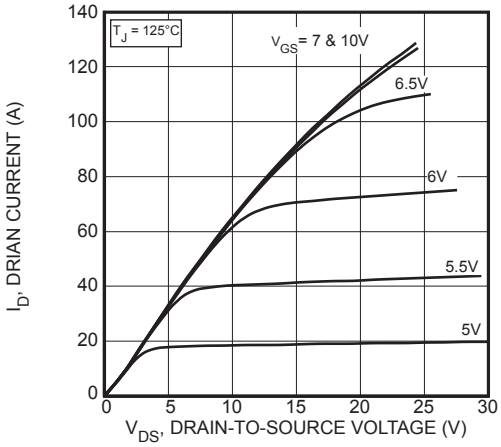


Figure 2, Output Characteristics

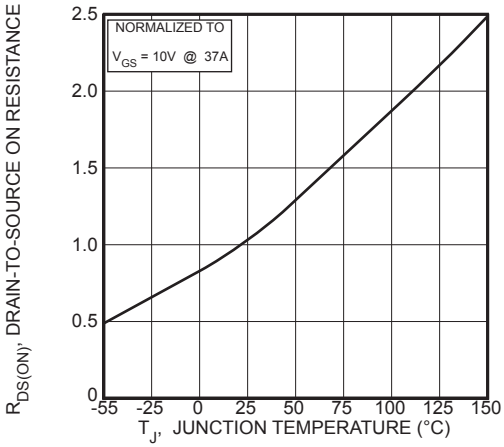


Figure 3, $R_{DS(ON)}$ vs Junction Temperature

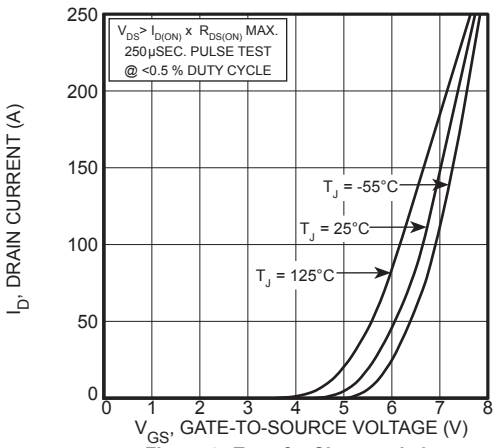


Figure 4, Transfer Characteristics

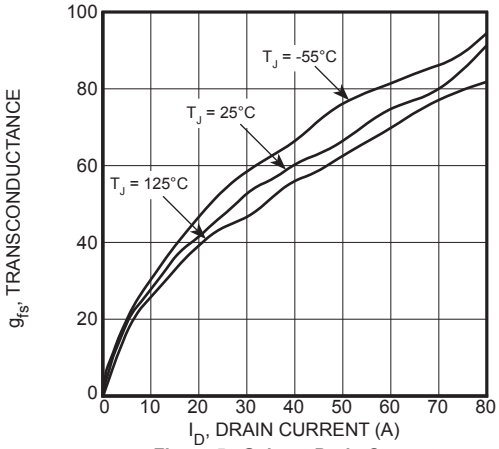


Figure 5, Gain vs Drain Current

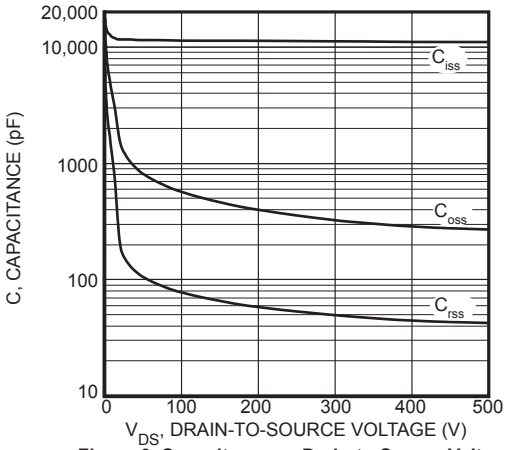


Figure 6, Capacitance vs Drain-to-Source Voltage

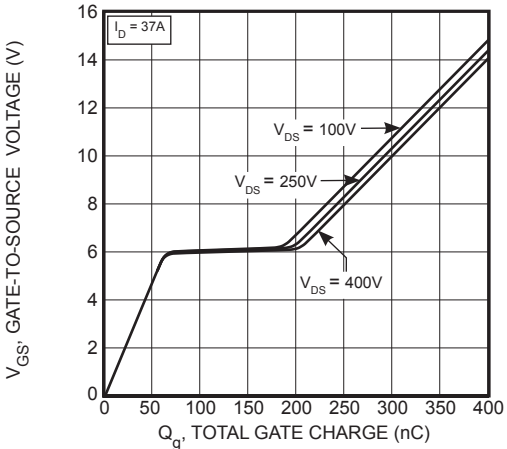


Figure 7, Gate Charge vs Gate-to-Source Voltage

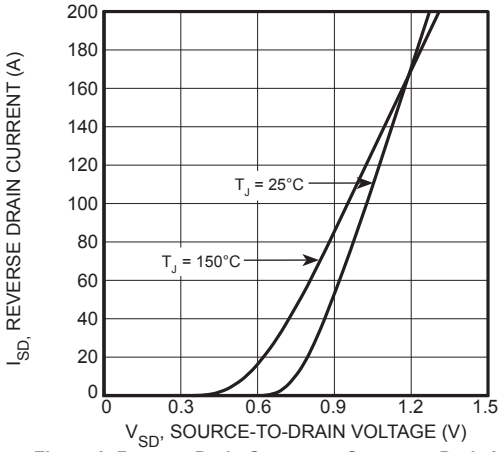


Figure 8, Reverse Drain Current vs Source-to-Drain Voltage

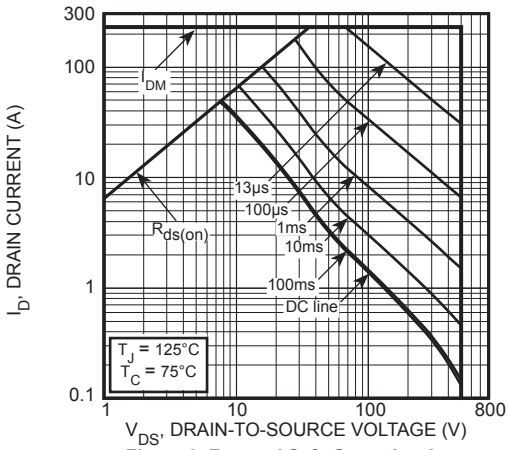


Figure 9, Forward Safe Operating Area

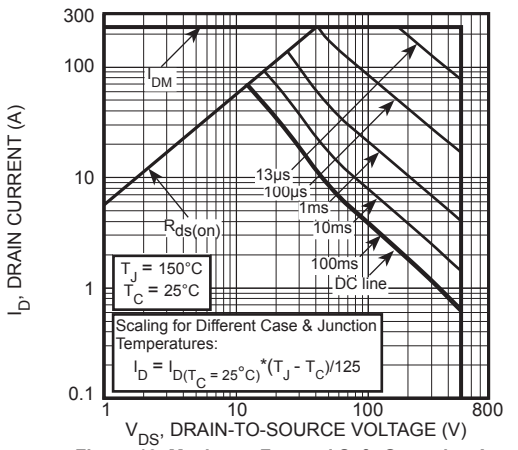


Figure 10, Maximum Forward Safe Operating Area

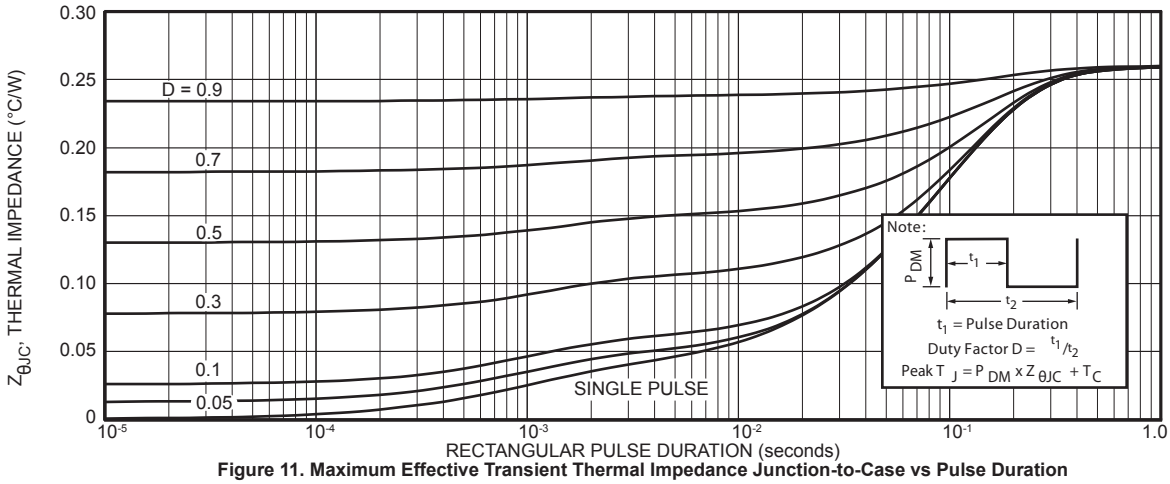
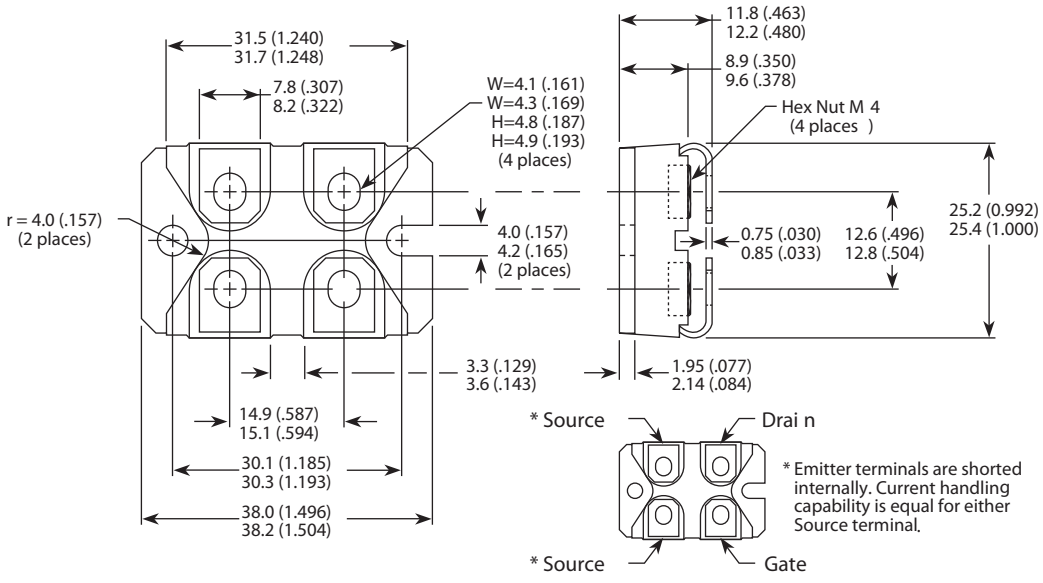


Figure 11. Maximum Effective Transient Thermal Impedance Junction-to-Case vs Pulse Duration

SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters and (Inches)