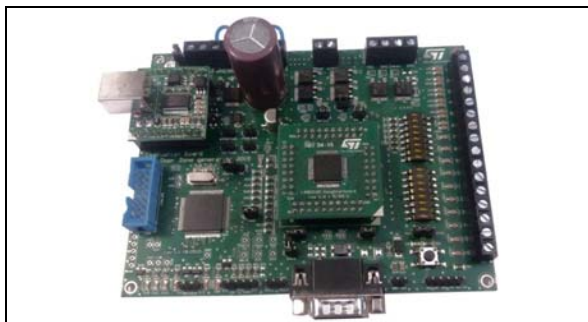


L99DZ100G, L99DZ120 Evaluation Board



Data brief

Features

- 1 half bridge for 7.5 A load ($R_{ON} = 100\text{ m}\Omega$)
- 1 half bridge for 7.5 A load ($R_{ON} = 150\text{ m}\Omega$)
- 2 half bridges for 0.5 A load ($R_{ON} = 2000\text{ m}\Omega$) (not present in L99DZ120)
- 2 half bridges for 3 A load ($R_{ON} = 300\text{ m}\Omega$)
- 1 configurable high-side driver for up to 1.5 A ($R_{ON} = 500\text{ m}\Omega$) or 0.35 A ($R_{ON} = 1600\text{ m}\Omega$) load
- 1 configurable high-side driver for 0.8 A ($R_{ON} = 800\text{ m}\Omega$) or 0.35 A ($R_{ON} = 1600\text{ m}\Omega$) load
- 3 configurable high-side drivers for 0.15 A/0.35 A ($R_{ON} = 2\text{ }\Omega$)
- 1 configurable high-side driver for 0.25 A/0.5 A ($R_{ON} = 2\text{ }\Omega$) to supply EC Glass MOSFET (not present in L99DZ120)
- 4 configurable HS drivers for 0.15A/0.25A ($R_{ON} = 5\text{ }\Omega$)
- Internal 10bit PWM timer for each stand-alone HS driver
- Buffered supply for voltage regulators and 2 high-side drivers (OUT15 & OUT_HS / both P-channel) to supply e.g. external contacts
- Programmable soft-start function to drive loads with higher inrush currents as current limitation

value (for OUT1-6, OUT7, OUT8 and OUT_HS) with thermal expiration feature

- Control block for electro-chromic element (not present in L99DZ120)
- Two 5 V voltage regulators for microcontroller and peripheral supply
- Configurable window watchdog
- LIN 2.2a compliant (SAEJ2602 compatible) transceiver
- Advanced high speed CAN transceiver (ISO 11898-2:2003 /-5:2007 and SAE J2284 compliant) with local failure and bus failure diagnosis and selective wake-up functionality according to ISO 11898-6:2013 (not present in L99DZ120)
- Separated (Isolated) fail-safe block with 2 LS ($R_{ON} = 1\text{ m}\Omega$) to pull down the gates of the external HS MOSFETs
- Thermal clusters
- A/D conversion of supply voltages and internal temperature sensors
- Embedded and programmable VS duty cycle adjustment for LED driver outputs

Table 1. Device summary

Order code	Reference
EVAL-L99DZ100G	L99DZ100G Mother and Daughter Board
EVAL-D-L99DZ100G	L99DZ100G Daughter Board
EVAL-L99DZ120	L99DZ120 Mother and Daughter Board
EVAL-D-L99DZ120	L99DZ120 Daughter Board

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Figure 6. L99DZ100G Daughterboard bottom layer 10

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1 Description

The EVAL-L99DZ100G and EVAL-L99DZ120 are evaluation boards designed for Door Zone Applications. Both are composed by the same motherboard and they differentiate for the daughter board on which is pre-assembled the L99DZ100G in the EVAL-L99DZ100G case (conceived for front door application) or the L99DZ120 in the EVAL-L99DZ120 case (conceived for rear door applications).

The motherboard, based on STM8 microcontroller, provides the logic section for monitoring and driving the L99DZ100G or the L99DZ120 assembled in the daughterboard.

With aim of making the board usage and settings simpler, ST provides a dedicated and a user-friendly software with a Graphic User Interface (GUI). This enables the user to set L99DZ100G or L99DZ120 parameters and at the same time it shows real time device diagnostic information as the current output evolution, the battery voltage monitoring, the board temperature and much more.

2 Application schematics and layouts

2.1 L99DZ100G motherboard

Figure 1. L99DZ100G motherboard top layer

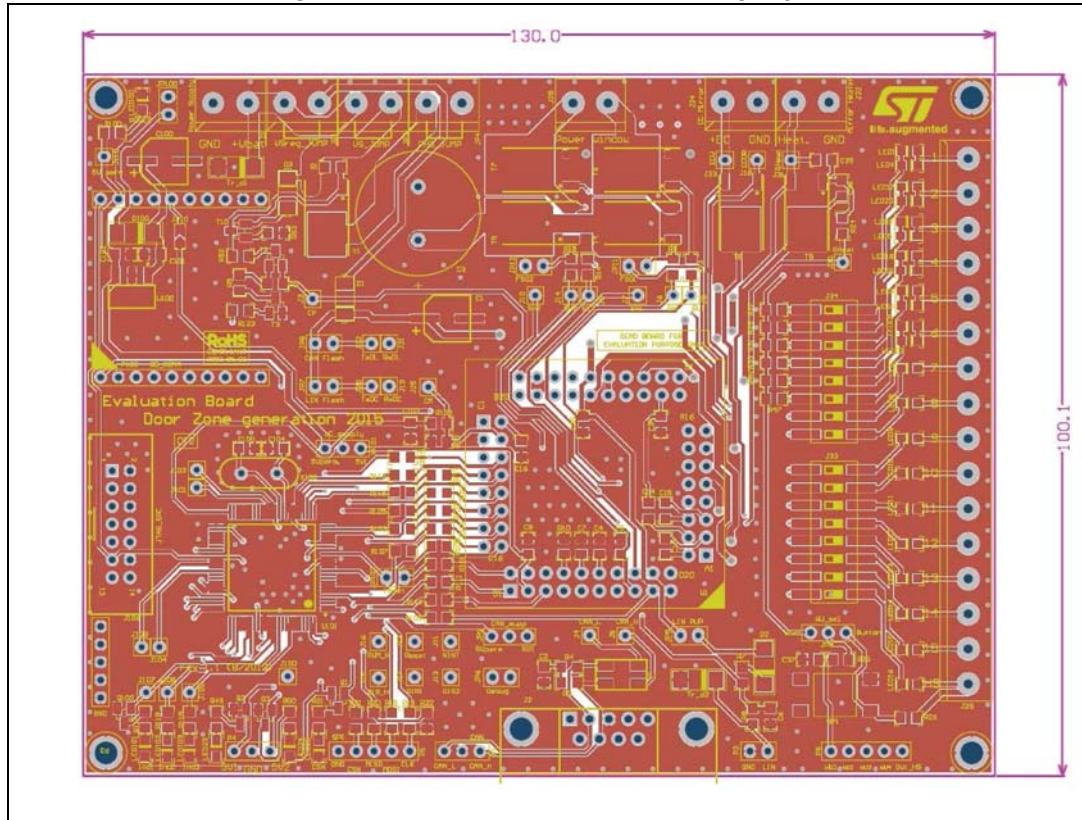
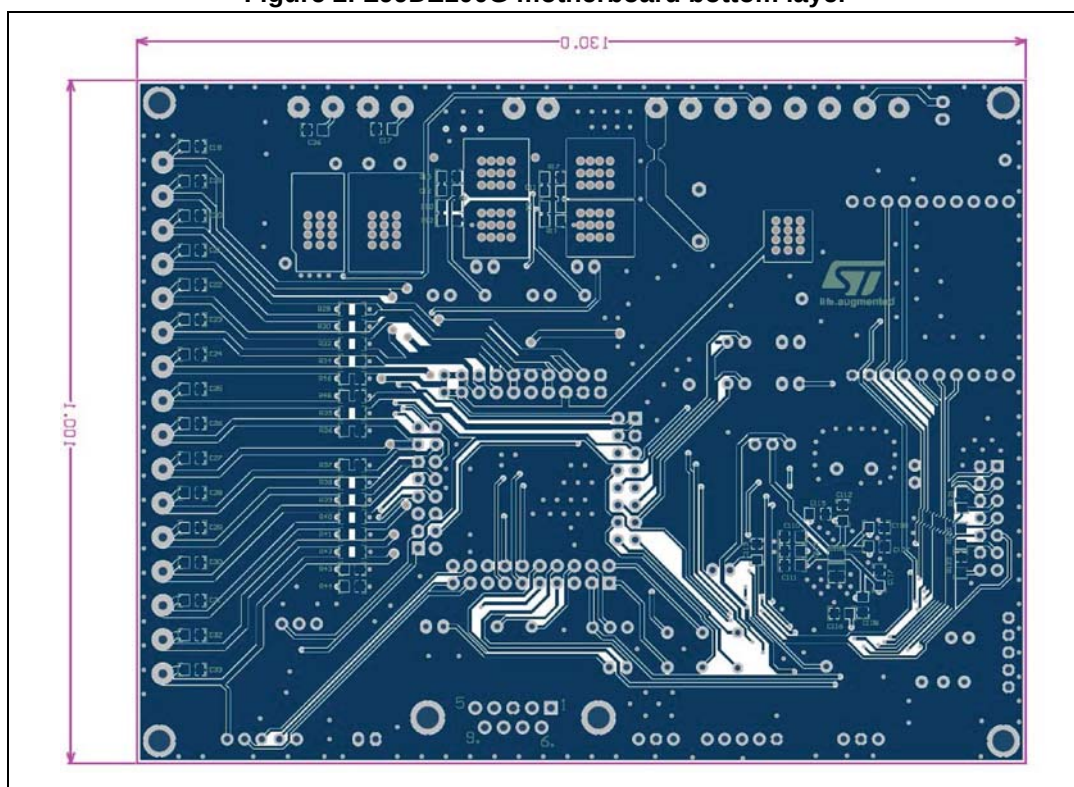
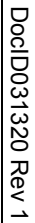


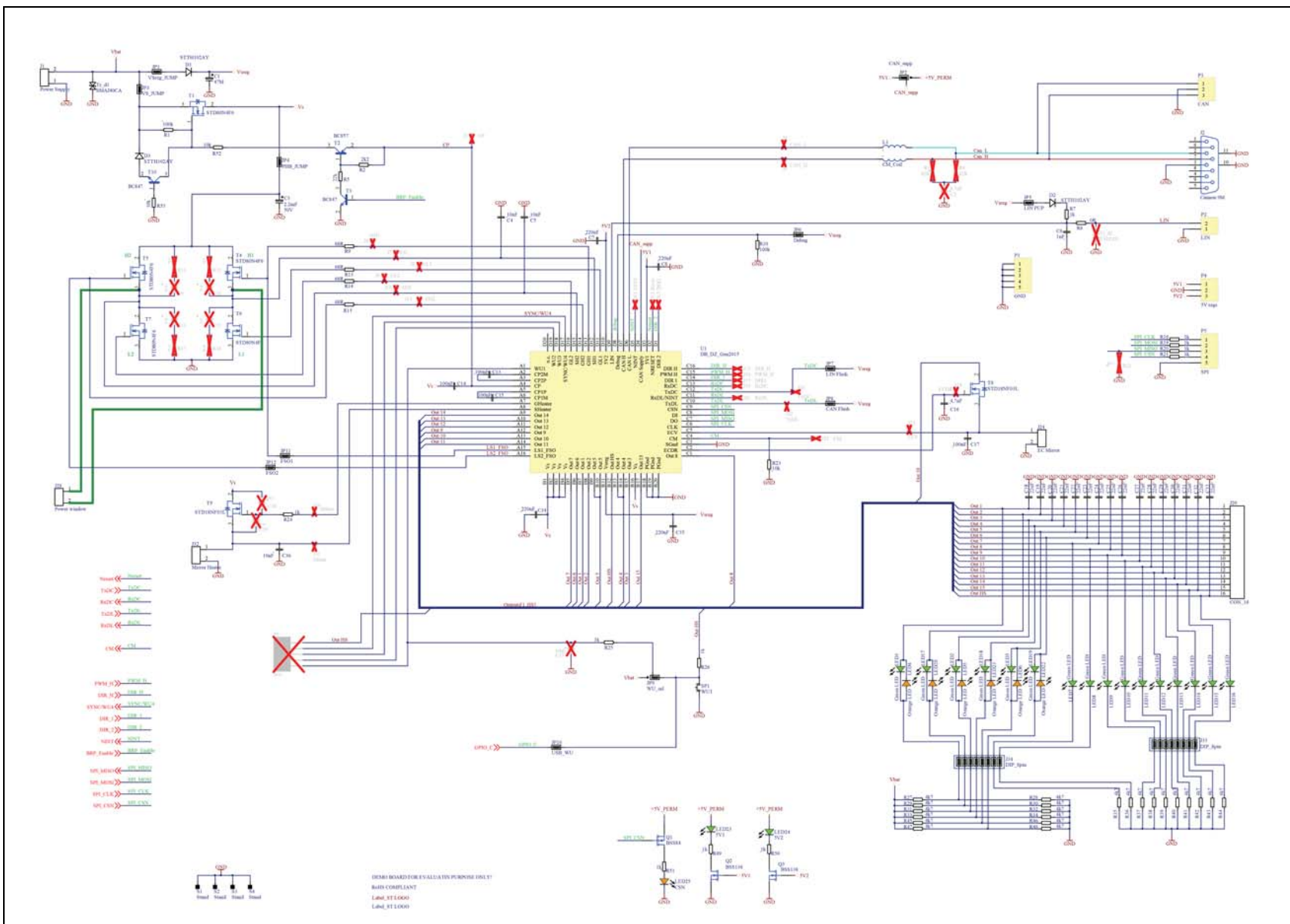
Figure 2. L99DZ100G motherboard bottom layer



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Application schematics and layouts



3 L99DZ100G daughterboard

Figure 5. L99DZ100G Daughterboard top layer

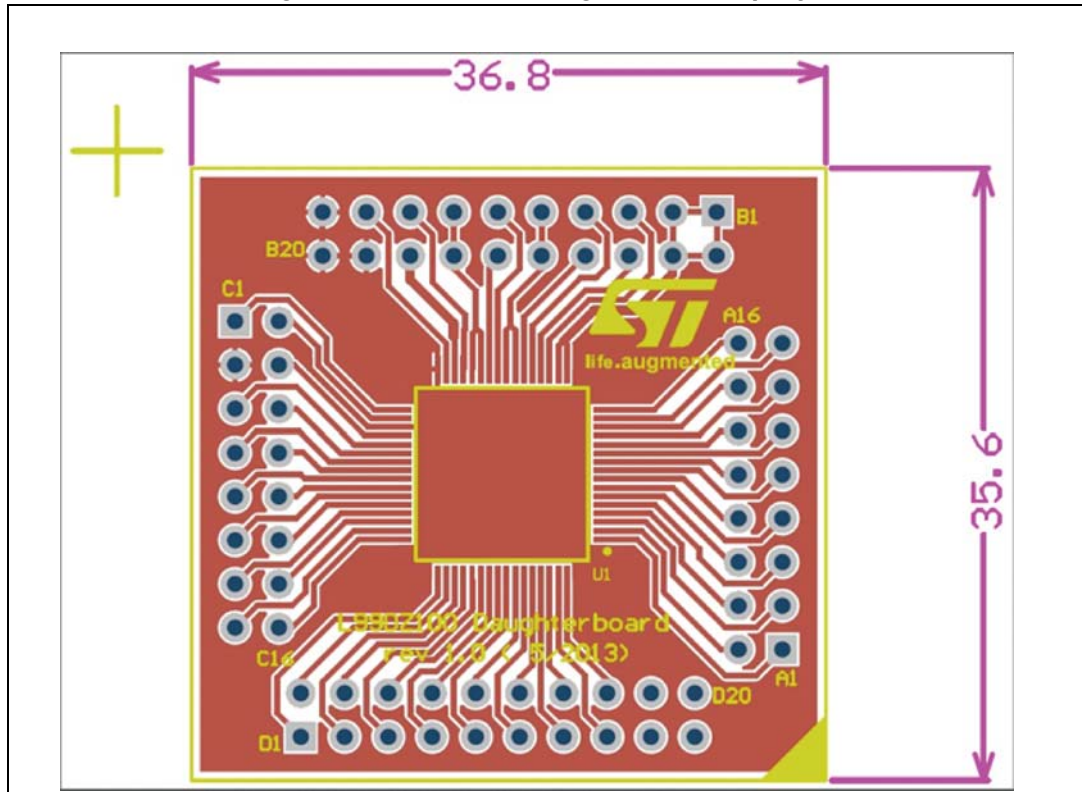


Figure 6. L99DZ100G Daughterboard bottom layer

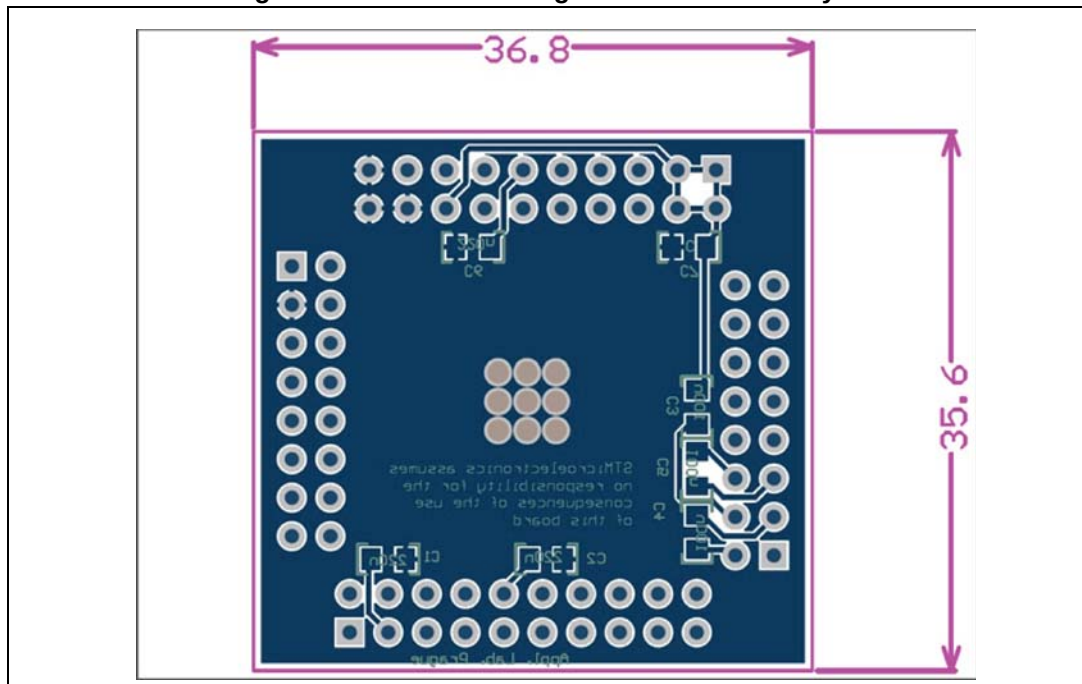
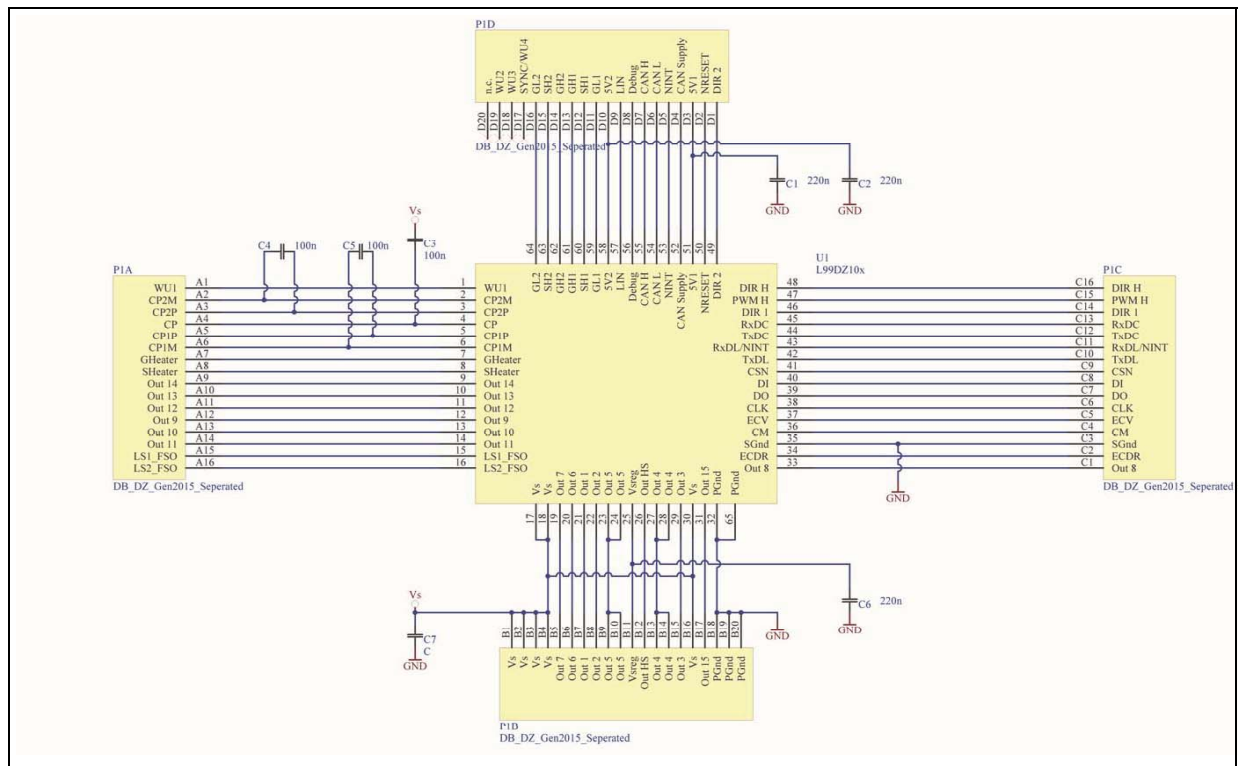


Figure 7. L99DZ100G Daughterboard typical application example



4 Demonstration software

A software GUI (Graphical User Interface) for easy demonstration of the L99DZ100G and L99DZ120 features is delivered on customer request.

4.1 System requirements

- Windows OS (XP, 7, 8)
- USB type B

5 Revision history

Table 2. Document revision history

Date	Revision	Changes
15-Dec-2017	1	Initial release.

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