

ADS5263EVM Evaluation Module

This user's guide gives an overview of the ADS5263EVM and describes how the evaluation module can be used to evaluate the performance, functions, and features of the ADS5263 device.

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1 Quick Look at the Evaluation Setup

Figure 1 shows an overview of the evaluation setup that includes the ADS5263EVM evaluation module (EVM), TSW1400 capture card, external equipment, and software requirements.

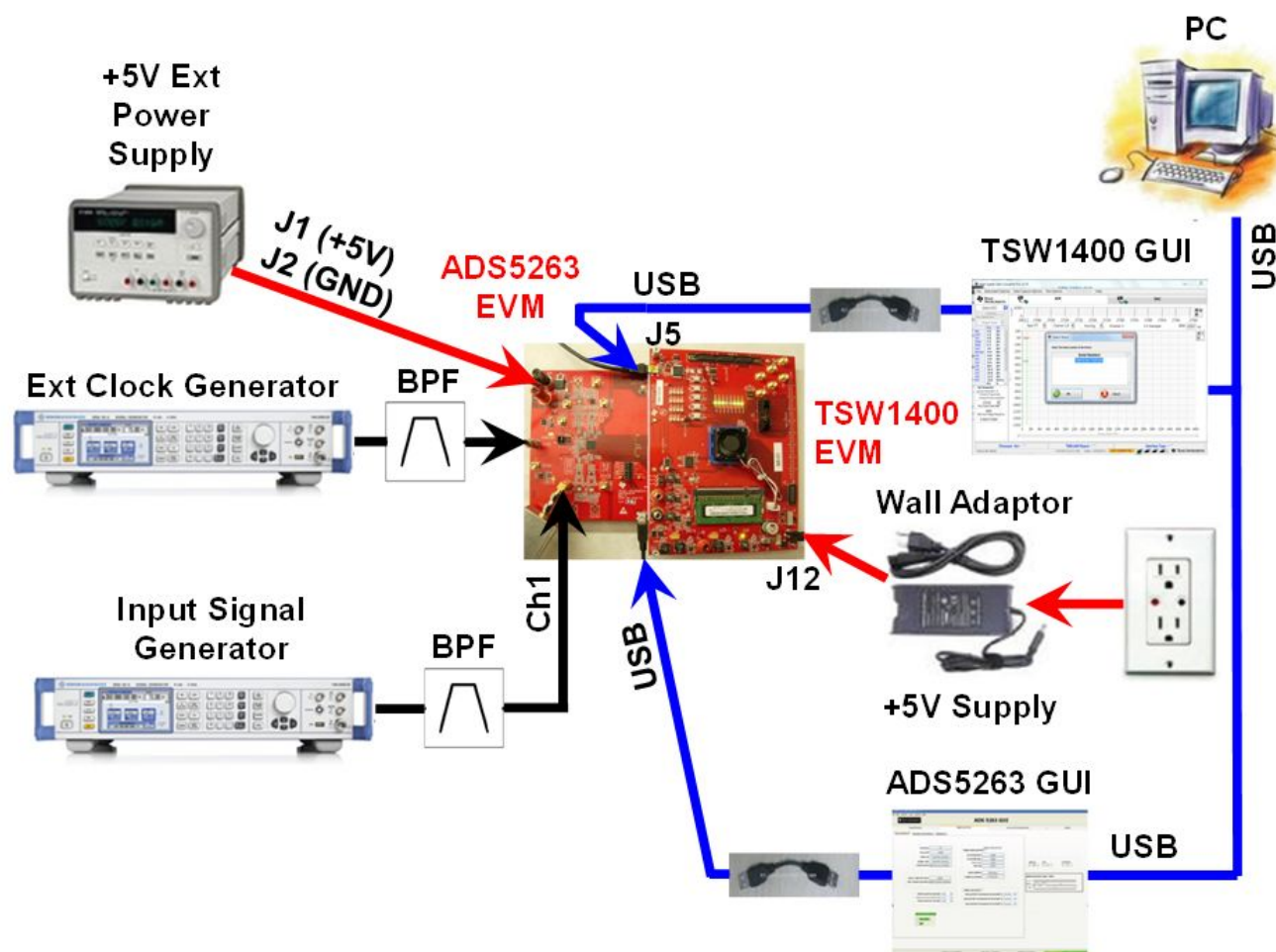


Figure 1. Evaluation Setup

TSW1400 Capture Card: The high-speed LVDS deserializer card is required for capturing data from the ADS5263EVM and its analysis using the TSW1400 graphical user interface (GUI).

For information pertaining to the TSW1400 card, see <http://www.ti.com/tool/tsw1400evm>.

Equipment: Signal generators (with low-phase noise) must be used as source of input signal and clock in order to get the desired performance. Additionally, band-pass filters are required in both the signal and clock paths to attenuate the harmonics and noise from the generators.

Power Supply: A single 5-V supply powers the EVM. The supplies for the ADS5263 device are derived from the 5-V supply. The power supply must be able to source up to 1.5 A. A 6-V supply can power the TSW1400 card using a laptop-style adapter.

USB Interface to PC: The USB connection from the ADS5263EVM and TSW1400 card to the personal computer (PC) must be set up; Step 3 in [Section 3.1](#) explains the USB driver installation.

ADS5263EVM GUI: [Section 3.1](#) explains the GUI installation procedure and its operation.

2 EVM Circuit Description

The complete schematic of the EVM can be found at the end of this user guide. Critical portions of the EVM are explained in the following text.

2.1 Power

The EVM requires a single 5-V supply for operation that can be supplied through banana jacks. Separate LDOs convert the 5-V input to generate the 3.3-V AVDD supply and the 1.8-V LVDD supply required for the ADS5263 operation.

2.2 Clock Input

The clock can be supplied to the analog-to-digital converter (ADC) in one of two ways. The default factory-configured option supplies a single-ended sine wave clock directly to the SMA connector J31. This clock is converted to differential by the TC4-1W transformer from MiniCircuits and is ac coupled to the ADC. This transformer has an impedance ratio of 4, so the voltage applied on J31 is stepped up by a factor of 2.

The clock input must be from a clean, low-jitter source (such as SMA100A or 8644B) and filtered by a narrow band-pass filter. Taking into account the attenuation of the filter, the clock amplitude must be set appropriately to get about 1.5-V peak-to-peak at the clock pins of the ADS5263.

The clock source is commonly synchronized with the signal generator of the input frequency to keep the clock and input coherent for meaningful FFT analysis.

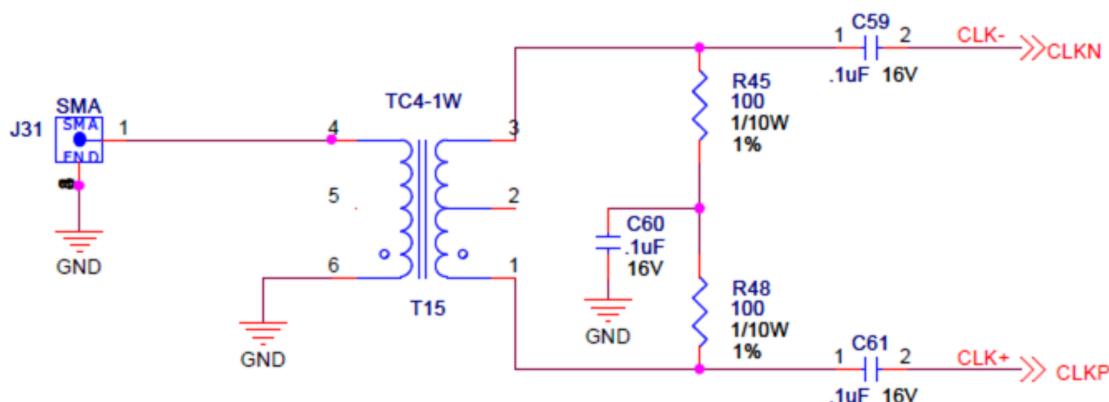


Figure 2. ADS5263 Sine Wave Clocking Using Transformer

LVPECL Clock Option

Alternately, the clock may be supplied by an onboard LVPECL clock buffer (TI's CDCLVP1102). To use this option,

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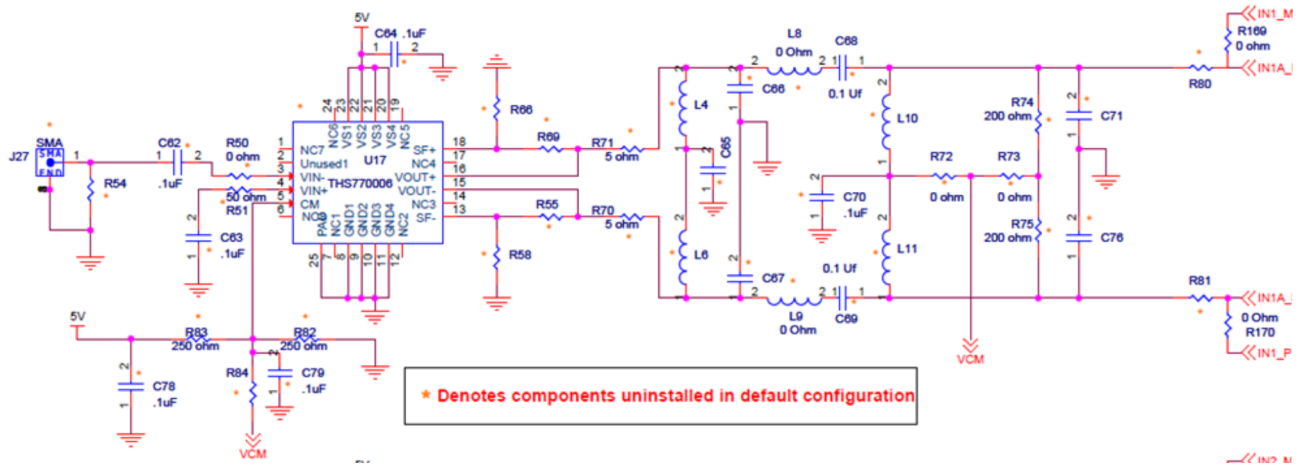


Figure 5. Input Drive Circuit – Using THS770006 Amplifier

Table 1. Two-Input Configurations

EVM Config	Transformer Drive	THS770006 Drive	Description
1	On channels 1, 2, 3, 4	On channel 3	- All components in the transformer drive path for all four channels are available in the EVM. - All components in the THS path for only channel 3 is available in the EVM.
2	On channels 1, 2, 3, 4	On channels 1, 2, 3, 4	All components in the transformer and THS paths for all four channels are available in the EVM.

Note that the 16-bit ADC and the 14-bit ADC have different analog input pins. Analog input pins 1A, 2A, 3A, 4A correspond to the 16-bit ADC inputs while 1B, 2B, 3B, 4B correspond to the 14-bit ADC inputs. Each of the four transformer paths can be configured to drive either the 16-bit or 14-bit ADC inputs. Similarly, each of the four THS paths can be configured to drive either the 16-bit or 14-bit ADC inputs. This configuration is achieved using pairs of 0 Ω SMT resistors, as listed in [Table 2](#).

Table 2. SMT Resistors

Drive Type	Chan 1A 16-Bit ADC	Chan 1B 14-Bit ADC	Chan 2A 16-Bit ADC	Chan 2B 14-Bit ADC
Transformer	R80, R81 = Open R169, R170 = 0 Ω R35, R153 = Open	R169, R170 = Open R35, R153 = 0 Ω	R98, R99 = Open R171, R172 = 0 Ω R154, R155 = Open	R171, R172 = Open R154, R155 = 0 Ω
THS770006	R80, R81 = 0 Ω R169, R170 = Open	This option is not supported	R98, R99 = 0 Ω R171, R172 = Open	This option is not supported
Drive Type	Chan 3A 16-Bit ADC	Chan 3B 14-Bit ADC	Chan 4A 16-Bit ADC	Chan 4B 14-Bit ADC
Transformer	R138, R139 = Open R173, R174 = 0 Ω R165, R166 = Open	R173, R174 = Open R165, R166 = 0 Ω	R125, R126 = Open R175, R176 = 0 Ω R167, R168 = Open	R175, R176 = Open R167, R168 = 0 Ω
THS770006	R138, R139 = 0 Ω R173, R174 = Open	This option is not supported	R125, R126 = 0 Ω R175, R176 = Open	This option is not supported

2.4 Onboard Band-Pass Filter in THS Path

A provision exists in the EVM to include a band-pass filter in the analog input between the THS770006 amplifier and the ADS5263 input pins. Component placeholders are provided to support up to sixth-order LC band-pass filter on each of the four channels. This allows users to design their own filters, populate the EVM with the corresponding components, and verify the performance on the EVM itself.

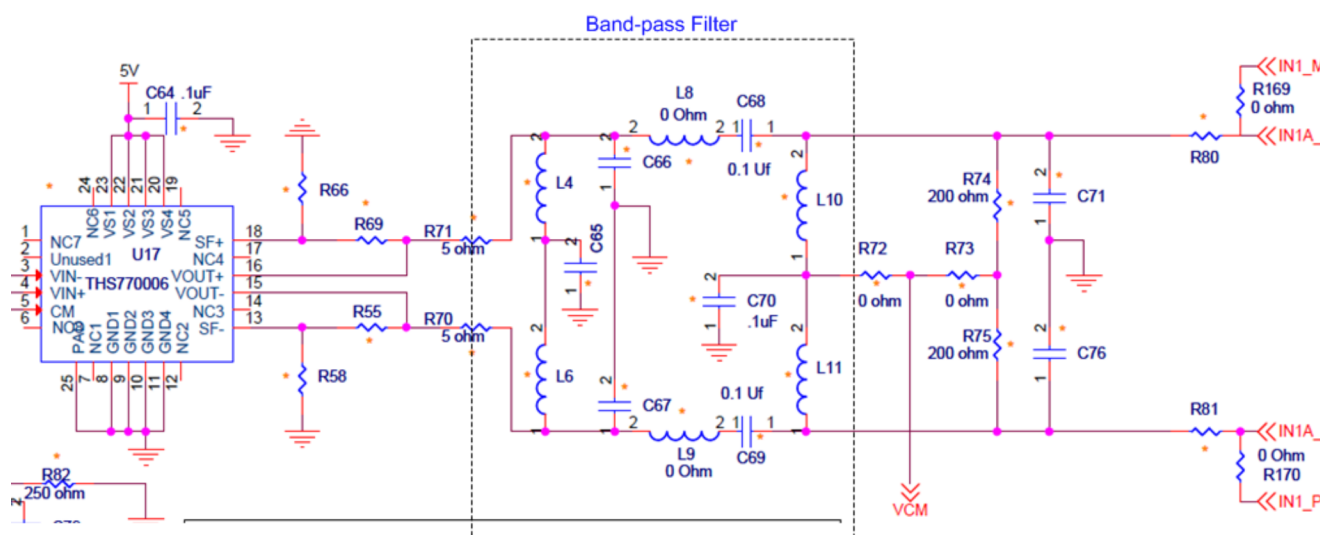


Figure 6. Band-Pass Filter

3 ADS5263EVM GUI

This section describes the software features accompanying the EVM kit. The ADS5263EVM control software allows users to write to the ADC registers found in the data sheet.

3.1 Installing the EVM GUI

The ADS5263EVM comes with a software install. To Download the software, visit the ADS5263 [product folder](#) under *Tools & software*.

Step 1 – Install the software before plugging in the USB cable for the first time.

- Unzip the installer file, and run the setup.exe file

Step 2 – Connect the USB cable from the PC to the EVM.

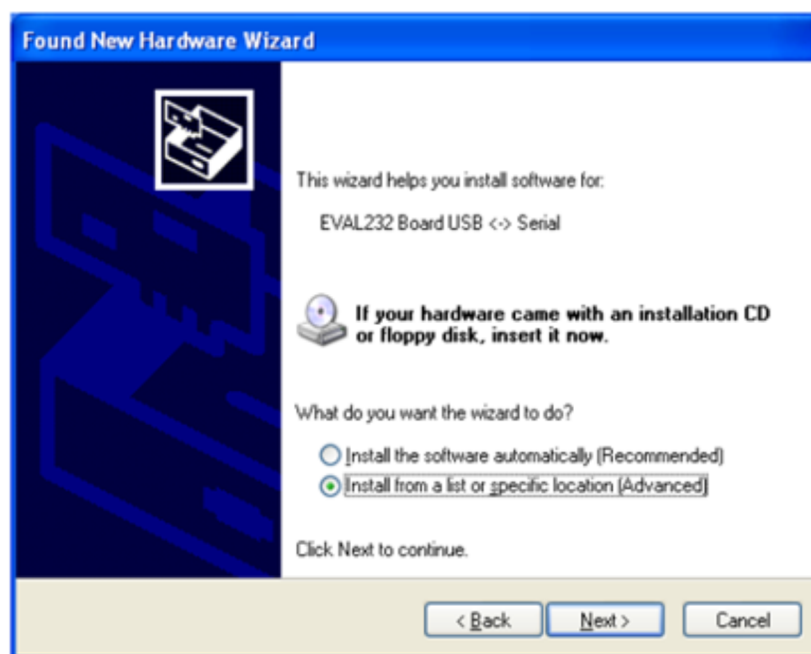
- If the USB driver has not been previously installed in the PC, then a Windows™ message *Found New Hardware* appears. Proceed to Step 3 to complete the installation.
- If the message does not appear, then skip Step 3

Step 3 – Completing the USB Driver Installation

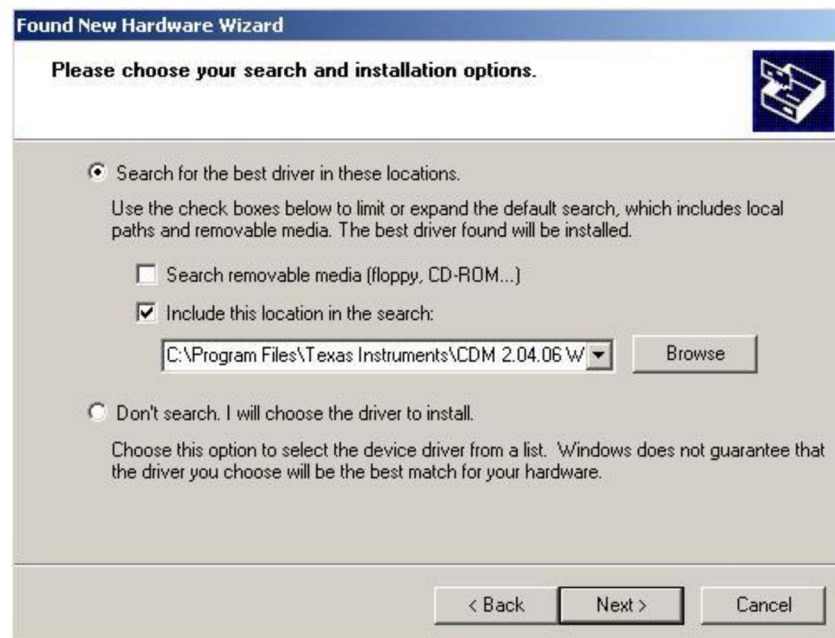
- In the Found New Hardware message, select *No, not this time* from the options, and press the Next button



- Select *Install from a list or specific location (Advanced)* as shown in the following illustration, and then click *Next*.



- Select *Search for the best driver in these locations.*, and enter the file path *C:\Program Files\Texas Instruments\CDM 2.04.06 WHQL Certified* in the combo-box, or browse to it by clicking the *Browse* button. Once the file path has been entered in the box, click *Next* to proceed.



- Windows XP can be configured to warn when unsigned (non-WHQL certified) drivers are about to be installed. In that case, the following screen is displayed. Click on *Continue Anyway* to continue with the installation.



- If Windows XP is configured to ignore file signature warnings, no message appears.

3.2 GUI Features

Once it is launched, the ADS5263 GUI comes up in the state shown in the following illustration. The ADS5263 has many programmable registers to control various modes. In the GUI, registers with similar functions are grouped into separate tabs such as:

- Top-level
- Interface
- Dig Sig Proc

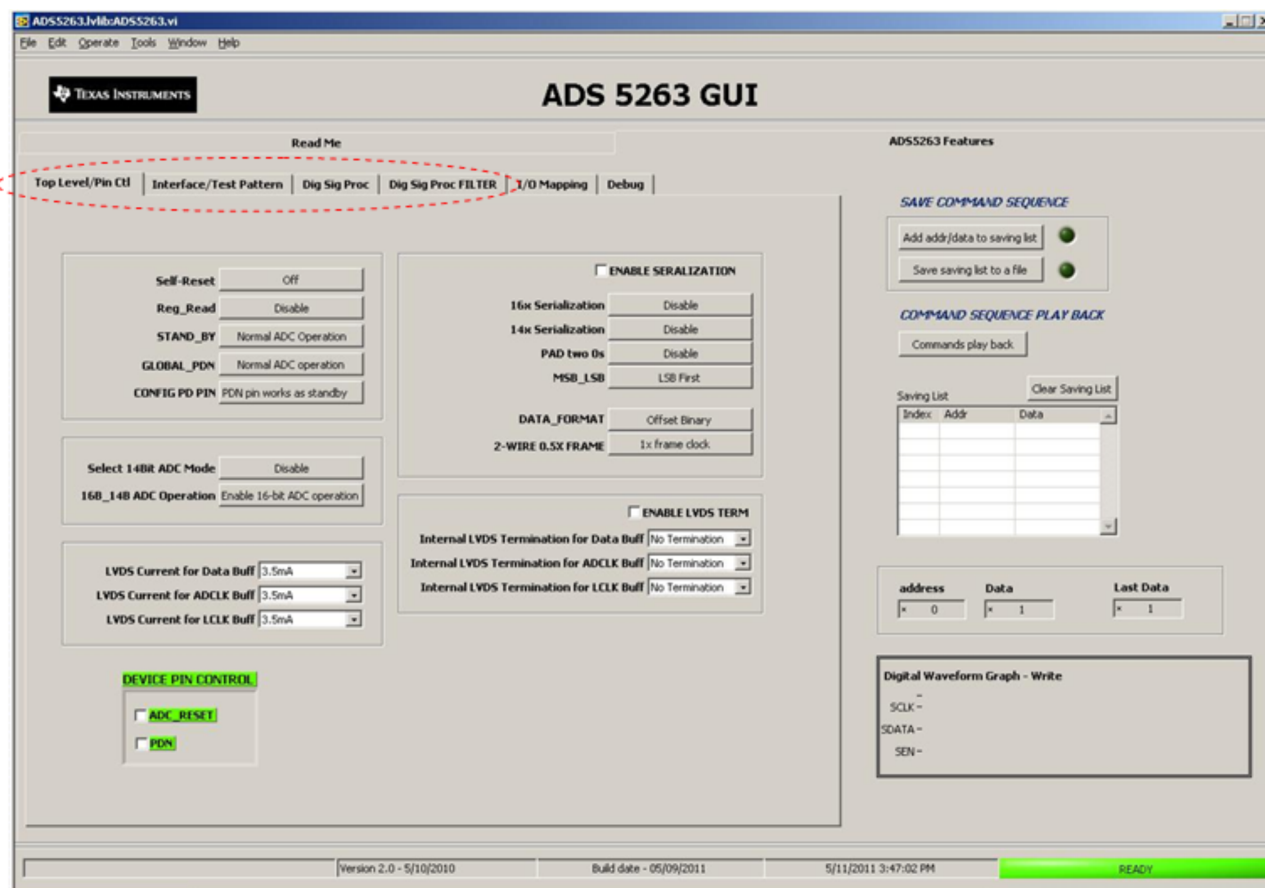


Figure 7. ADS5263 GUI

Additionally, the GUI has a couple of interesting features – debug mode and command sequence. The debug mode is an alternate way of controlling the registers in the device by directly specifying the hexadecimal values for the register address and register data.

The command sequence can be used to record and store a sequence of register writes into a text file. The next time, after a device reset, the text file can be simply played back. The GUI configures the device with the registers stored in the text file.

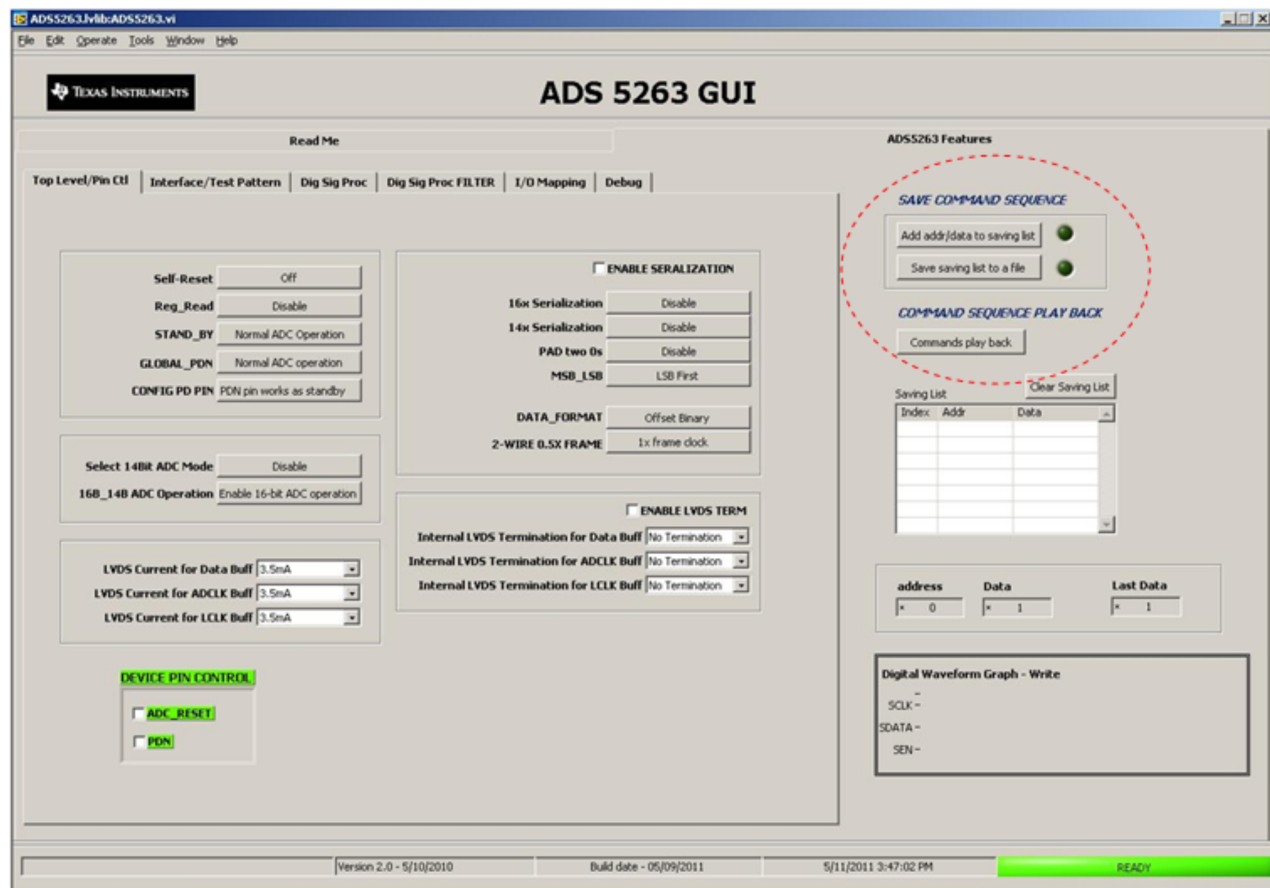


Figure 8. ADS5263 GUI

4 TSW1400 GUI

The TSW1400 GUI is required to transfer ADC data from the TSW1400 card. The data can be viewed in the time-domain. The spectrum of the captured data can be viewed in the Single Tone FFT tab.

For installation of the TSW GUI, see *TSW1400EVM: High-Speed LVDS Deserializer and Analysis System user's guide*. Also see [Appendix A](#) for how to download the software.

If the PC is already pre-installed with TSW1400, it may be an earlier revision that does not support the ADS5263 device. Follow the steps outlined in the relevant section of the TSW1400EVM user's guide to *first uninstall* and then re-install the latest TSW1400 version.

4.1 Verify the Setup

Perform the following steps before launching the TSW1400 GUI:

- Connect the TSW1400 card to the ADS5263EVM.
- Connect the power supplies to both cards and power up.
- Connect the USB cables from the PC to the ADS EVM and TSW card.

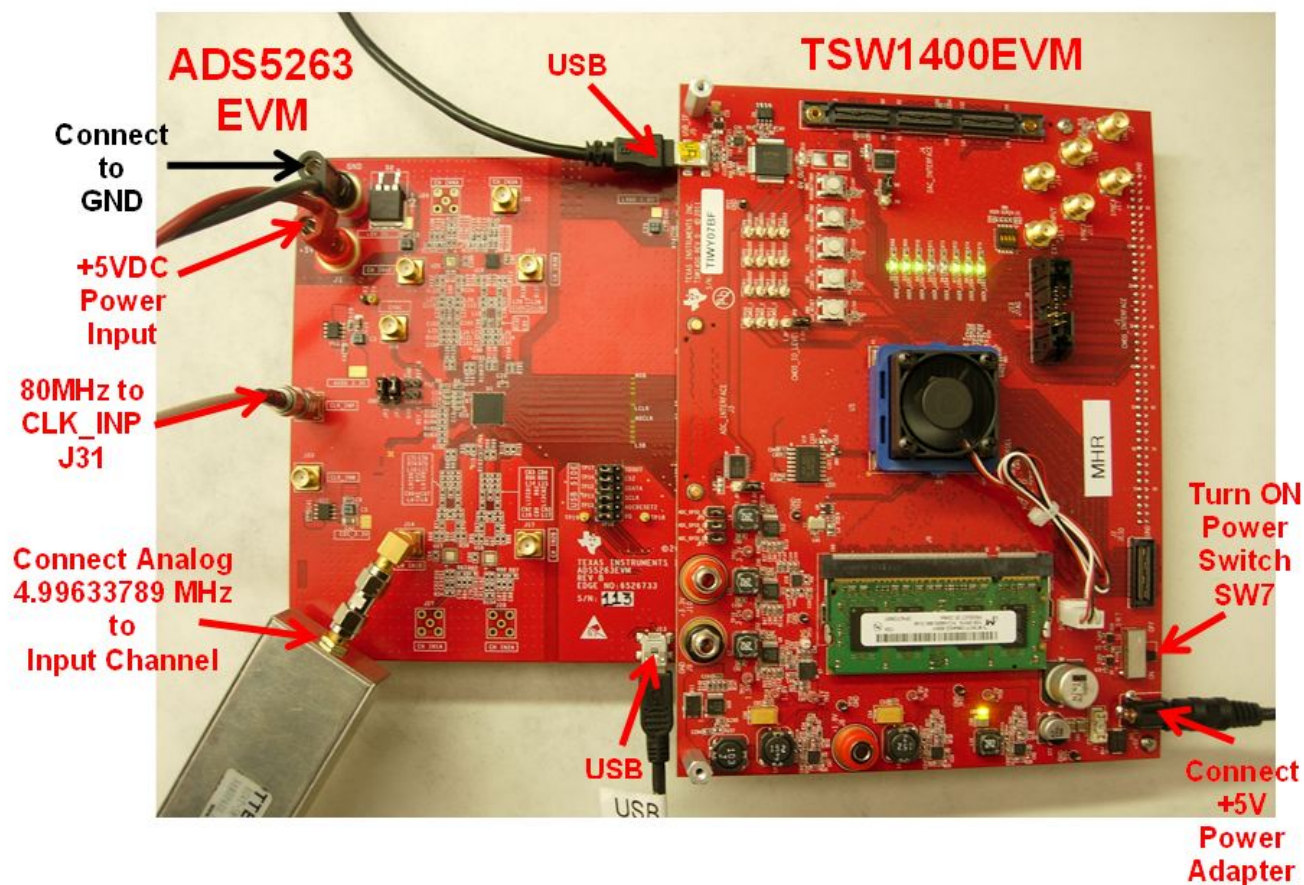


Figure 9. EVM and TSW Card Connections

- Launch the ADS5263GUI, and initialize the device
 - First, reset the device by clicking the *Self-reset* button (this is a self-clearing bit that resets the device and clears itself to zero)
 - Put device in 2-wire, ...modes

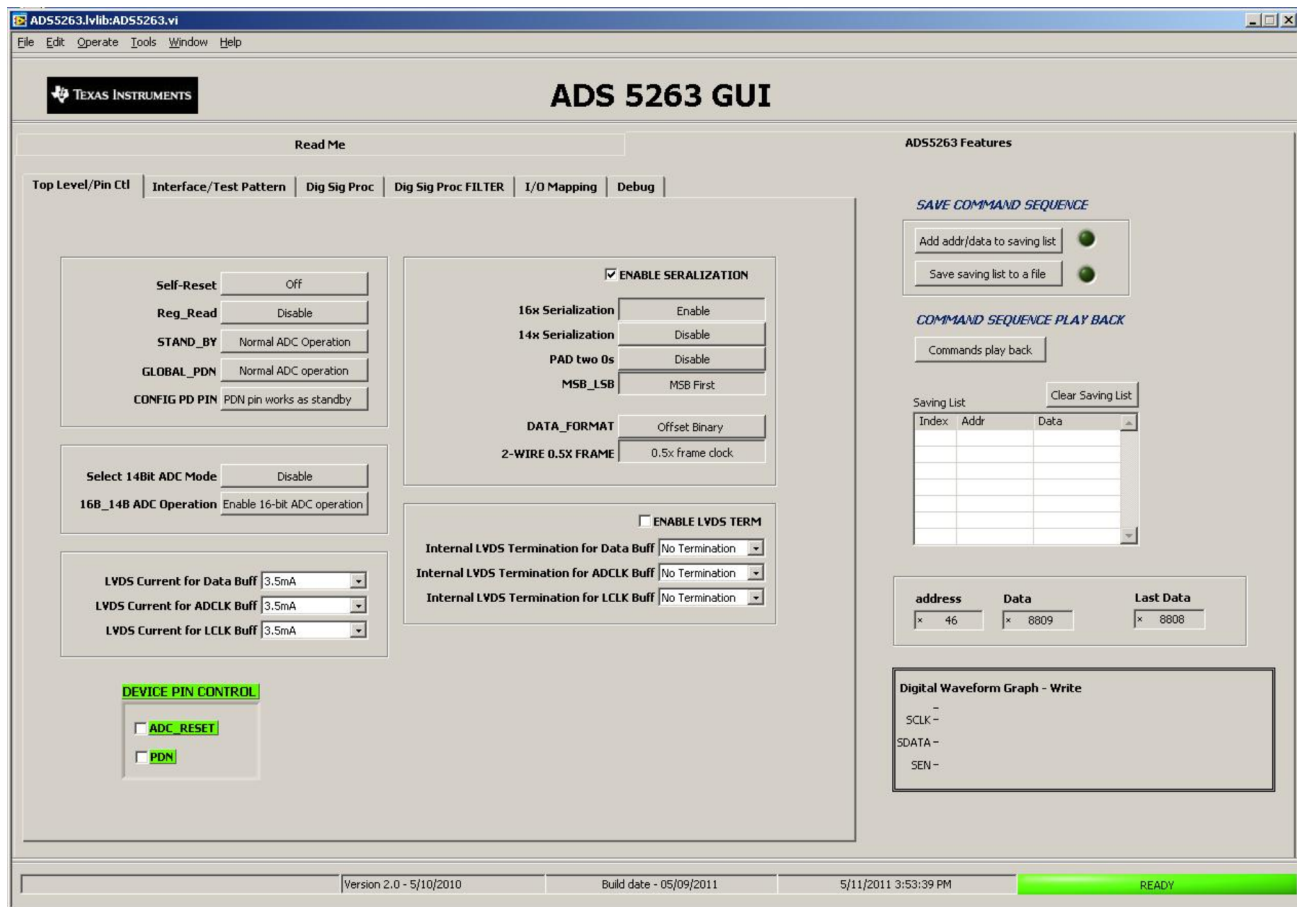


Figure 10. Initializing the ADS5263GUI

- Apply input clock signal to SMA connector J31.
- The device is now ready for data capture by the TSW card.
 - Check the DCM LED on the TSW card – it must be flashing. This indicates that the TSW card is able to correctly detect the bit clock output from the ADS EVM.
- Launch the TSW GUI; at this point, the TSW GUI communicates with the EVM and once proper communication is established, a message is displayed at the bottom left of the GUI.
- This completes the setup verification.

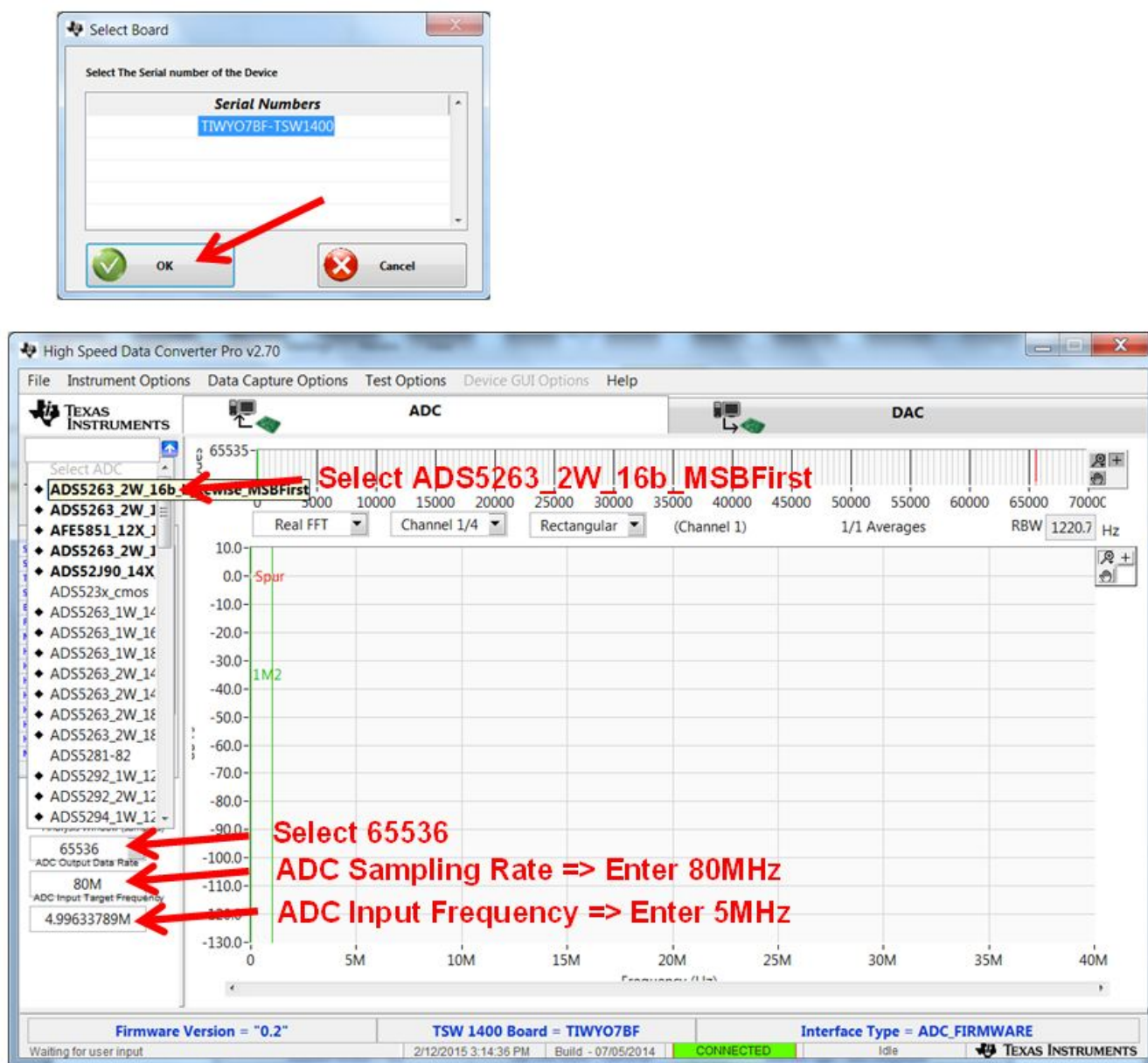


Figure 11. Successful Interface Between TSW Card and Computer

4.2 Verify the Ramp Test Pattern

- Initialize the TSW GUI with the following settings.
 - Select the device.
 - Choose the channel.
 - Enter the ADC sample rate. For example, 80M for 80-MSPS sample rate.
- Using the ADS5263 GUI, enable the Ramp Test Pattern mode

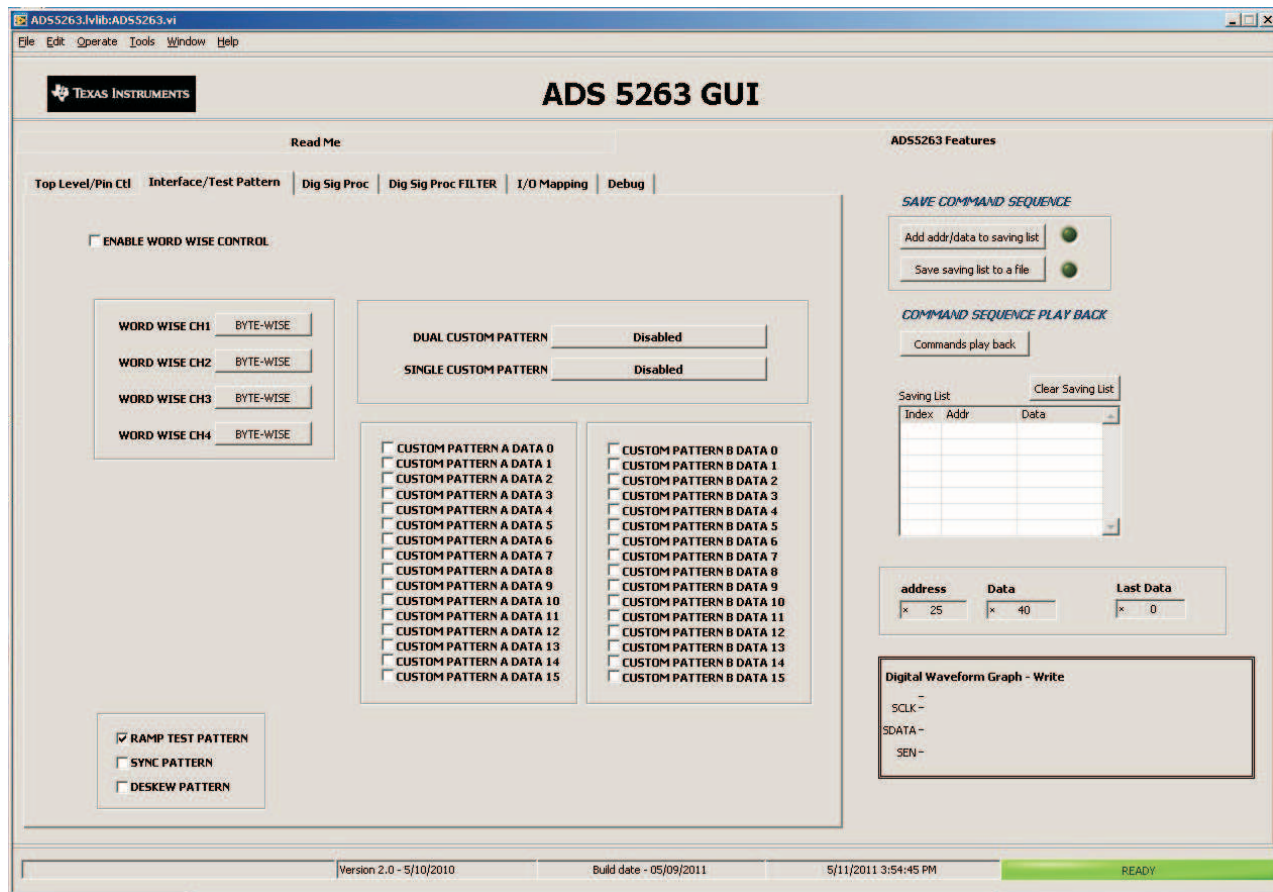


Figure 12. Enabling Ramp Test Pattern in the ADS5263 GUI

- Press the Capture button in the TSW GUI.
- Time Domain:
 - Select the Time Domain tab in the GUI.
 - Graph shows the captured time domain data.
It Must be a clean digital ramp that increases from code 0 to code 65535 from one sample to the next. Note that the starting code in the capture graph is not fixed and can be anywhere from 0 to 65535.

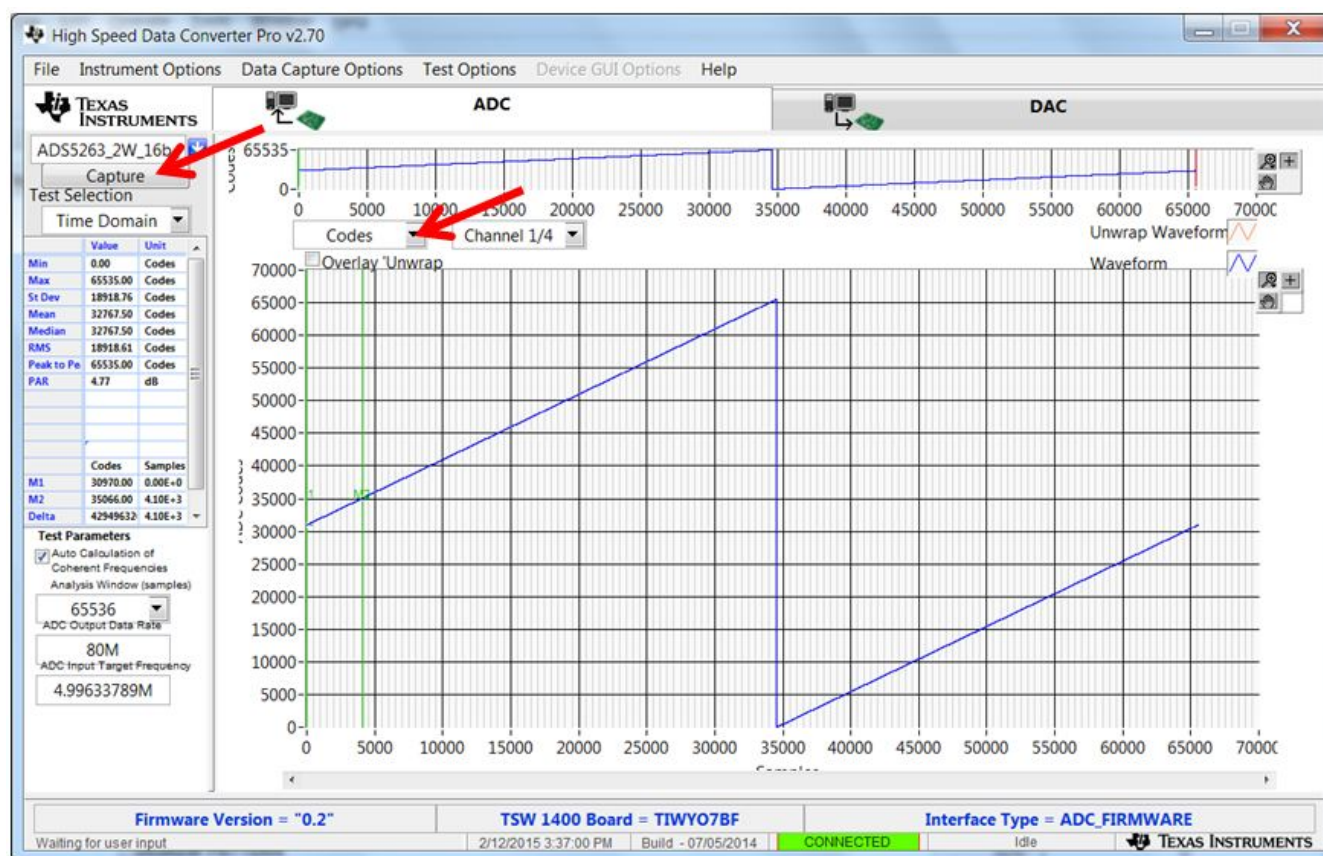


Figure 13. Verifying With Ramp Test Pattern

- Now, disable the *Ramp Test Pattern* in the ADS5263 GUI.
- This completes the verification of the ramp test pattern.

4.3 Verify With a Sine Wave Analog Input

- Note that for all performance evaluation, low-phase noise signal generators are required (see [Figure 1](#)) for both analog signal and sampling clock inputs.
- In the TSW GUI,
 - Enter the ADC input frequency being applied. For example, 3M for 3-MHz input frequency.
 - choose the desired channel
- Set the frequency in the signal generator to the value displayed in the field *ADC Input Coherent frequency* and connect the generator output to the desired channel. This makes the input signal coherent with respect to the sampling clock, which is required for FFT analysis.
- Now, press the Capture button
- Frequency Domain:
 - Select the Single tone FFT tab in the GUI.
 - The fft graph shows the spectrum of the captured sine wave data

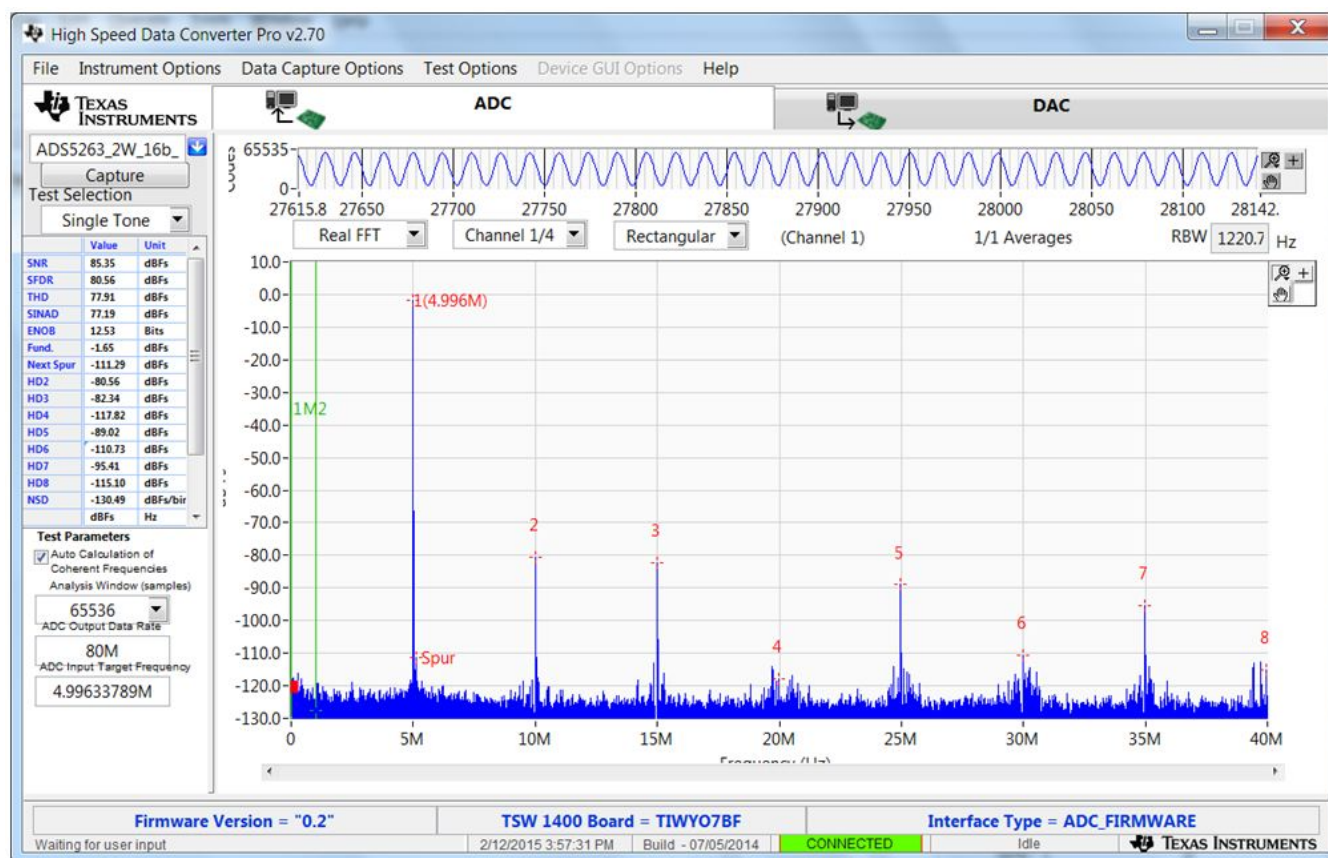


Figure 14. Spectrum of Captured Data

5 ADS5263EVM Schematics and Test Points

The schematics for the ADS5263EVM evaluation module are attached at the end of this document.

5.1 List of Test Points

Test Points on the EVM

Test Points	Name	Description
TP1	SDOUT	Serial register output
TP2	Ground	Board ground
TP9	VCM	1.5V common-mode output
TP12	SDATA	Serial interface data input
TP13	SCLK	Serial interface clock input
TP14	CSZ	Serial interface enable input
TP15	ADCRESETZ	Reset input
TP16	PD	Power down control input

6 ADS5263EVM PCB Layout

The following figures (Figure 15 through Figure 20) show the design of the ADS5263EVM printed-circuit board. PCB dimensions: L x W = 5 x 6 inches, four layers and 0.5-oz copper on outer layers and 1oz copper on inner layers.

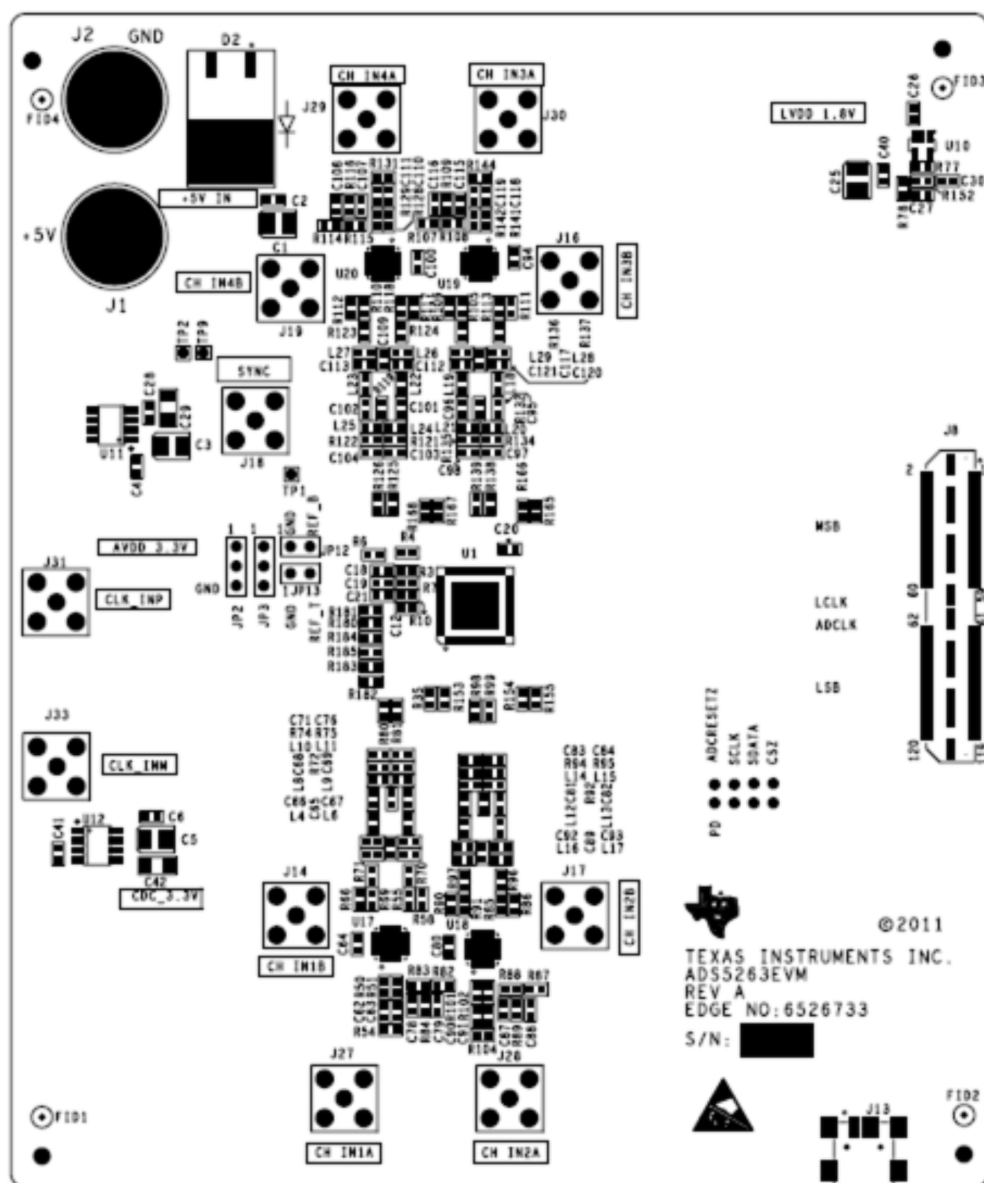


Figure 15. ADS5263EVM Top Layer Assembly Drawing – Top View

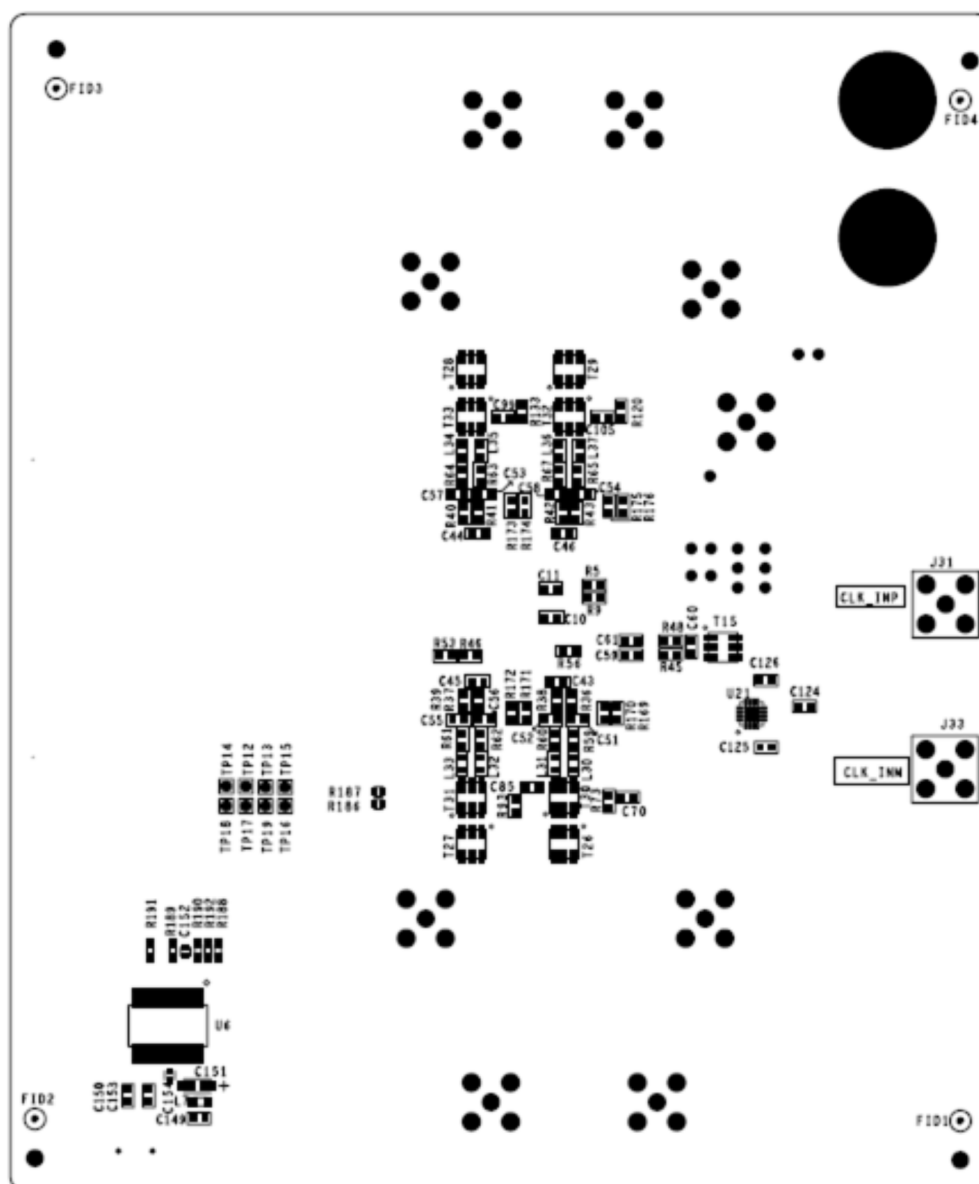


Figure 16. ADS5263EVM Bottom Layer Assembly Drawing – Bottom View

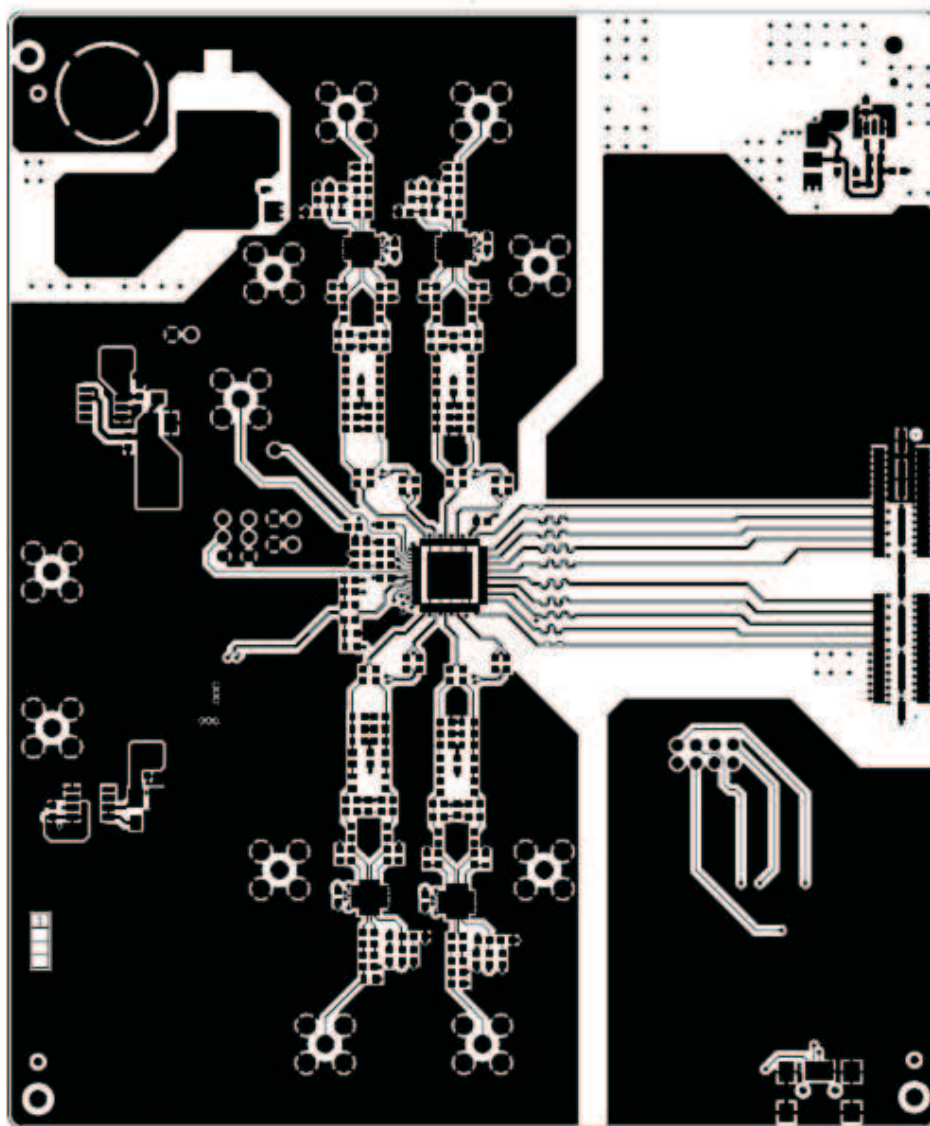


Figure 17. ADS5263EVM Top Layer Copper – Top View

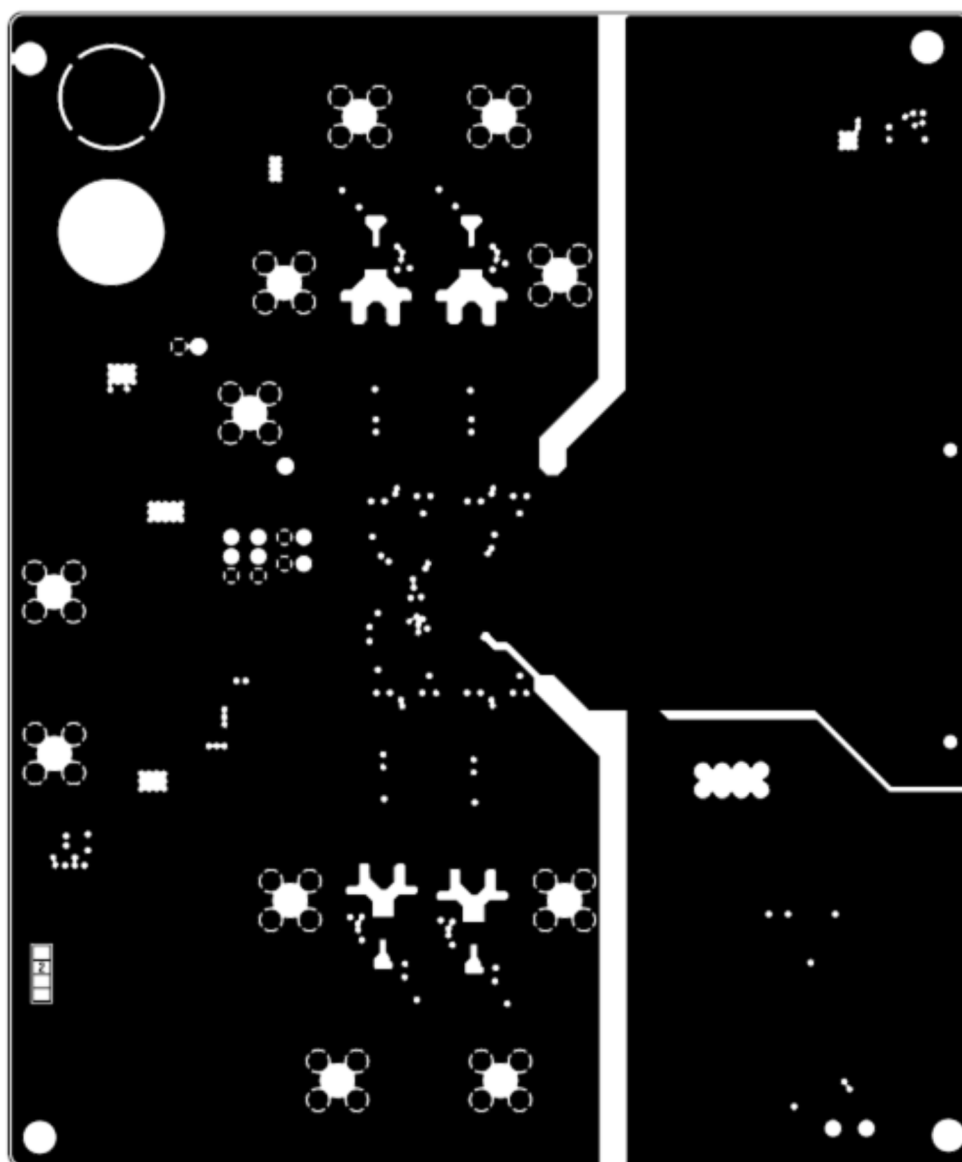


Figure 18. ADS5263EVM Internal Layer 1, Ground – Top View

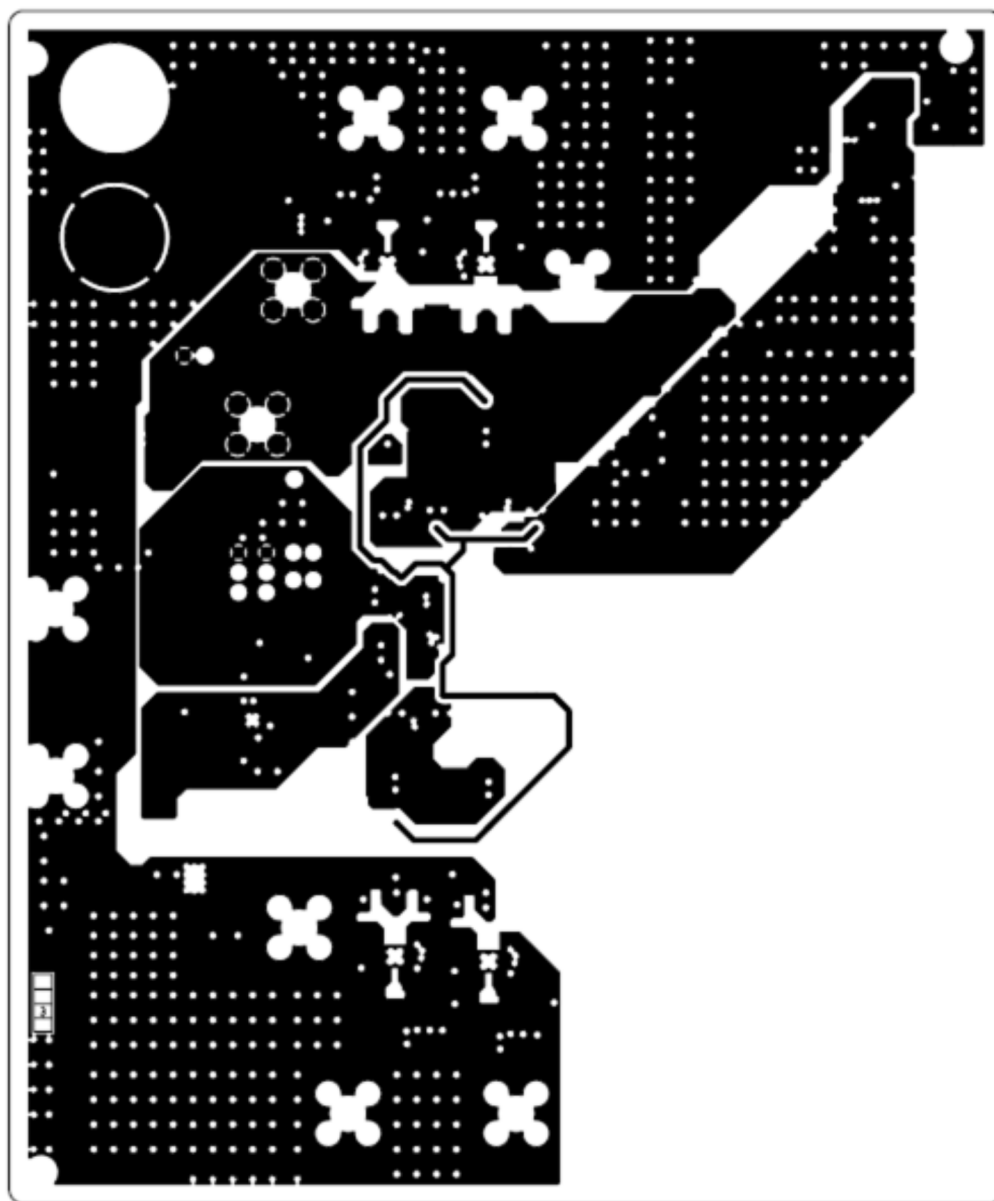


Figure 19. ADS5263EVM Internal Layer 2, Power – Top View

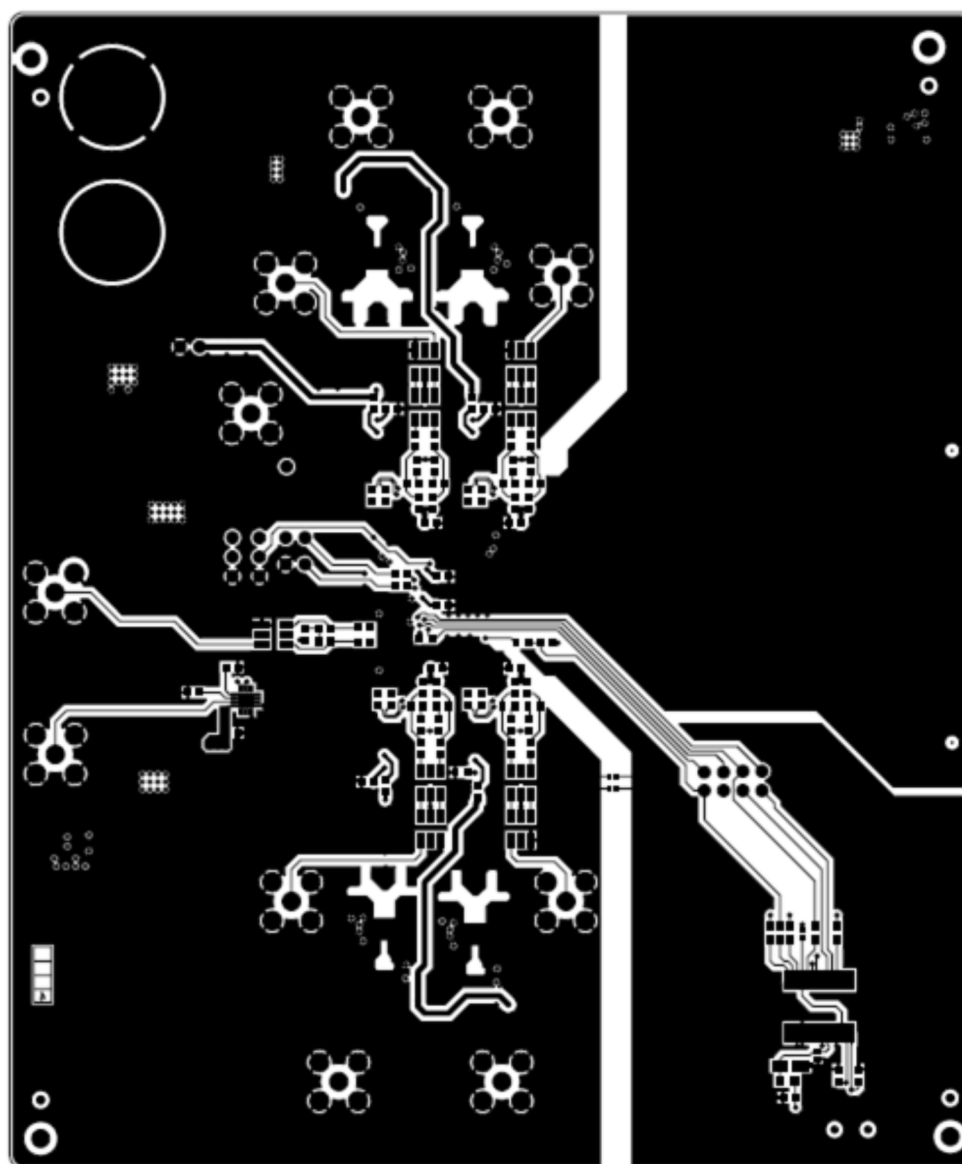


Figure 20. ADS5263EVM Bottom Layer Copper – Top View

7 Bill of Materials

Table 3. EVM Components List

Qty	Reference Designator	Value	Manufacturer	Part Number	Description
6	C1,C3,C5,C25,C29,C42	10 μ F	AVX	1206YC106MAT2A	CAP CER 10UF 16V X7R 20% 1206
4	C2,C4,C6,C40	1 μ F	AVX	0603YC105KAT2A	CAP CER 1.0UF 16V X7R 10% 0603
32	C10,C11,C12,C18,C19,C20, C21,C26,C27,C30,C43,C44, C45,C46,C59,C60,C61,C94, C99,C115,C116,C118,C119, C124,C125,C126,C152,C154 C28,C41,C95,C96	0.1 μ F	AVX	ECJ-1VB1C104K	CAP CER .10UF 50V X7R 10% 0603
8	C51,C52,C53,C54,C55,C56, C57,C58	220 pF	AVX	06035A221FAT2A	CAP CERM 220PF 1% 50V NP0 0603
1	C149	10 nF	Panasonic	ECJ-1VB1C103K	CAP 10000PF 16V CERM X7R 0603
2	C150,C153	47 pF	Panasonic	ECJ-1VC1H470J	CAP CERAMIC 47PF 50V 0603 SMD
1	C151	4.7 μ F	AVX	TAJA475K020R	
1	D2	MBRB2515L	MBRB2515LT4GOSCT-ND	MBRB2515LT4GOSCT-ND	DIODE SCHOTTKY 15V 25A D2PAK
2	JP2,JP3	HEADER 3POS 0.1 CTR	ANY	JUMPER,3P,,100CC	JUMPER,3P,,1CC
2	JP12,JP13	HEADER_1x2_100_430L	ANY		
1	J1	RED	ALLIED ELECTRONICS	ST-351A	Banana Female Red
1	J2	BLK	ALLIED ELECTRONICS	ST-351B	Banana Female Black
1	J8	QTH-060-02-F-D-A	SAMTEC	QTH-060-02-F-D-A	High speed connector
1	J13	USB_MINI_AB	JAE	DX3R005HN2E700	USB_MINI_AB
8	J14,J16,J17,J18,J19,J30,J31,J33	SMA	SAMTEC	SMA-J-P-H-ST-TH1	JACK PANEL MOUNT SMA
1	L7	1K at 100 MHz			
2	L18,L19	0 Ω	Panasonic	ERJ-3GEY0R00	RESISTOR,SMT,0603,0 OHM,5%, ZERO OHM JUMPER
8	L30,L31,L32,L33,L34,L35,L36,L37	56 nH	Panasonic	EXC-ML32A680U	Inductor
23	R3,R5,R7,R9,R10,R132, R133,R138,R139,R141,R165, R166,R169,R170,R171,R172, R175,R176,R188,R189,R190, R191,R192	0 Ω	Panasonic	ERJ-3GEY0R00	RESISTOR,SMT,0603,0 OHM,5%, ZERO OHM JUMPER
2	R4,R78	56.2K	Panasonic - ECG	ERJ-3EKF5622V	RES 56.2K OHM 1/10W 1% 0603 SMD
1	R6	50 Ω	Panasonic	ERJ-3EKF49R9V	RES 49.9 OHM 1/10W 1% 0603 SMD
8	R36,R37,R38,R39,R40,R41,R42,R43	25 Ω	Panasonic	ERJ-3EKF24R9V	RES 24.9 OHM 1/10W 1% 0603 SMD
2	R45,R48	100	Panasonic	ERJ-3EKF1000V	RES 100 OHM 1/10W 1% 0603 SMD
1	R46	10 Ω	Panasonic	ERJ-3EKF10R0V	RES 10.0 OHM 1/10W 1% 0603 SMD
2	R53,R56	10K	Panasonic	ERJ-3EKF1002V	RES 10.0K OHM 1/10W 1% 0603 SMD
8	R59,R60,R61,R62,R63,R64,R65,R67	12.4 Ω	Panasonic	ERJ-3EKF12R4V	RES 12.4 OHM 1/10W 1% 0603 SMD
2	R77,R152	56K	Panasonic	ERJ-3EKF5602V	RES 56.0K OHM 1/10W 1% 0603 SMD
2	R107,R108	250 Ω	Vishay	PLT0603Z2500AST5	RES 250 OHM 0.05% 5PPM 0603 SMD
2	R134,R135	200 Ω	Panasonic	ERJ-3EKF2000V	RES 200 OHM 1/10W 1% 0603 SMD
2	R136,R137	15 Ω	Vishay	CRCW06035R10FNEA	RES 15 OHM 1/10W 1% 0603 SMD
1	R142	50 Ω	Panasonic	ERJ-3EKF49R9V	RES 49.9 OHM 1/10W 1% 0603 SMD
2	R186,R187	0 Ω	Vishay/Dale	CRCW04023K00FKED	RESISTOR,SMT,0603,0 OHM,5%, ZERO OHM JUMPER
11	TP1,TP2,TP9,TP12,TP13, TP14,TP15,TP16,TP17,TP18,TP19	T POINT R			
1	T15	TC4-1W	MINI CIRCUITS	TC4-1WG2+	Transformer
8	T26,T27,T28,T29,T30,T31,T32,T33	WBC1-1	COIL CRAFT	WBC1-1TLB	Transformer
1	U1	ADS5263_QFN64	Texas Instruments		16-Bit Quad channel ADC
1	U6	FT245RL	FTDI	FT245RL	IC USB TO PARALLEL FIFO 28-SSOP
1	U10	TPS73201-SOT23	Texas Instruments	TPS73201DBVT	IC LDO REG 250MA ADJ-V SOT23-5
2	U11,U12	TPS77533D	Texas Instruments	TPS77533D	IC 3.3V 500MA LDO REG 8-SOIC
1	U19	THS770006	Texas Instruments	TPS770006IRGER	IC AMP DIFF ADC DVR 16BIT 24VQFN
1	U21	CDCLVP1102	Texas Instruments	CDCLVP1102RGTT	IC CLK BUFF 1:2 LVPECL SGL 16QFN
2	Z_SH-H3,Z_SH-H4	SHUNT-HEADER			
2	Z_SH-J1,Z_SH-J2	SHUNT-JUMPER-0402			

Table 3. EVM Components List (continued)

Qty	Reference Designator	Value	Manufacturer	Part Number	Description
4	STANDOFF HEX M3 THR ALUM 18MM		Digi-Key	24436K-ND	STANDOFF HEX M3 THR ALUM 18MM
4	SCREW STEEL M3 THR 6MM		Digi-Key	29311K-ND	SCREW STEEL M3 THR 6MM

High Speed Data Converter Pro (HSDCPro) GUI Installation

Download the HSDCPro GUI Installer using this link: [HSDCPro GUI](#)

- Unzip the saved folder and run the installer executable to obtain the pop-up shown in [Figure 21](#).
- Click the *Install* button.

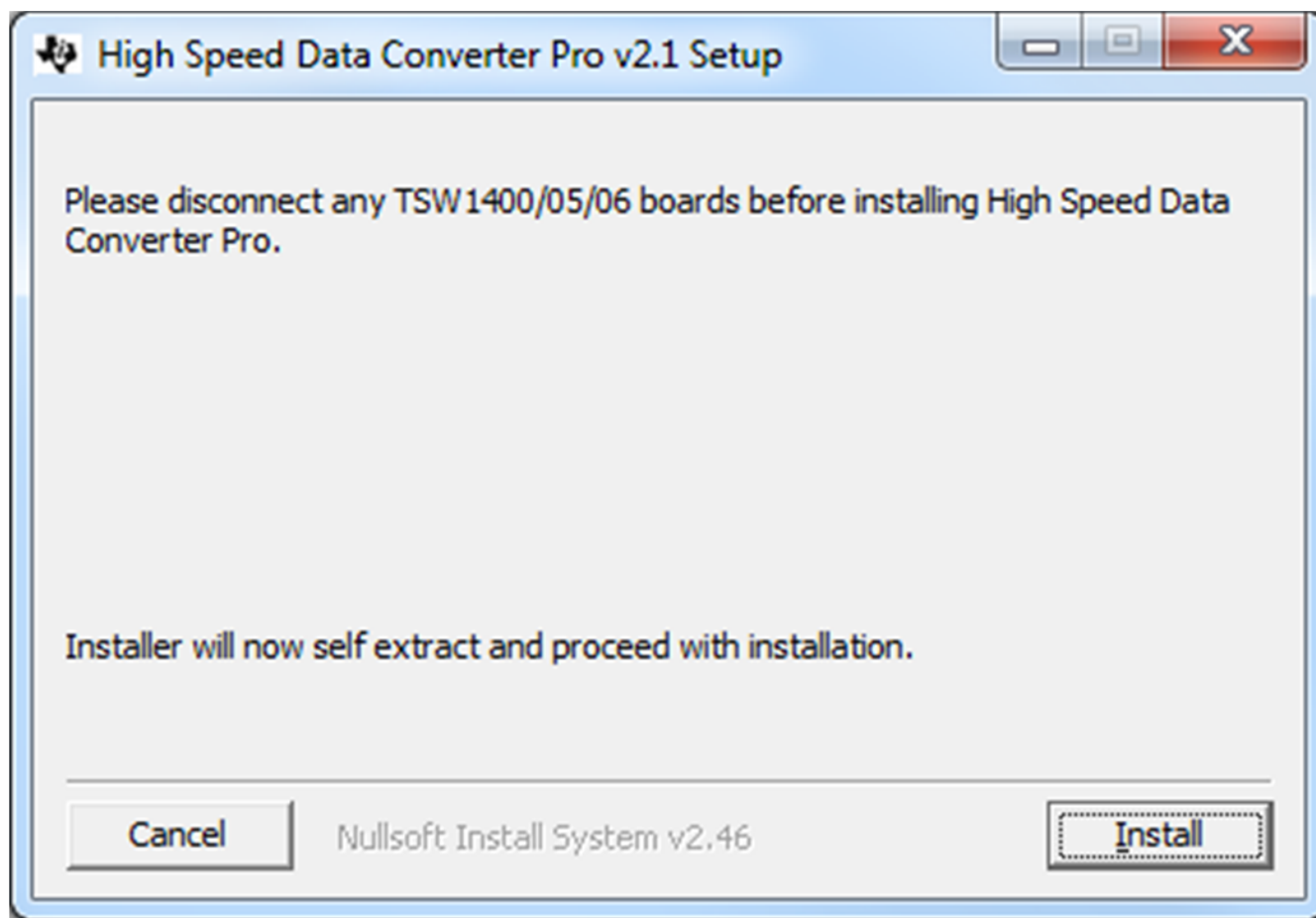


Figure 21. HSDCPro Install (Begin)

- Leave the destination directories as the default location, for the TSW1400GUI installation and press the NEXT button as shown in [Figure 22](#).

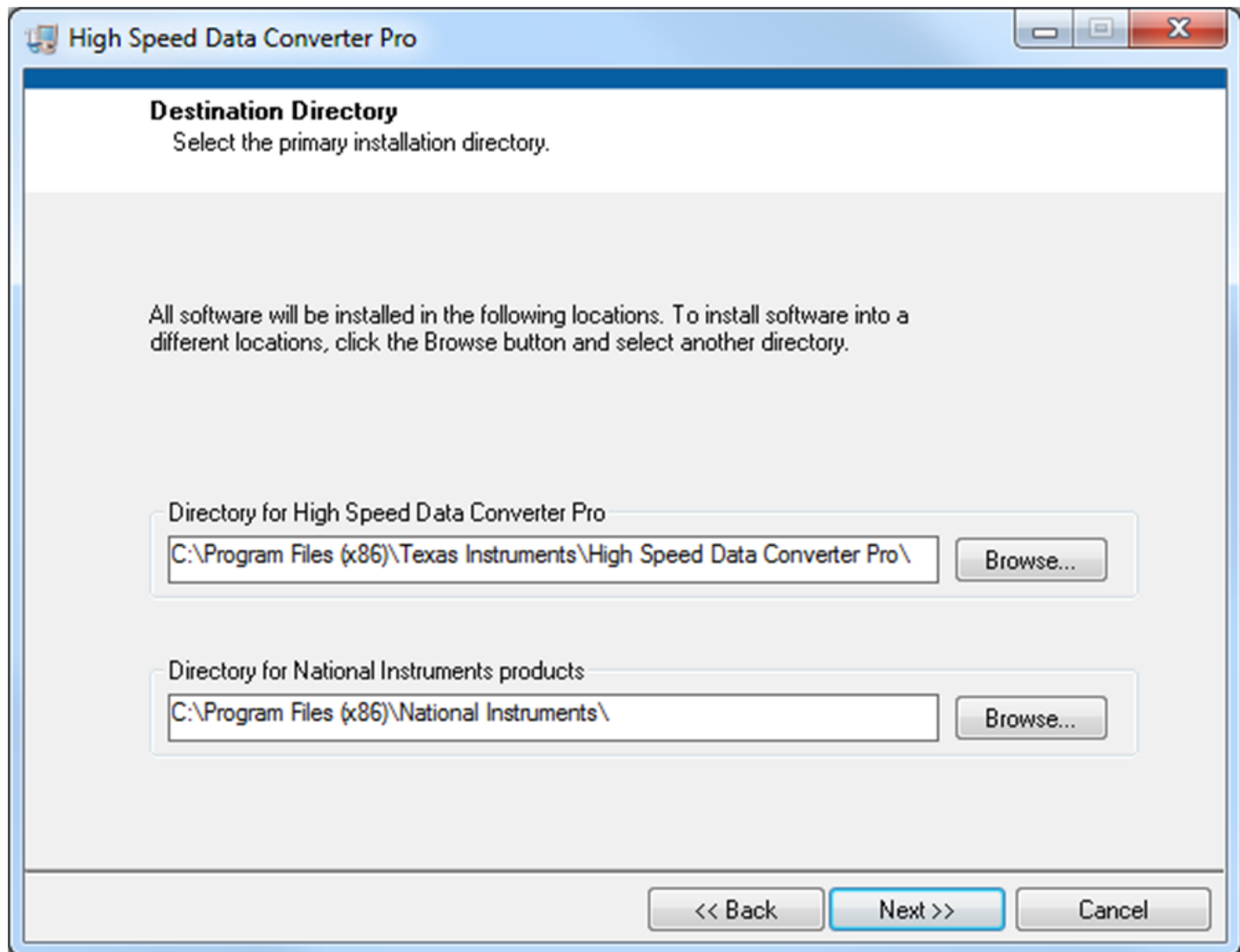


Figure 22. HSDCPro Install (Install Directory)

- Read the License Agreement from Texas Instruments and select *I accept the License Agreement* and press the *Next* button as shown in [Figure 23](#).

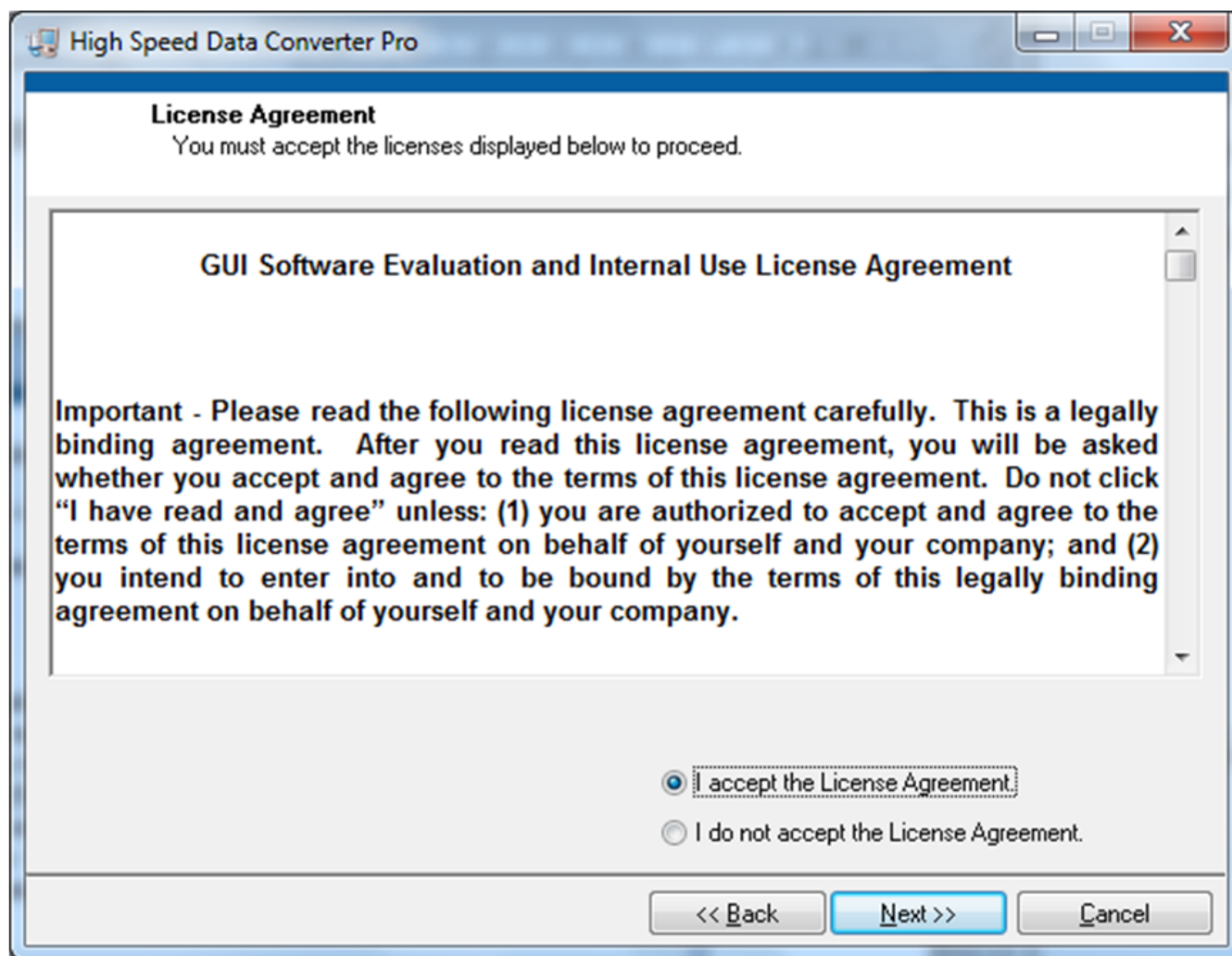


Figure 23. HSDCPro Install (TI License Agreement)

- Read the License Agreement from National Instruments and select *I accept the License Agreement* and press the *Next* button as shown in [Figure 24](#).

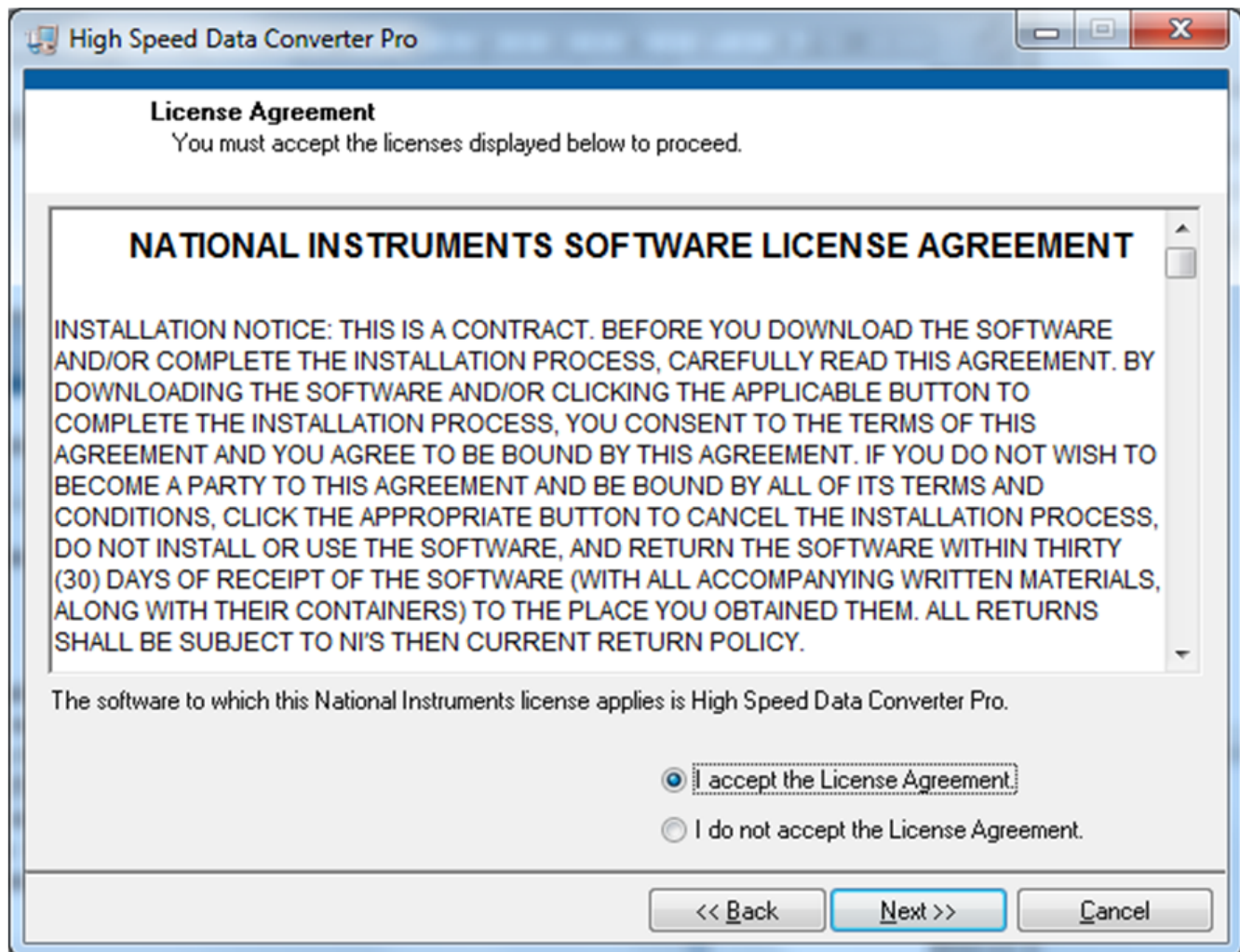


Figure 24. HSDCPro Install (NI License Agreement)

- Press the *Next* button as shown in [Figure 25](#).

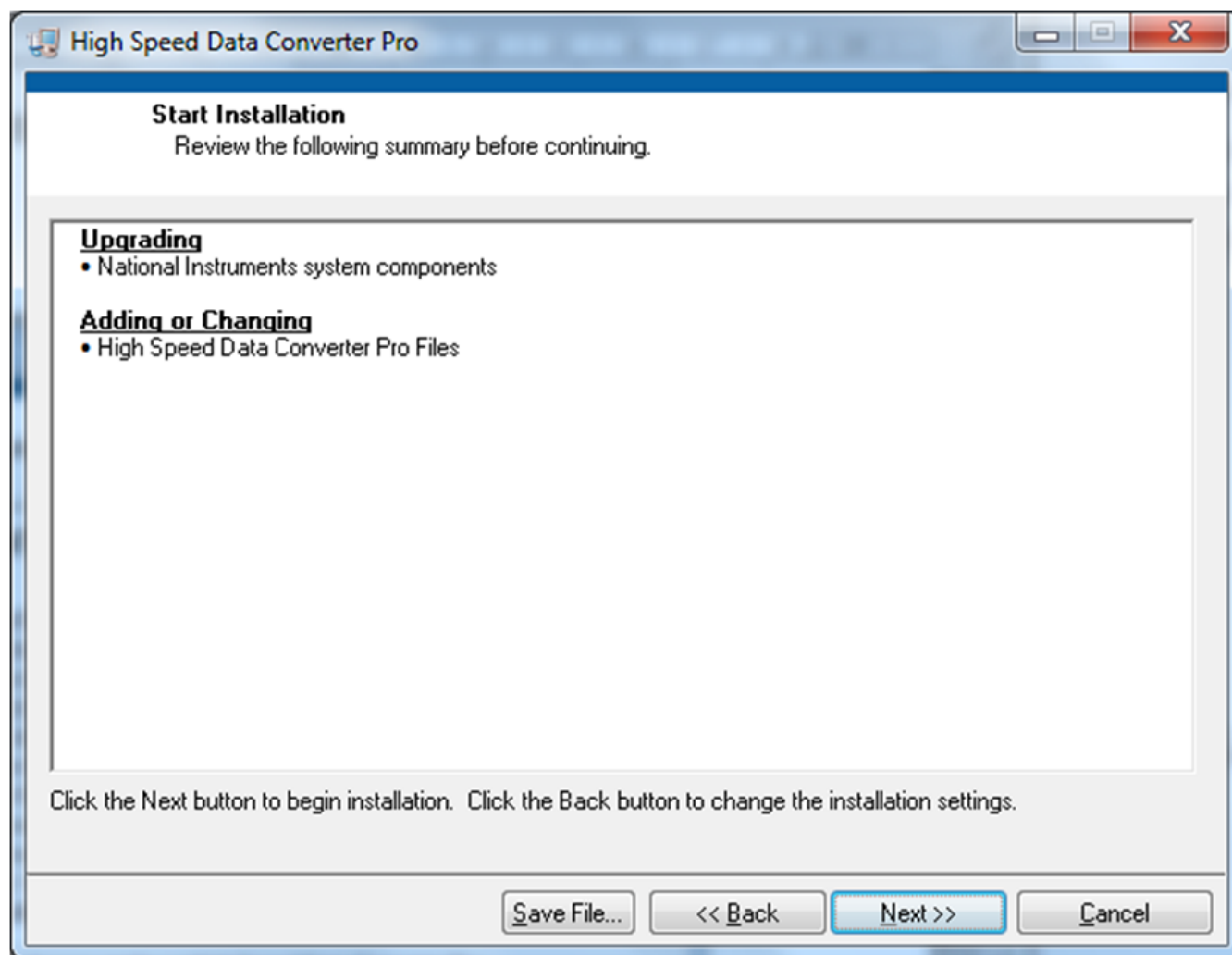


Figure 25. HSDCPro Install (Start Installation)

- The window shown in [Figure 26](#) should appear indicating that the installation is in progress.

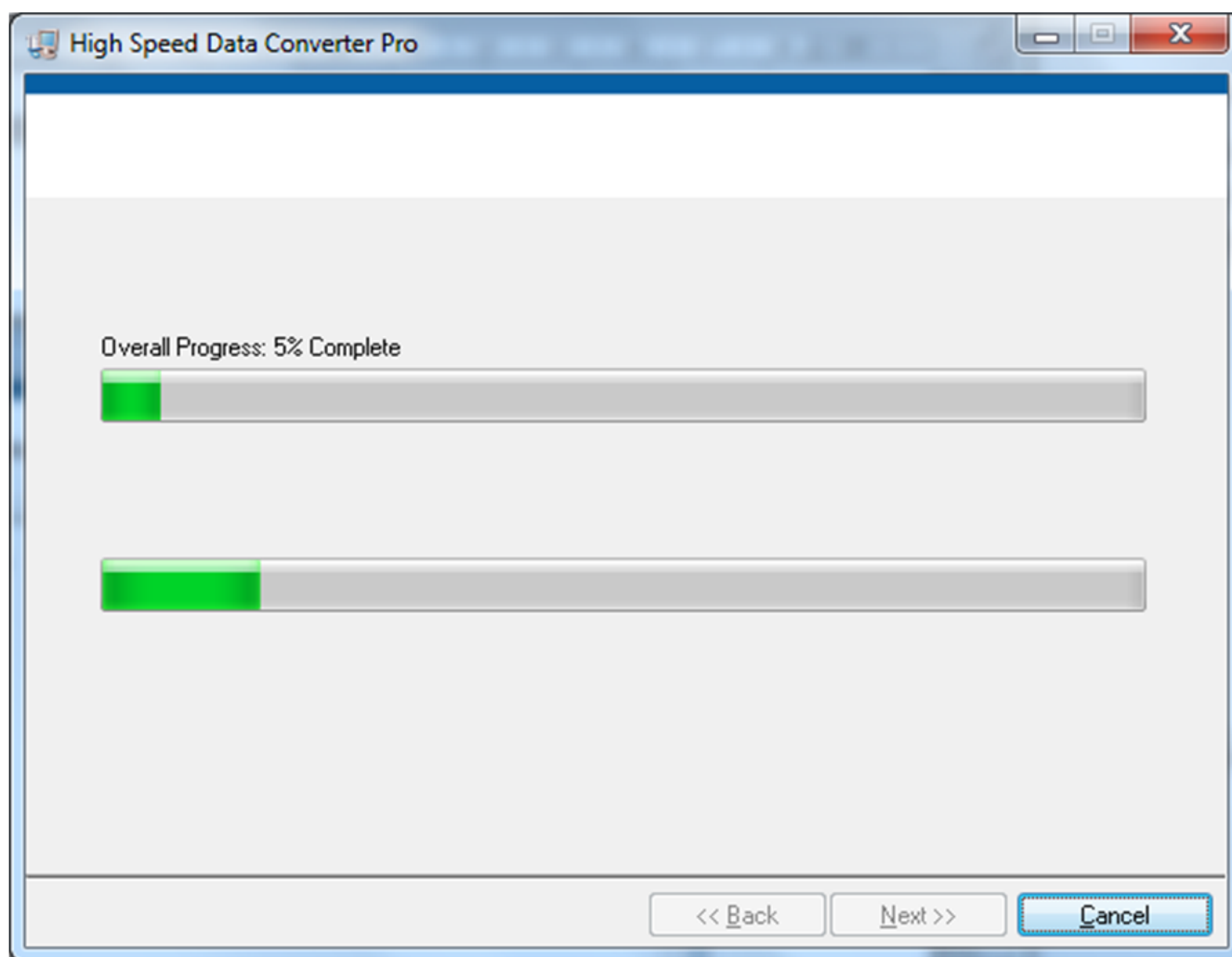


Figure 26. HSDCPro Install (Installation Progress)

- The window shown in [Figure 27](#) appears indicating *Installation Complete*. Press the *Next* button.

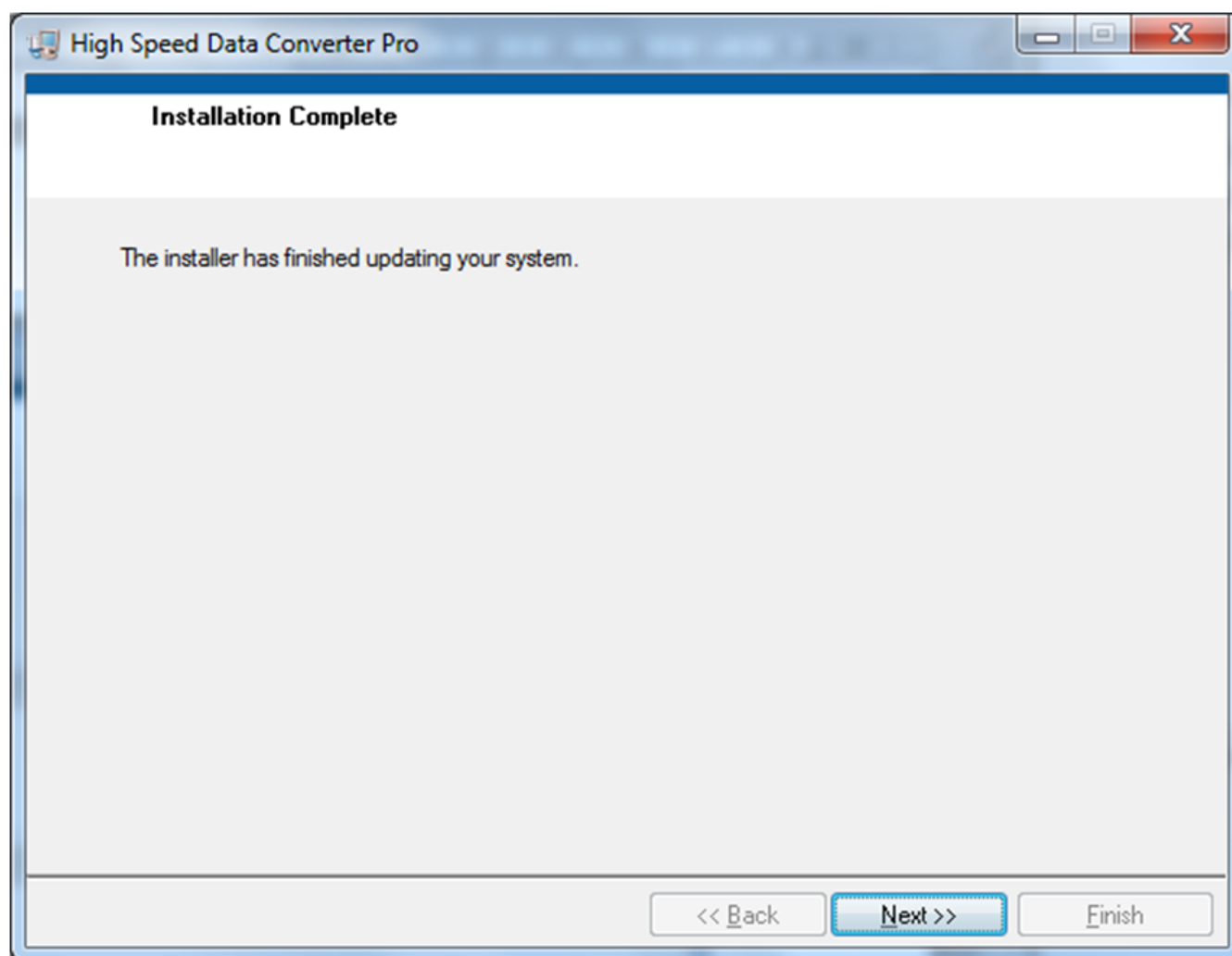


Figure 27. HSDCPro Install (Installation Complete)

- The window shown in [Figure 28](#) appears briefly to complete the process.

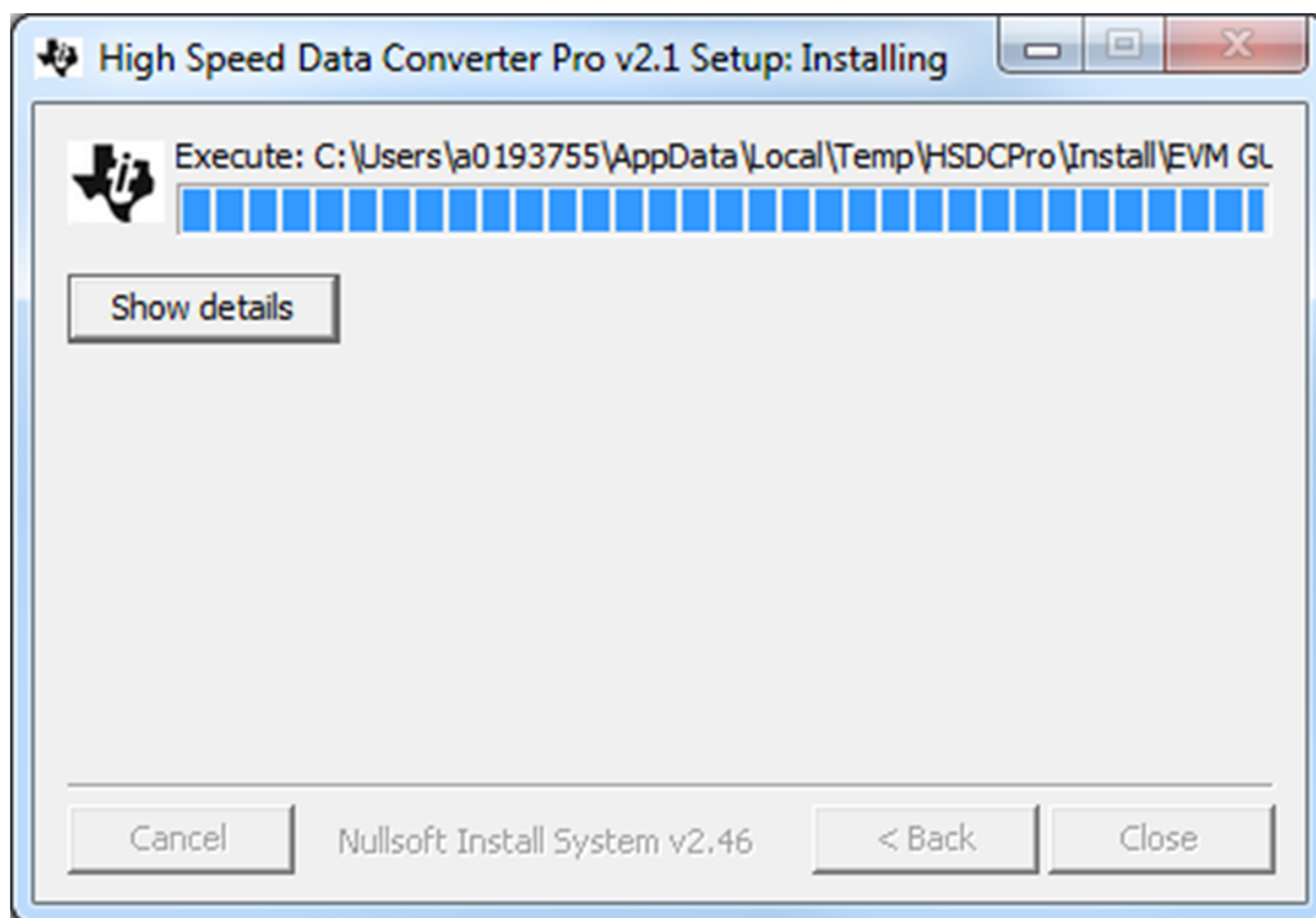


Figure 28. HSDCPro Install (h)

- As shown in [Figure 29](#) a restart might be requested depending on whether or not the PC already had the National Instruments MCR Installer. If requested, hit the *Restart* button to complete the installation.

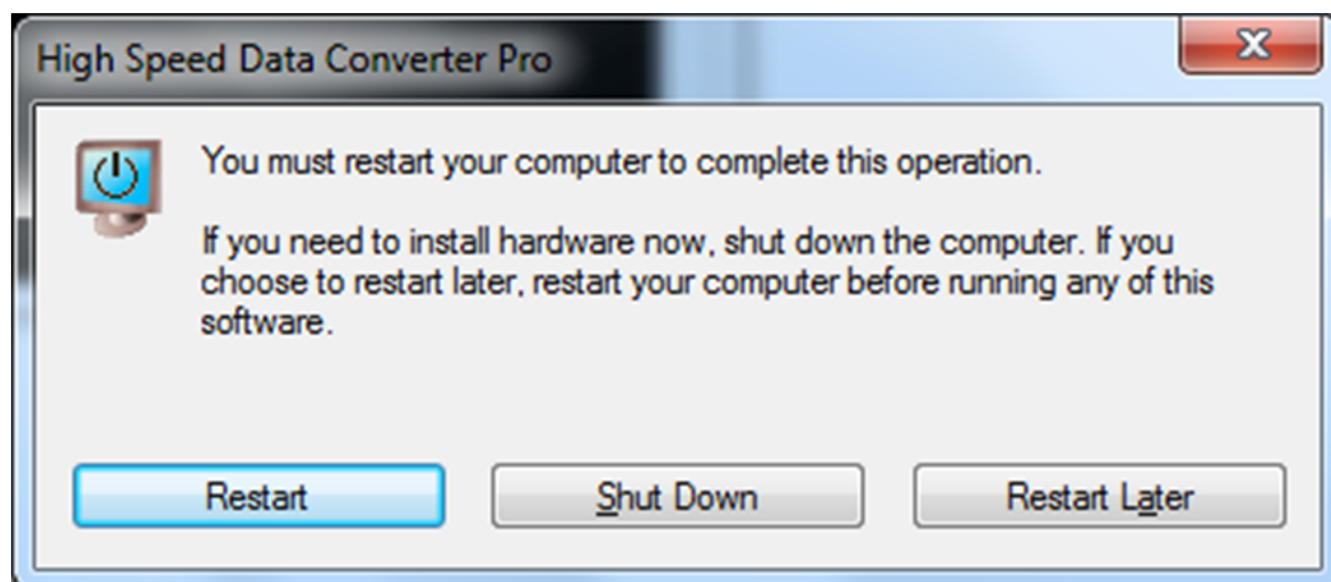


Figure 29. HSDCPro Install

EVM CONFIGURATION MODES

DEFAULT (SINGLE AMP) CONFIGURATION

Default Config (with transformer drive on 3 channels &
THS770006 on one channel (IN3A)

OPTIONAL (4 CHANNEL AMP) CONFIGURATION

THS7700 Config - THS770006-based drive on all 4 channels
Channel (IN1A, IN2A, IN3A IN4A)

 **TEXAS INSTRUMENTS**

Title

ADS5263EVM

Size

A

EDGE No : 6526733

ECR No : 2115370

Rev

A

Date:

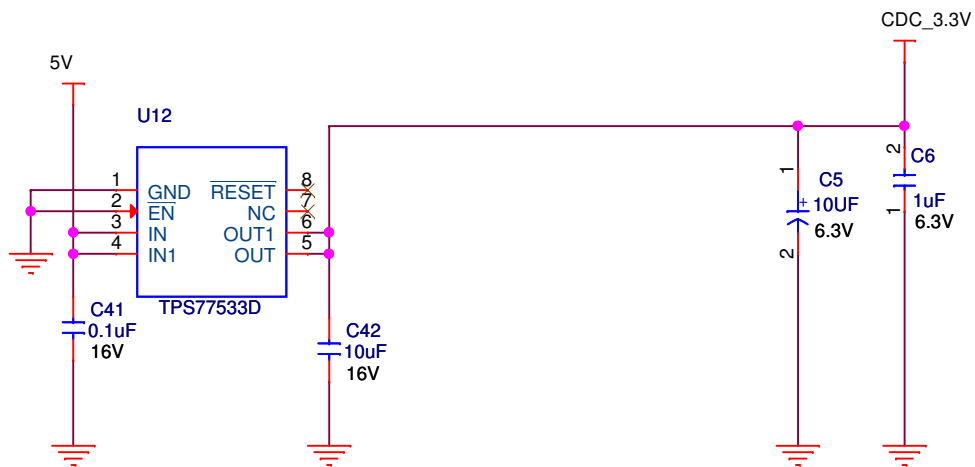
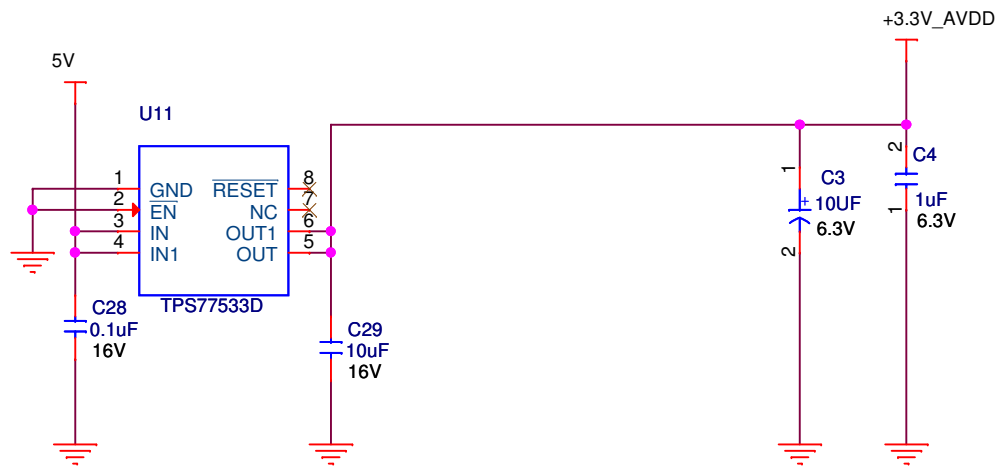
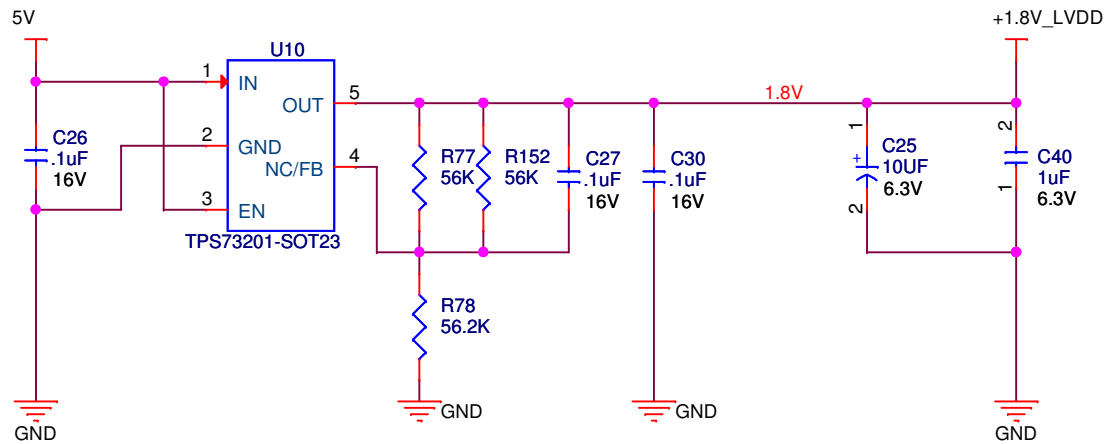
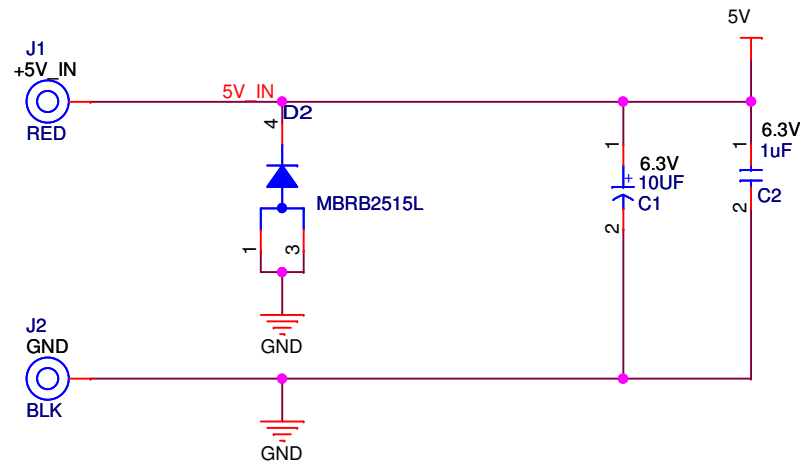
Tuesday, May 03, 2011

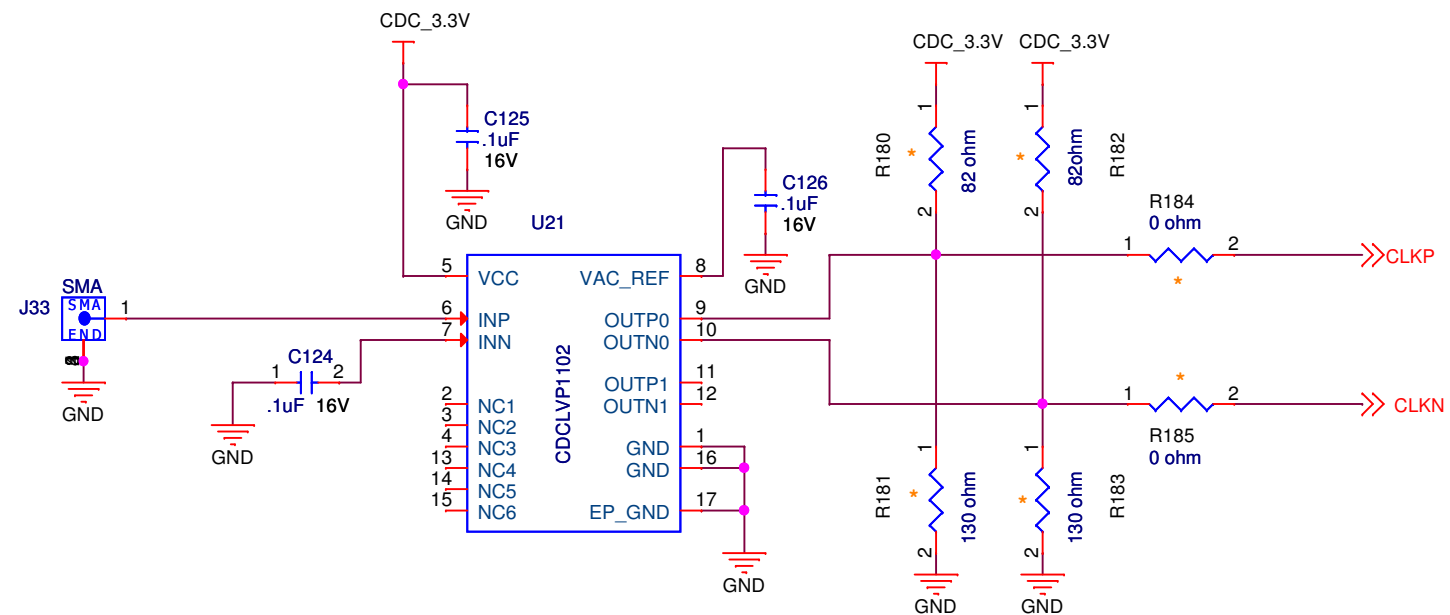
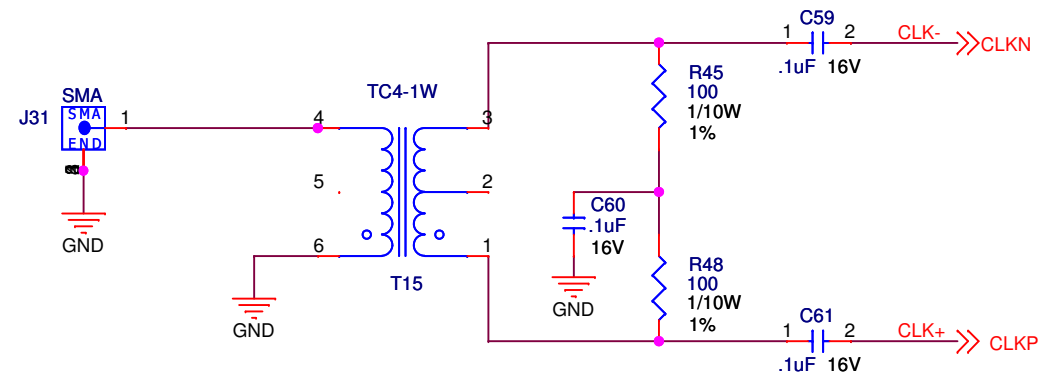
Sheet

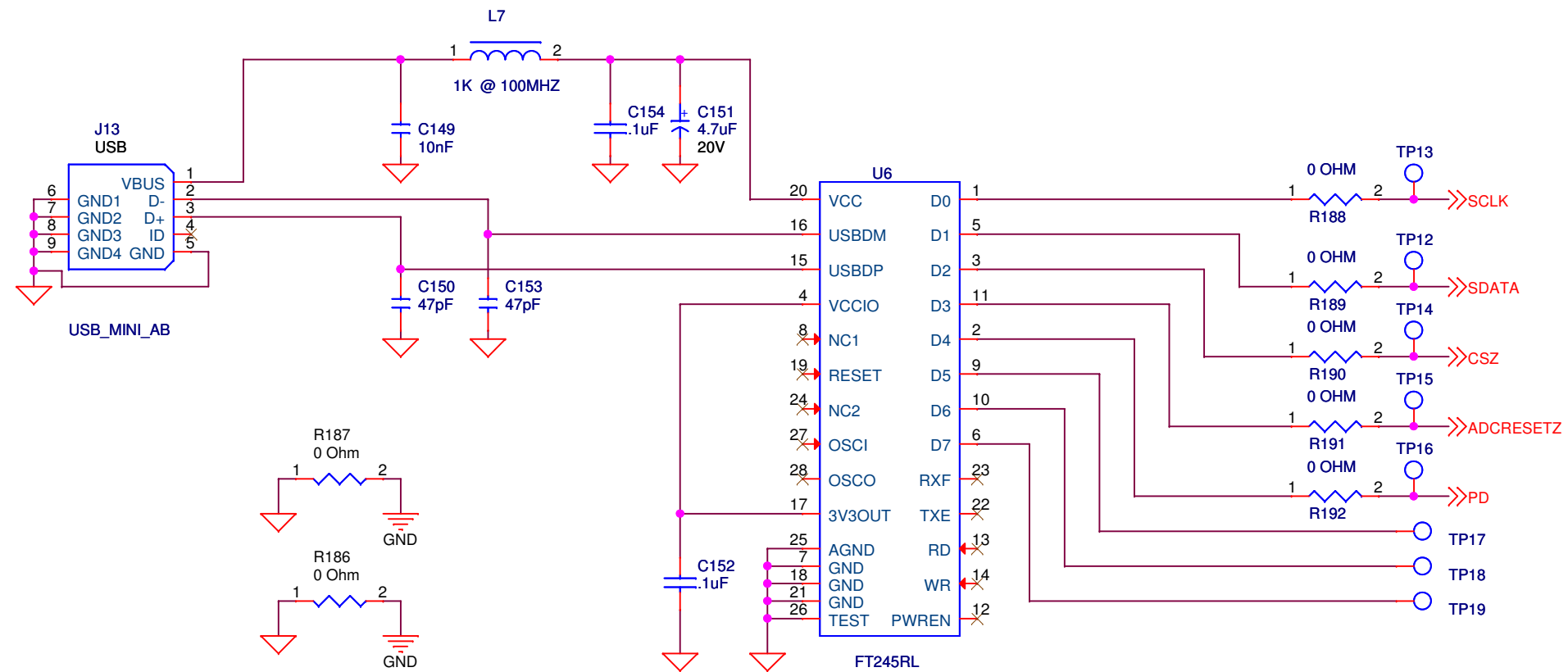
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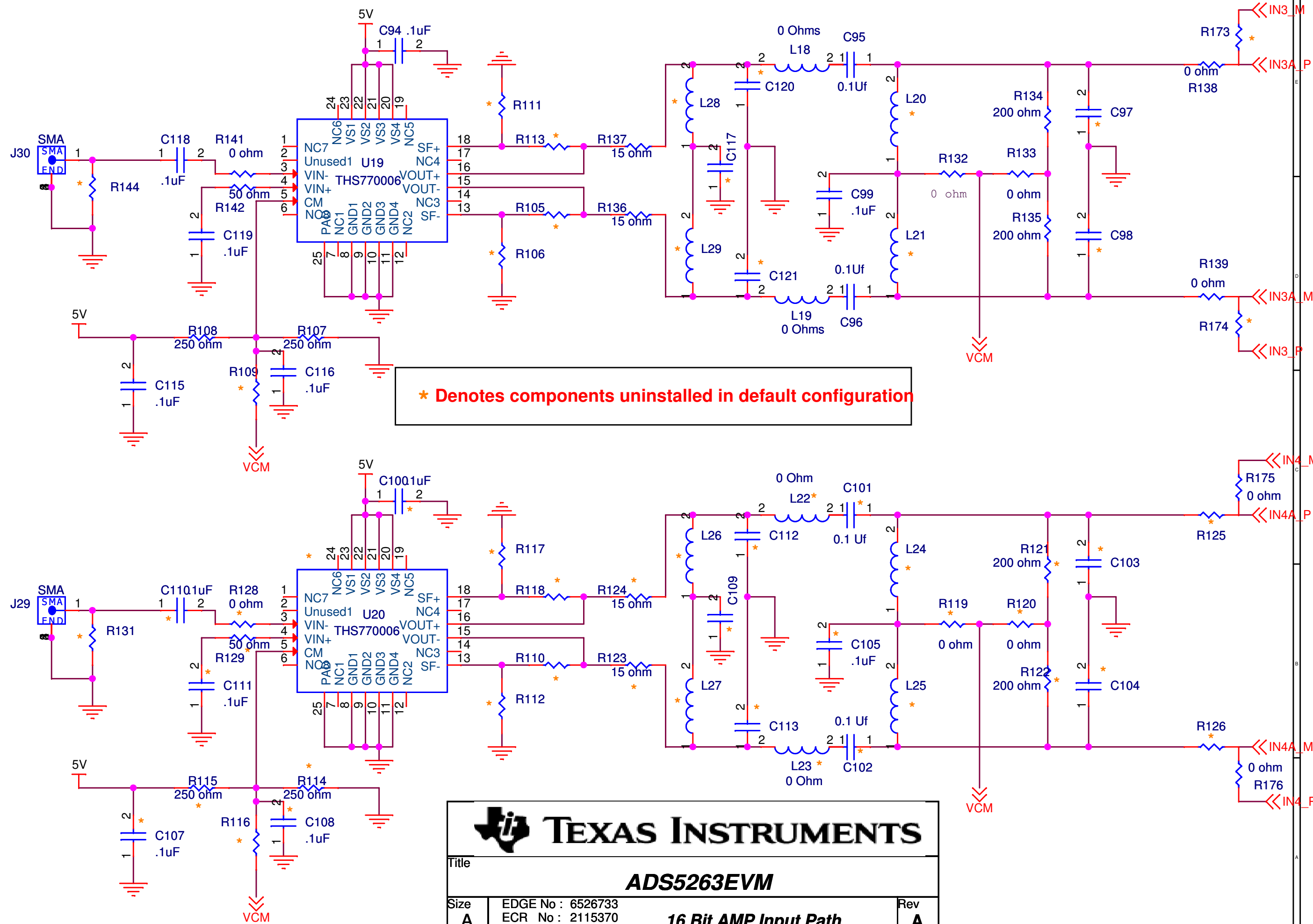
of

8







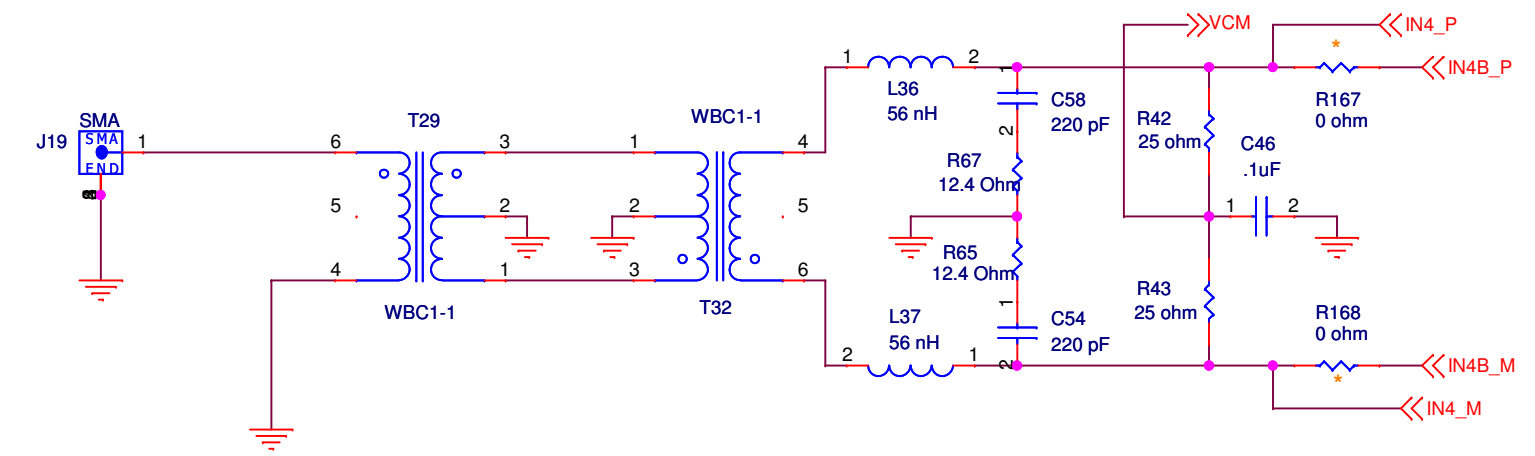
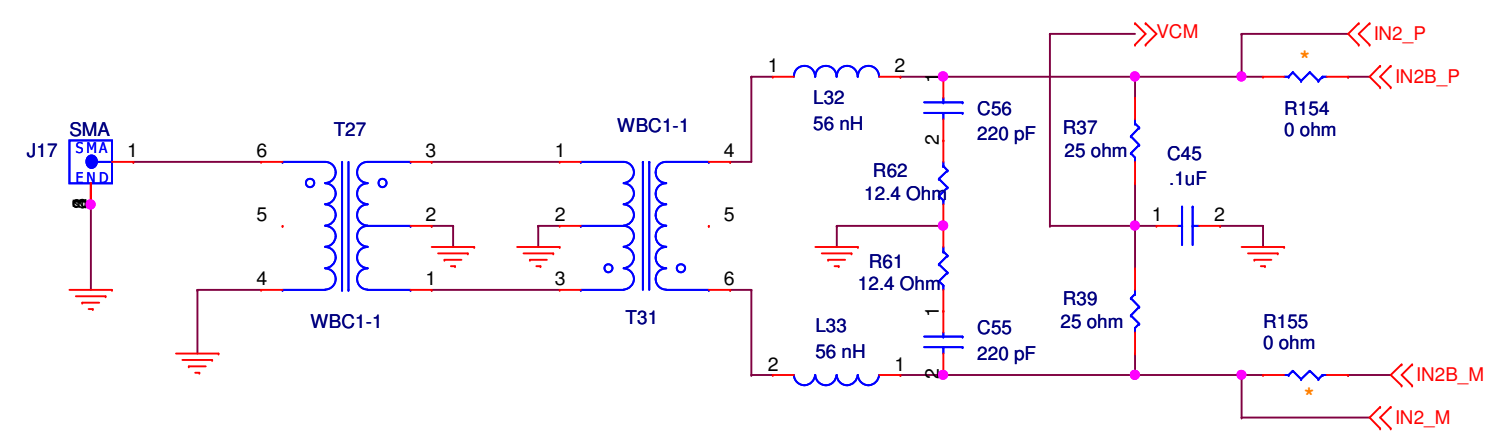
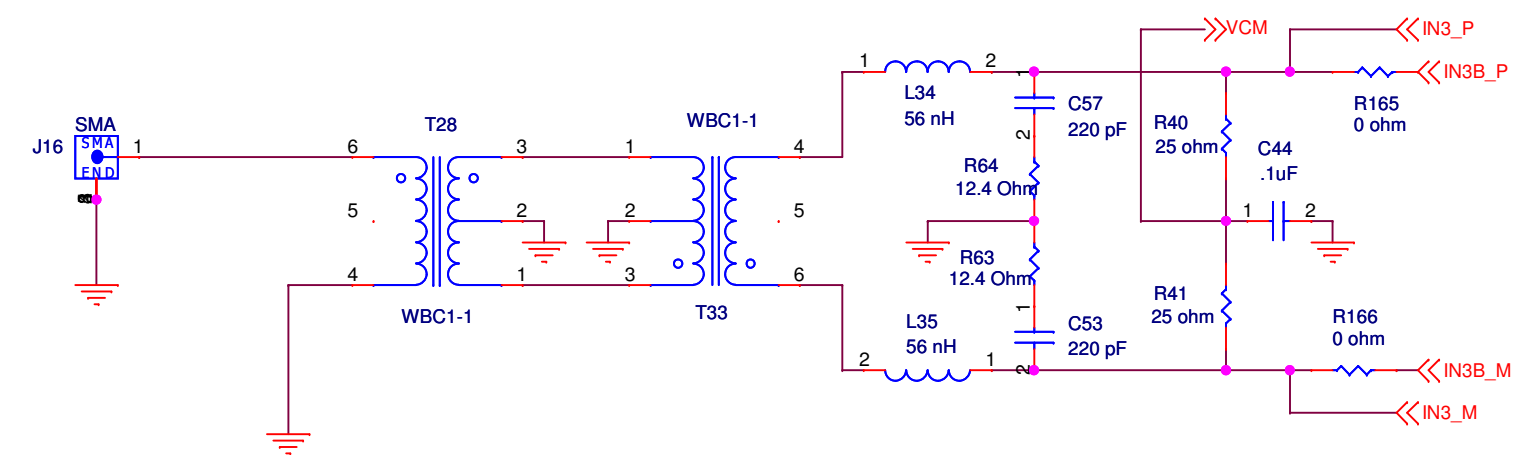
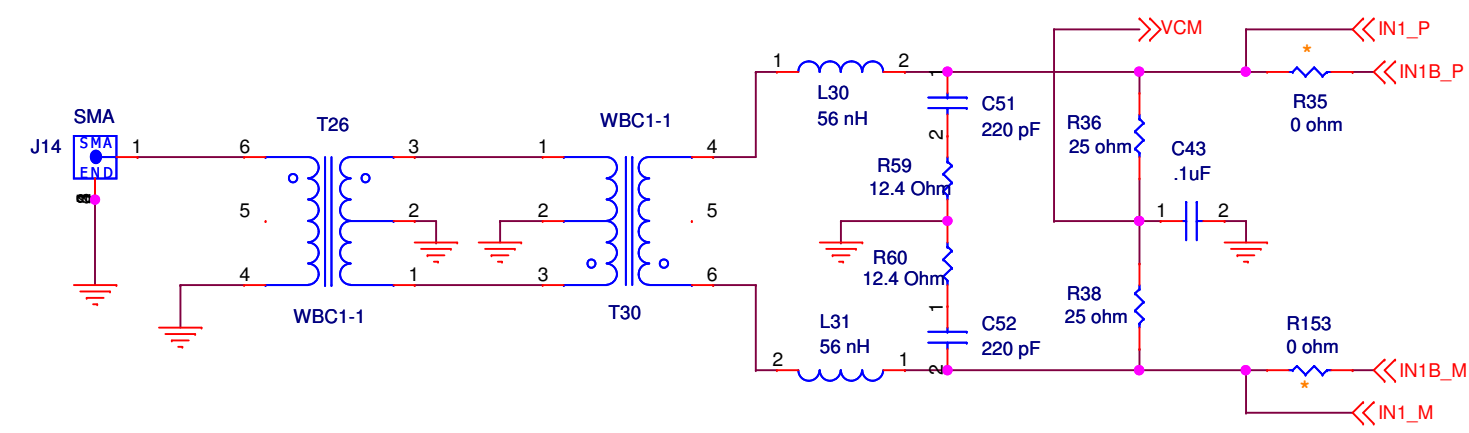




TEXAS INSTRUMENTS

Title		
ADS5263EVM		
Size	EDGE No : 6526733 ECR No : 2115370	Rev
A	16 Bit AMP Input Path	A
Date:	Tuesday, May 03, 2011	Sheet 7 of 8

* Denotes components uninstalled in default configuration



Revision History

Changes from Original (May 2011) to A Revision	Page
• Changed Appendix A to TSW1400 software.	25

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

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3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

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- *Reorient or relocate the receiving antenna.*
- *Increase the separation between the equipment and receiver.*
- *Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.*
- *Consult the dealer or an experienced radio/TV technician for help.*

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

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Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

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http://www.tij.co.jp/llds/ti_ja/general/eStore/notice_01.page

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1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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