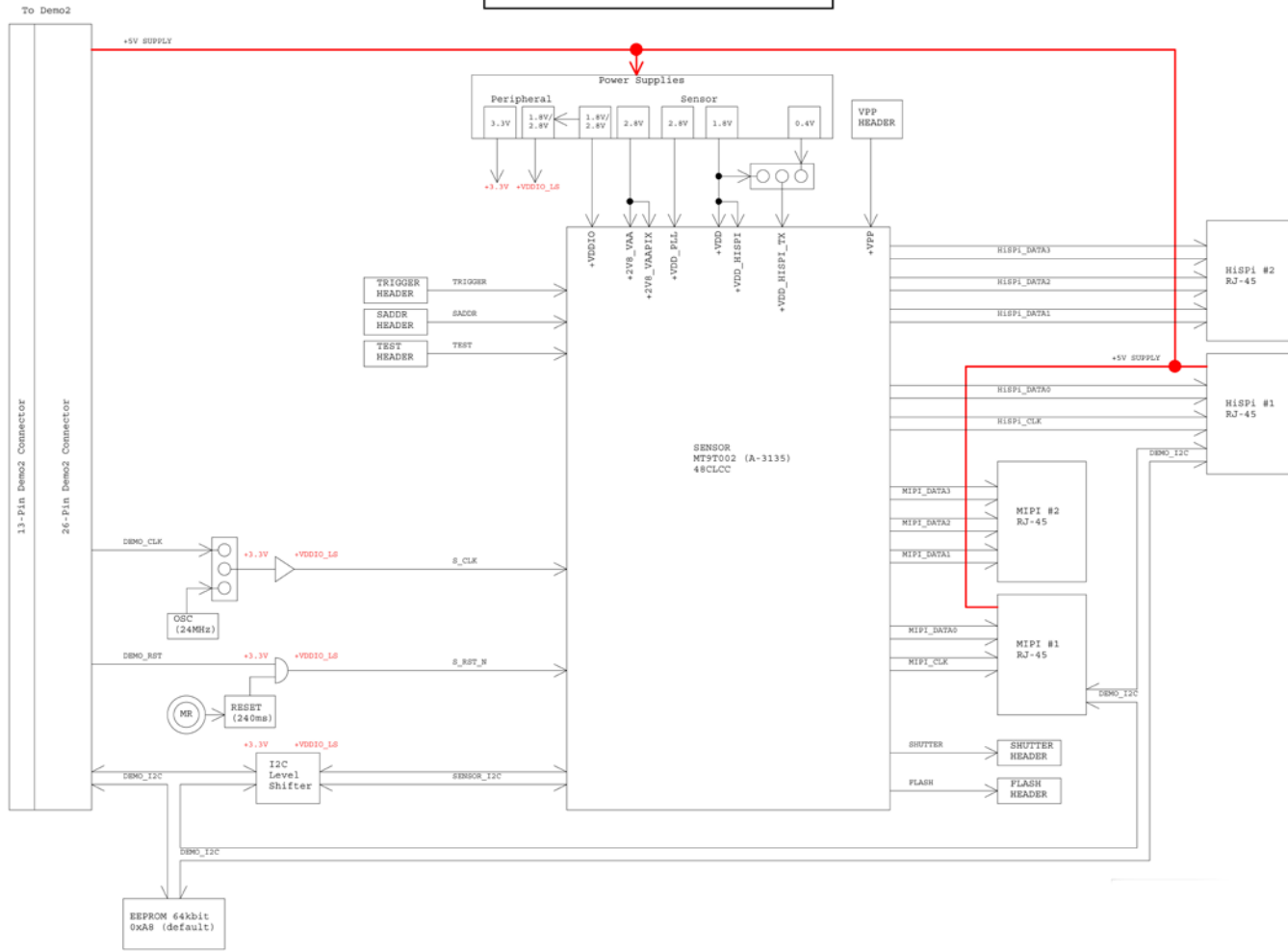
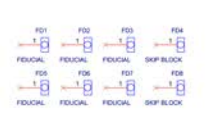
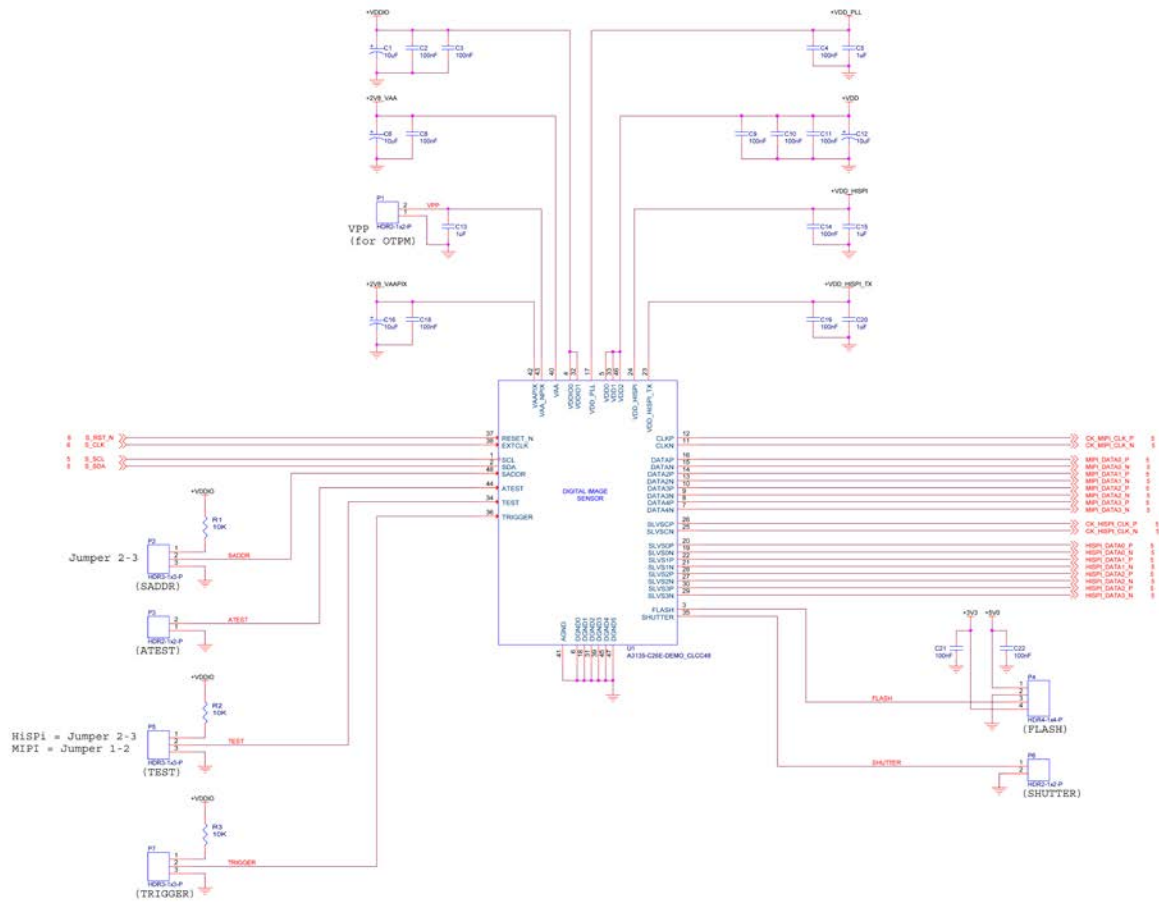


Schematic for the AR0330CM1C00SHAAH3-GEVB Evaluation Board

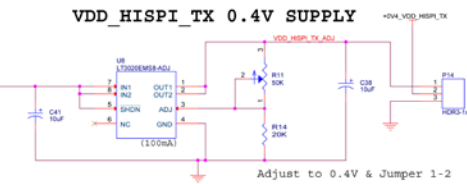
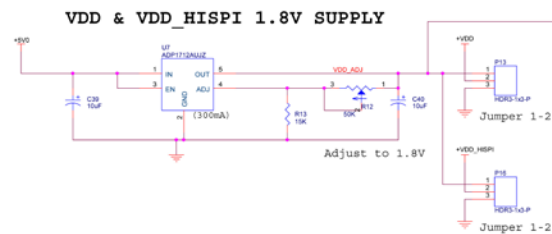
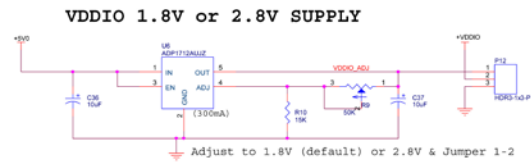
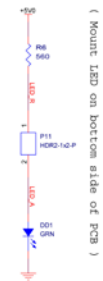
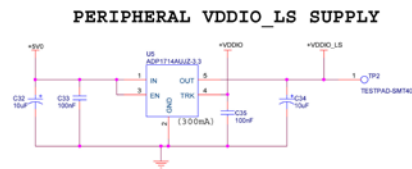
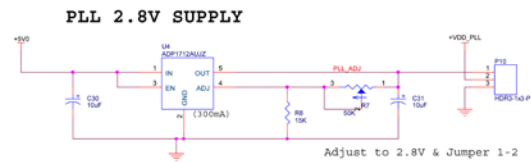
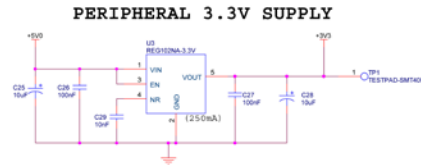
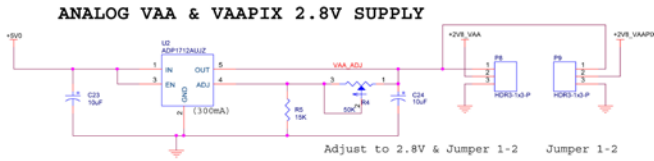
Block Diagram



Sensor - MT9T002 (A-3135)_48CLCC



Power Supply



Selection of 0.4V or 1.8V for +VDD_HISPI_TX supply

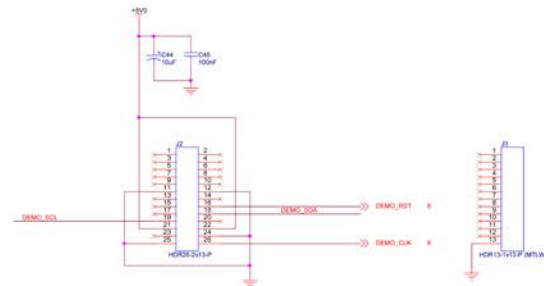
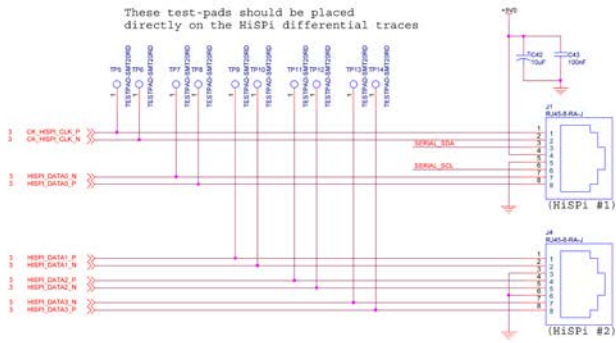


3.5	+5V	P16
3	+2V_VAA	+2V_VAA
3	+2V_VAPIX	+2V_VAPIX
3	+VDD_PLL	+VDD_PLL
3	+VDDIO	+VDDIO
3	+VDD	+VDD
3	+VDD_HISPI	+VDD_HISPI
3	+VDD_HISPI_TX	+VDD_HISPI_TX
3.6	+3V3	+3V3
3.6	+VDDIO_LS	+VDDIO_LS

External Interface

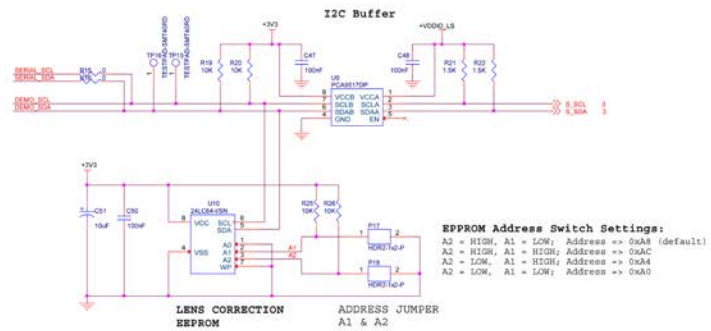
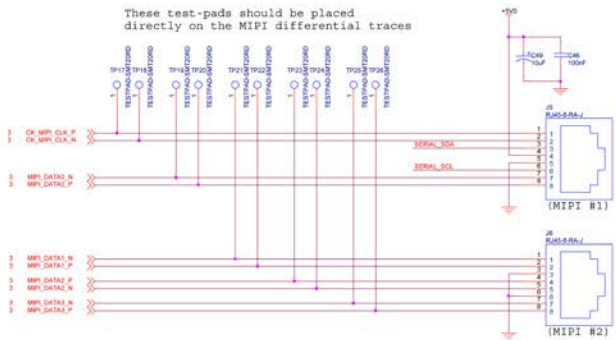
HISPI INTERFACE

These test-pads should be placed directly on the HISPI differential traces



MIPI INTERFACE

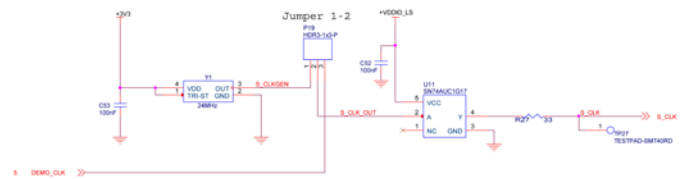
These test-pads should be placed directly on the MIPI differential traces



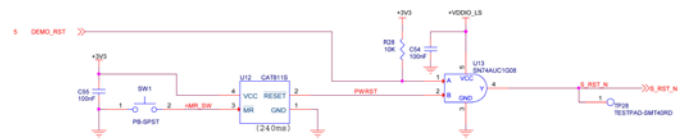


Clock and Reset

CLOCK CIRCUIT



RESET CIRCUIT



3.5	+VDD		+VDD
3.6	+VDD_VDDA		+VDD_VDDA
3.4	+VDD_VDDAPX		+VDD_VDDAPX
3.4	+VDD_FT1		+VDD_FT1
3.4	+VDDO		+VDDO
3.4	+VDDO		+VDDO
3.4	+VDDO_HSR1		+VDDO_HSR1
3.4	+VDDO_HSR1_TX		+VDDO_HSR1_TX
3.5	+VDDO_L15		+VDDO_L15
4.5	+VDDO_L15		+VDDO_L15