

Product Discontinuation Notice for Development Systems Product

XCN11007 (v1.0) January 10, 2011

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx is discontinuing certain Development Systems products.

Description

As part of standard Xilinx product review activities, the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at http://www.xilinx.com/support/documentation/customer_notices.htm.

Products Affected

- 1. LogiCORE[™] 3GPP Downlink Chip Rate
- 2. LogiCORE, SPI-3 Link Layer Interface, Multi-channel
- 3. LogiCORE, Security Monitor Part Numbers
- 4. Virtex[®]-5 LXT Gigabit Ethernet Development Kit
- 5. Xilinx® Development Systems Kits and Boards Products on While Supplies Last

Table 1: LogiCORE, 3GPP Downlink Chip Rate

| Part Number | Description | Replacement Part |
|-----------------------|---|--------------------------|
| EF-DI-DLCR-3GPP-SITE | LogiCORE, 3GPP Downlink Chip Rate, Site License | No Replacement Available |
| EFR-DI-DLCR-3GPP-SITE | LogiCORE, 3GPP Downlink Chip Rate, Site License Renewal | No Replacement Available |
| EF-DI-DLCR-3GPP-WW | LogiCORE, 3GPP Downlink Chip Rate, Worldwide License | No Replacement Available |
| EFR-DI-DLCR-3GPP-WW | LogiCORE, 3GPP Downlink Chip Rate, Worldwide License Renewal | No Replacement Available |

Table 2: LogiCORE, SPI-3 Link Layer Interface, Multi-channel

| Part Number | Description | Replacement Part |
|---------------------|---|--------------------------|
| EF-DI-POSL3MC-SITE | LogiCORE, POS-PHY L3 Link Layer Interface, Multi-Channel, Site License | No Replacement Available |
| EFR-DI-POSL3MC-SITE | LogiCORE, POS-PHY L3 Link Layer Interface, Multi-Channel, Site License Renewal | No Replacement Available |

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| Part Number | Description | Replacement Part Number |
|-------------------------------------|---|-------------------------|
| DO-DI-MIL-SECMON-GNU-PROG | LogiCORE, Security Monitor with GNU, Multi-Site, Single Project, Single Government Program License | DO-DI-MIL-SECMON-GNU-WW |
| DO-DI-MIL-SECMON-GNU-SITE | LogiCORE, Security Monitor with GNU, Single Site, Multi-Project License | DO-DI-MIL-SECMON-GNU-WW |
| DO-DI-MIL-SECMON-PROG | LogiCORE, Security Monitor without GNU, Multi-Site, Single Project, Single Government Program License | DO-DI-MIL-SECMON-GNU-WW |
| DO-DI-MIL-SECMON-SITE | LogiCORE, Security Monitor without GNU, Single Site, Multi-Project License | DO-DI-MIL-SECMON-GNU-WW |
| DO-DI-MIL-SECMON-WW | LogiCORE, Security Monitor without GNU, Multi-Site, Multi-Project License | DO-DI-MIL-SECMON-GNU-WW |
| DX-DI-MIL-SECMON-GNU-SITE-TO- WW | LogiCORE, Security Monitor with GNU, Single Site, Multi-Project to Multi-Site, Multi-Project License | DO-DI-MIL-SECMON-GNU-WW |
| DX-DI-MIL-SECMON-SITE-TO-WW | LogiCORE, Security Monitor without GNU, Single Site, Multi-Project to Multi-Site, Multi-Project License | DO-DI-MIL-SECMON-GNU-WW |

Table 3: LogiCORE, Security Monitor Part Numbers

Table 4: Virtex-5 LXT Gigabit Ethernet Development Kit

| Part Number | Description | Replacement Part |
|---------------------|---|----------------------|
| HW-V5GBE-DK-UNI-G | Virtex- 5 LXT FPGA Gigabit Ethernet Development Kit | <u>EK-V6-ML605-G</u> |
| HW-V5GBE-DK-UNI-G-J | Virtex-5 LXT FPGA Gigabit Ethernet Development Kit, Japan Specific | EK-V6-ML605-G-J |

| Part Number | Description | Suggested Replacement |
|-------------------------|--|---------------------------------|
| HW-AFX-BERG-EPHY | Add-On Ethernet Module | No Xilinx Suggested Replacement |
| HW-AFX-SMA-NQSL-G | NELCO QUAD Serial Loop (Lead-Free) | No Xilinx Suggested Replacement |
| HW-AFX-SMA-SATA | SMA to Serial ATA (SATA) Conversion Module | No Xilinx Suggested Replacement |
| HW-PCIE-SMA-G | PCIe® to SMA Conversion Module (Lead-Free) | No Xilinx Suggested Replacement |
| HW-AFX-FF665-500-G | Virtex®-5 Socketed Board Supporting Devices in FF665 Package (Lead-Free) | No Xilinx Suggested Replacement |
| HW-V4-ML401-UNI-G | Virtex Virtex Evaluation Board | <u>EK-V6-ML605-G</u> |
| HW-V4-ML401-UNI-G-J | Virtex-4 General Purpose/FPGA Evaluation Board, Japan Specific | EK-V6-ML605-G-J |
| HW-V4-ML423-UNI-G | Virtex-4 FX100 Characterization Board (Lead- Free) | <u>CK-V6-ML623-G</u> |
| HW-V4-ML423-UNI-G-J | Virtex-4 FX100 Characterization Board, Japan Specific (Lead-Free) | <u>CK-V6-ML623-G-J</u> |
| HW-V5-ML561-UNI-G | Virtex-5 Memory Development Board | No Xilinx Suggested Replacement |
| HW-V5-ML561-UNI-G-J | Virtex-5 Memory Development Board, Japan Specific | No Xilinx Suggested Replacement |
| HW-V5-ML521-UNI-G | Virtex-5 LX50T Characterization Board (Lead- Free) | <u>CK-V6-ML623-G</u> |
| HW-V5-ML521-UNI-G-J | Virtex-5 LX50T Characterization Board, Japan Specific (Lead-Free) | CK-V6-ML623-G-J |
| HW-V5-ML525-UNI-G | Virtex-5 LXT ML525 RocketIO™ Characterization Platform | <u>CK-V6-ML623-G</u> |
| HW-V5-ML525-UNI-G-J | Virtex-5 LXT FPGA ML525 RocketIO Characterization Platform, Japan Specific | <u>CK-V6-ML623-G-J</u> |
| HW-V4-ML410-UNI-G | Virtex-4 Embedded System Development Board (Lead-Free) | <u>HW-V5-ML510-G</u> |
| HW-V4-ML410-UNI-G-J | Virtex-4 Embedded System Development Board, Japan Specific (Lead-Free) | <u>HW-V5-ML510-G-J</u> |
| HW-V5-ML550-UNI-G | Virtex-5 Networking Interface Board (Lead- Free) | No Xilinx Suggested Replacement |
| HW-V5-ML523-UNI-G | Virtex-5 LX110T Characterization Board (Lead- Free) | <u>CK-V6-ML623-G</u> |
| HW-V5-ML523-UNI-G-J | Virtex-5 LX110T Characterization Board, Japan Specific (Lead-Free) | CK-V6-ML623-G-J |
| HW-V5-ML523-FXT-UNI-G | Xilinx Virtex-5 FXT FF1136 RocketIO GTX Characterization Platform | <u>CK-V6-ML623-G</u> |
| HW-V5-ML523-FXT-UNI-G-J | Xilinx Virtex-5 FXT FF1136 RocketIO GTX Characterization Platform, Japan Specific | CK-V6-ML623-G-J |

Table 5: Xilinx Development Systems Products on While Supplies Last

NOTE:

1. Reference designs provided with all the above Development Systems are intended to be used as is.

2. The last version of design environment to support the reference designs is ISE[®] 11.1.

Key Dates and Ordering Information

LogiCORE, 3GPP Downlink Chip Rate

Product discontinued November 22nd, 2010. Existing Warranty Contracts will be honored after product discontinuance but will not be renewable.

LogiCORE, SPI-3 Link Layer Interface, Multi-channel

Final orders accepted until February 11th, 2011. Final orders shipped by February 18th, 2011. No technical support available after February 11th, 2012. Existing Warranty Contracts will be honored after product discontinuance but will not be renewable.

LogiCORE, Security Monitor Part Numbers

Product discontinued January 11th, 2011. Please use part number DO-DI-MIL-SECMON-GNU-WW for all new orders effective immediately. Existing Warranty Contracts will be honored after product discontinuance but will not be renewable.

Virtex-5 LXT Gigabit Ethernet Development Kit

Final orders accepted until March 1st, 2011. Final orders shipped by March 15th, 2011. No technical support available 6 months after product shipped to customer.

Multiple Xilinx Development Systems Kits and Boards

Available While Supplies Last: The products listed are in stock, but will NOT be replenished when inventory has been depleted. When this occurs the product will be discontinued and no longer available for order.

Six months after the obsolescence process begins no technical support will be available for the product.

Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis with the non-cancellable and non-returnable (NCNR) condition. For additional information or questions, contact your local <u>Xilinx Sales representative</u> or <u>Distributor</u>.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<u>http://www.xilinx.com/support</u>). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to <u>Xilinx Answer Record</u> <u>18683</u>.

Revision History

The following table shows the revision history for this document:

| Date | Version | Description of Revisions |
|----------|---------|--------------------------|
| 01/10/11 | 1.0 | Initial release. |

Notice of Disclaimer

THE XILINX HARDWARE FPGA AND CPLD DEVICES REFERRED TO HEREIN ("PRODUCTS") ARE SUBJECT TO THE TERMS AND CONDITIONS OF THE XILINX LIMITED WARRANTY WHICH CAN BE VIEWED AT http://www.xilinx.com/warranty.htm. THIS LIMITED WARRANTY DOES NOT EXTEND TO ANY USE OF PRODUCTS IN AN APPLICATION OR ENVIRONMENT THAT IS NOT WITHIN THE SPECIFICATIONS STATED ON THE XILINX DATA SHEET. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE. PRODUCTS ARE NOT DESIGNED OR INTENDED TO BE FAIL-SAFE, OR FOR USE IN ANY APPLICATION REQUIRING FAIL-SAFE PERFORMANCE, SUCH AS LIFESUPPORT OR SAFETY DEVICES OR SYSTEMS, OR ANY OTHER APPLICATION THAT INVOKES THE POTENTIAL RISKS OF DEATH, PERSONAL INJURY OR PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). USE OF PRODUCTS IN CRITICAL APPLICATIONS IS AT THE SOLE RISK OF CUSTOMER, SUBJECT TO APPLICABLE LAWS AND REGULATIONS. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.