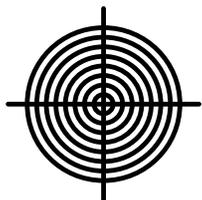
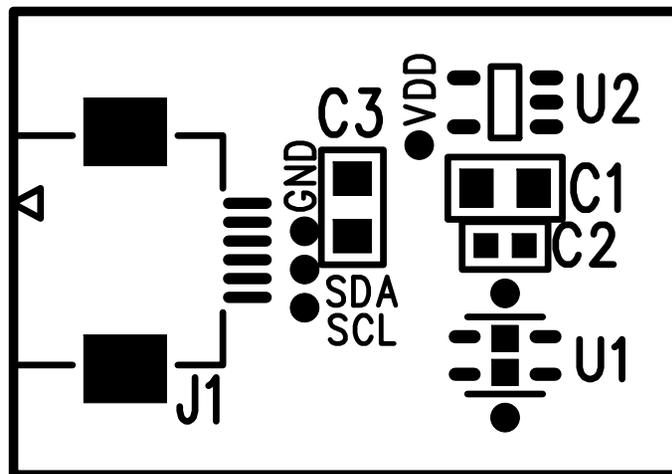
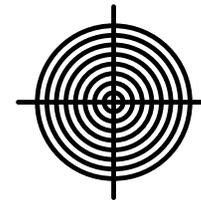
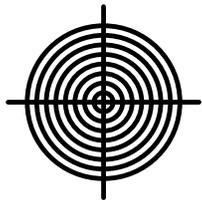
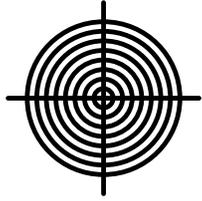
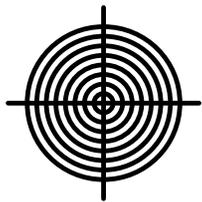
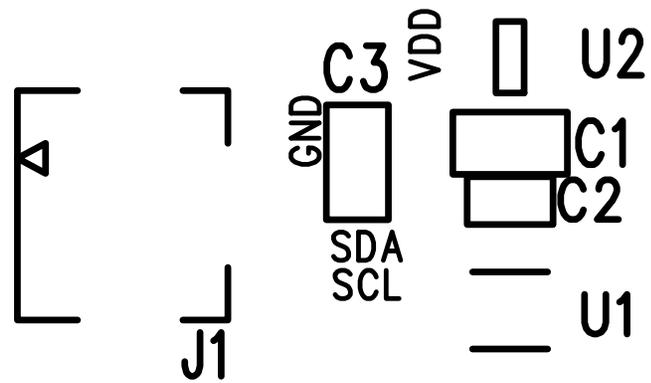
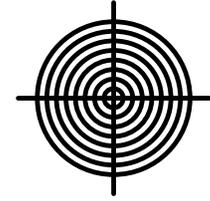


PRIMARY SILKSCREEN

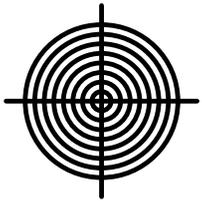
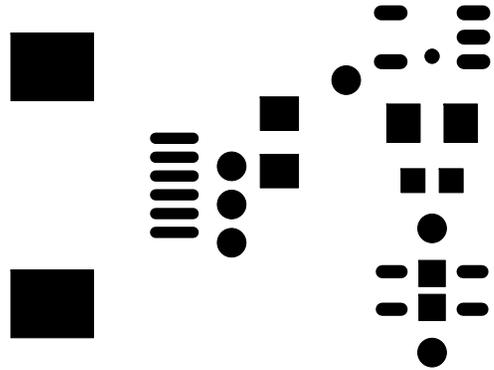
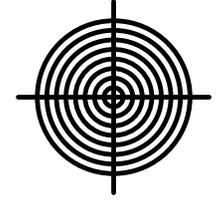
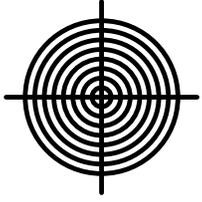




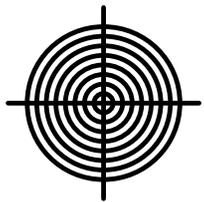
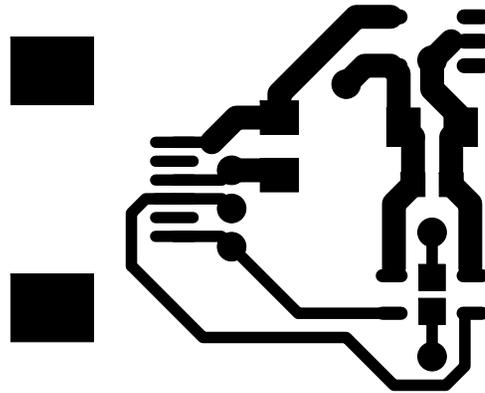
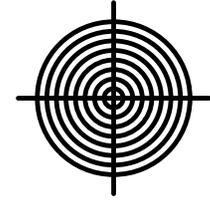
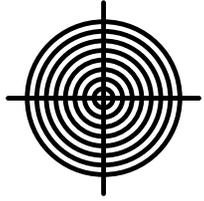
PRIMARY SILKSCREEN



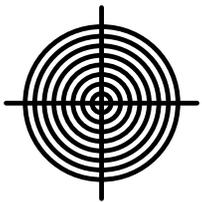
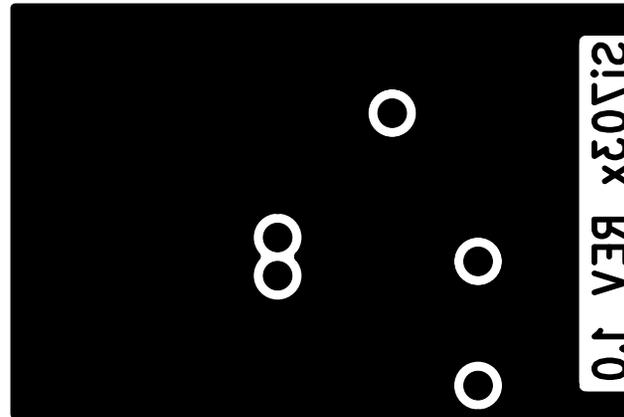
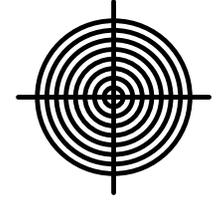
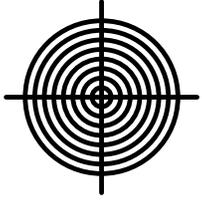
PRIMARY SOLDER MASK



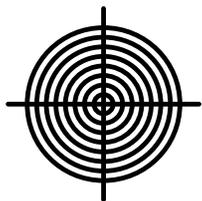
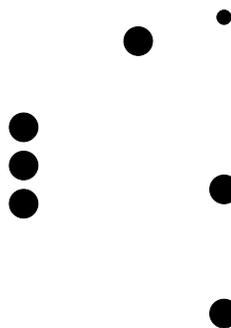
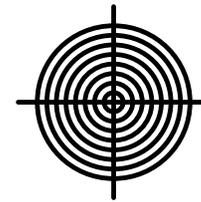
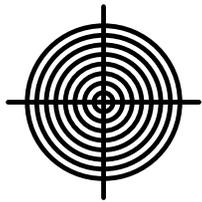
PRIMARY SIDE

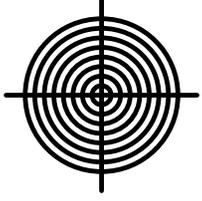


SECONDARY SIDE

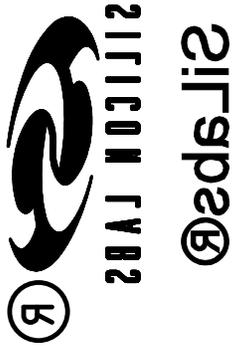
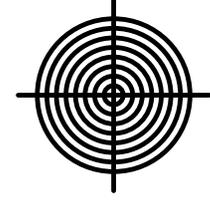


SECONDARY SOLDER MASK





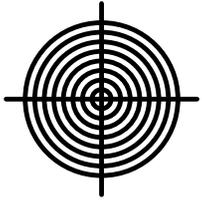
SECONDARY SILKSCREEN



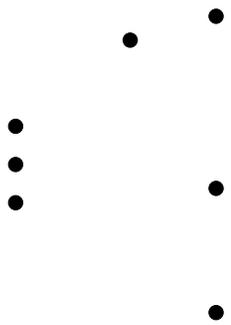
Silabs®



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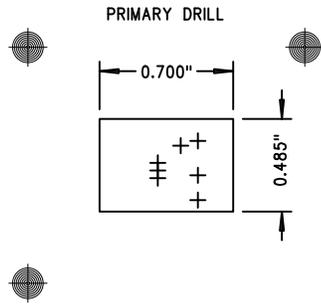






NOTES : UNLESS OTHERWISE SPECIFIED

1. MANUFACTURE IN ACCORDANCE WITH IPC-6012, TYPE 3, CLASS 2.
2. END PRODUCT FEATURES SHALL NOT VARY MORE THAN 20% FROM ARTWORK ORIGINALS.
3. LAMINATE MATERIAL SHALL BE AS PER IPC-4101/26, COLOR NATURAL.
4. COPPER WEIGHT SHALL BE 1.0 OZ./SQ. FT. BEFORE PLATING.
5. ALL PLATED THROUGH HOLES SHALL HAVE A MINIMUM OF 0.001" COPPER.
6. DRILL HOLE TOLERANCE AFTER PLATING SHALL BE ± 0.003 ".
7. MINIMUM ANNULAR RING SHALL BE 0.001".
8. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE 0.003".
9. FINAL PCB THICKNESS SHALL BE 0.062" $\pm 10\%$.
10. WARP/TWIST SHALL NOT EXCEED 1.0%
11. FINISH SHALL BE LPI, GREEN S.M.O.B.C., BALANCE ENIG.
12. SILKSCREEN WITH NONCONDUCTIVE WHITE EPOXY INK.
13. REFERENCE ADDITIONAL FAB NOTES IN FILE README.TXT



LAYER STACKUP FILE NAMES

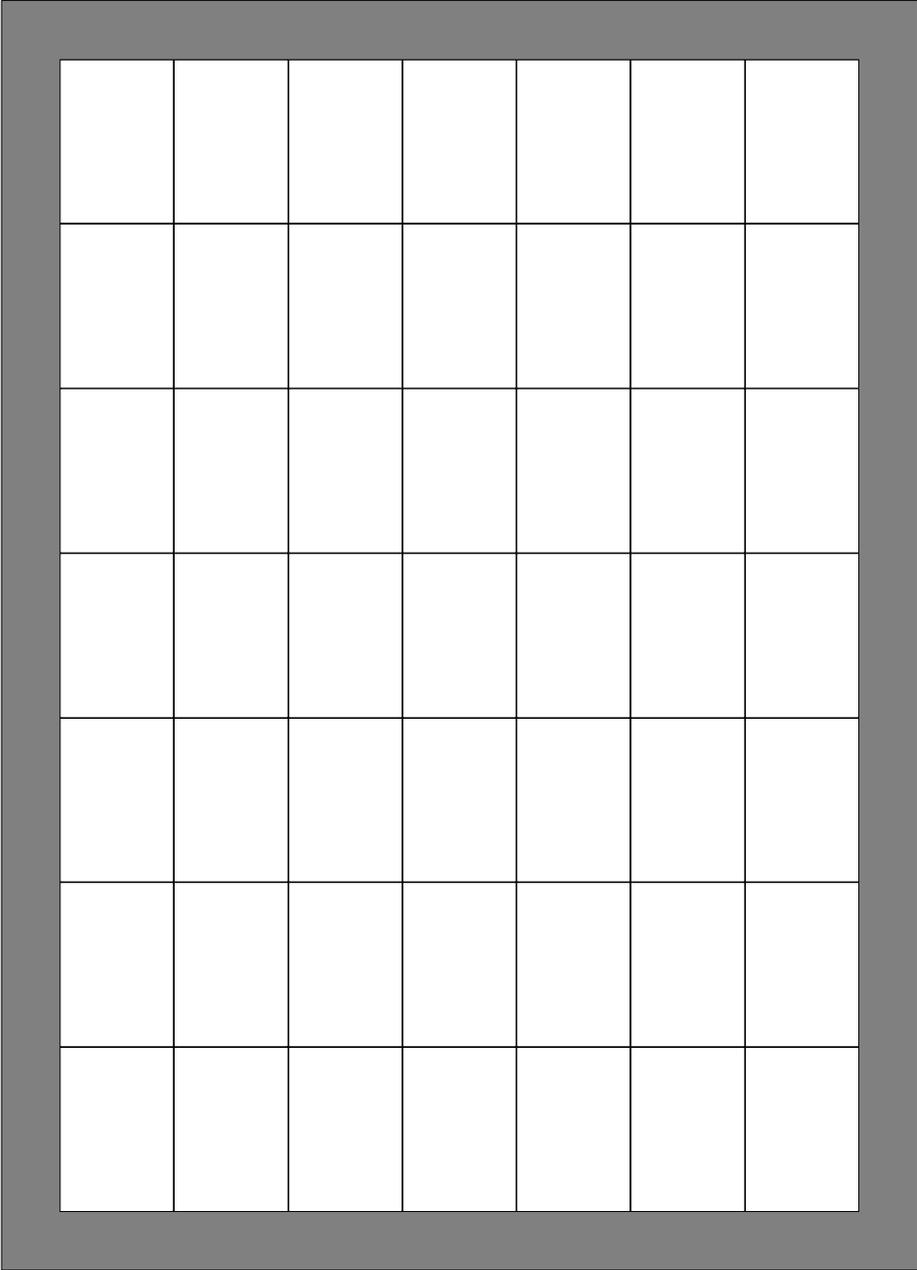
| | |
|----------------------|--------------|
| PRIMARY SILKSCREEN | 7034_PSS.PHO |
| PRIMARY SOLDERMASK | 7034_PSM.PHO |
| PRIMARY SIDE | 7034_PRI.PHO |
| SECONDARY SIDE | 7034_SEC.PHO |
| SECONDARY SOLDERMASK | 7034_SSM.PHO |
| SECONDARY SILKSCREEN | 7034_SSS.PHO |

SCALE: NONE

| SIZE | QTY | SYM | PLT | TOOL | TOL |
|-------|-----|-----|-----|------|-----------|
| 0.015 | 7 | + | P | 1 | +0/-0.015 |

| | | | | | |
|--|--------------------|---------------------------|--|---------------------|--------------|
| UNLESS OTHERWISE SPECIFIED | | | COMPANY: | | |
| DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH DIMENSIONS IN BRACKETS [] ARE IN MILLIMETERS INTERPRET DRAWING PER MIL-D-1000 | | |  400 W Cesar Chavez AUSTIN, TX 78701 (512)416-8500 www.silabs.com | | |
| TOLERANCES | | | THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SHALL NOT BE DUPLICATED OR USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED IN WHOLE OR IN PART, WITHOUT THE WRITTEN CONSENT OF SILICON LABORATORIES, INC.. | | |
| HOLE TOLERANCES PER 78027 | | | NAME: Si7034 | | |
| DECIMALS .XX +/- .XXX +/- | ANGLES +/- | SURFACES MICROINCHES ✓ | DESIGN | JG | 07NOV2013 |
| PART TO BE FREE OF BURRS | | | LAYOUT | CT | 07NOV2013 |
| BREAK EDGES MAX | BEND RADIUS MAX | BEND RELIEF MAX | SIZE A PART NUMBER: . | | |
| DO NOT SCALE DRAWING | | | SCALE 1:1 | FABRICATION DRAWING | SHEET 1 OF 1 |

REV : 1.0



Si703x Rev 1.0

Size:
Array: 3.895 x 5.4
Part: 0.485 x 0.7

Parts On Array:
49 Parts

Matrix:
7 x 7

Spacing:
0.0 x 0.0

Array Borders:
Left: 0.25 Right: 0.25
Top: 0.25 Bottom: 0.25

Notes:

Please add 4, 0.125" NP tooling holes located 0.125" from tab corners and 3, 40/120 fiducials located 0.25" from tooling holes.