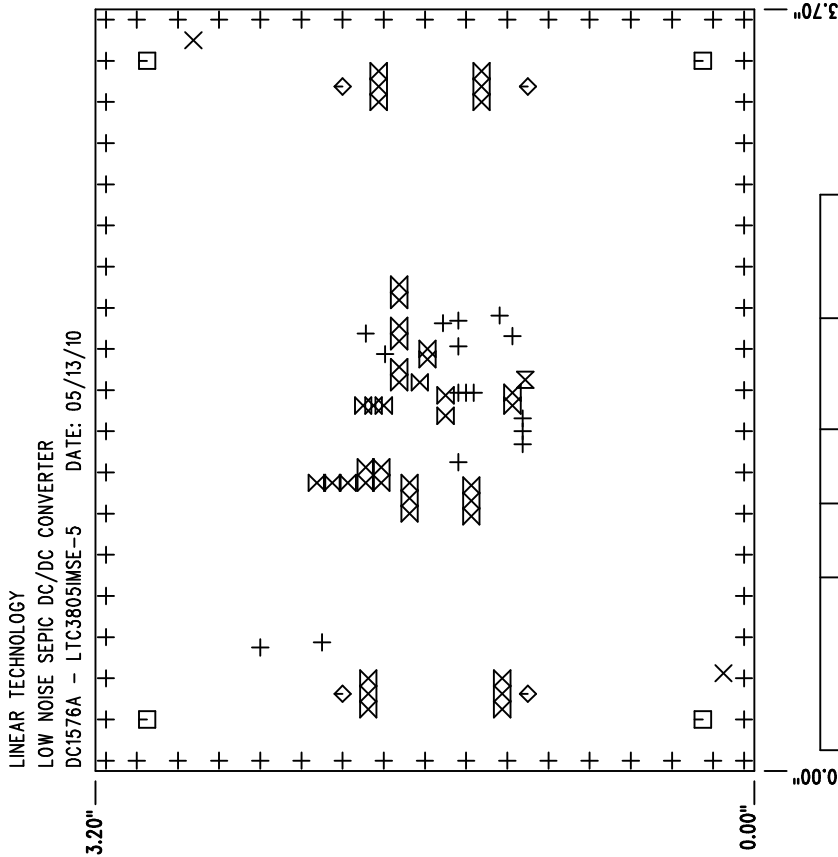
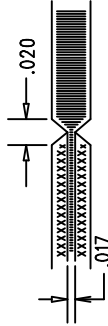
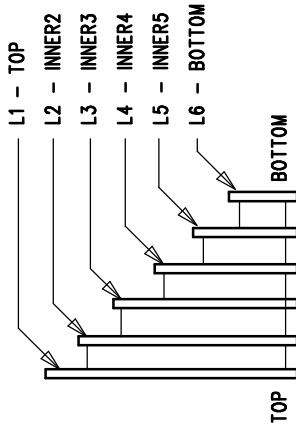


REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG.
-	2	PRODUCTION	GORAN P.
		DATE	05-13-10

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
5. FINISH: -SMBBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:

LAYER STRUCTURE



SIZE	QTY	SYM	PLATED	TOL
0.015	84	+	YES	+/-0.003
0.07	2	X	NO	+/-0.003
0.188	4	□	YES	+/-0.003
0.094	4	◇	YES	+/-0.003
0.02	41	⊗	YES	+/-0.003
0.03	1	⊗	YES	+/-0.003

1650 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
WWW.LINEARTECH.COM

LINEAR
TECHNOLOGY

TITLE: FABRICATION DRAWING

LOW NOISE SEPIC DC/DC CONVERTER

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON ANGLE ±1°
0.1X" = ±0.01"
0.1XX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994

APPROVALS

PCB DES. JW

APP. ENG. GORAN P.

SCALE = NONE

DO NOT SCALE DRAWING

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FOR CUSTOMER USE ONLY

SIZE IC NO. REV.

N/A LTC3805IMSE-5 2

FILENAME: 1576A.PCB

SHT 1 of 1