



KSZ8863 Rev. A3 Errata Sheet

The following errata are exhibited in the Rev. A3 of the KSZ8863 product family.

Item	Symptom	Solution/Workaround
1	For the egress port rate limiting with tag insertion, the actual rate limiting value is about twice of the programmed value.	For the egress port rate limiting, don't set tag insertion on the egress port. Will be fixed in the next revision.
2	Upon reset or warm reset, the start switch bit in Register 1 is cleared to '0' in the I2C master mode. As the result, the switch doesn't work in the I2C master mode.	Workaround solution: Set Register 78 (0x4E) bit 0 = '1' when programming the EEPROM. The I2C master mode will then work fine. Will be fixed in the next revision.
3	In MDC/MDIO access, SMI access is failed.	Workaround solution: Pin 39 SPIS_N should have an external pull-up by a pull-up resistor.