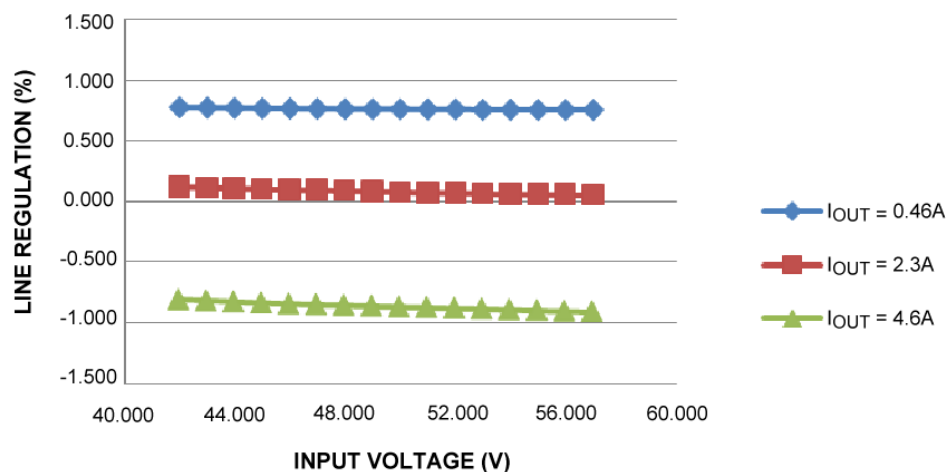
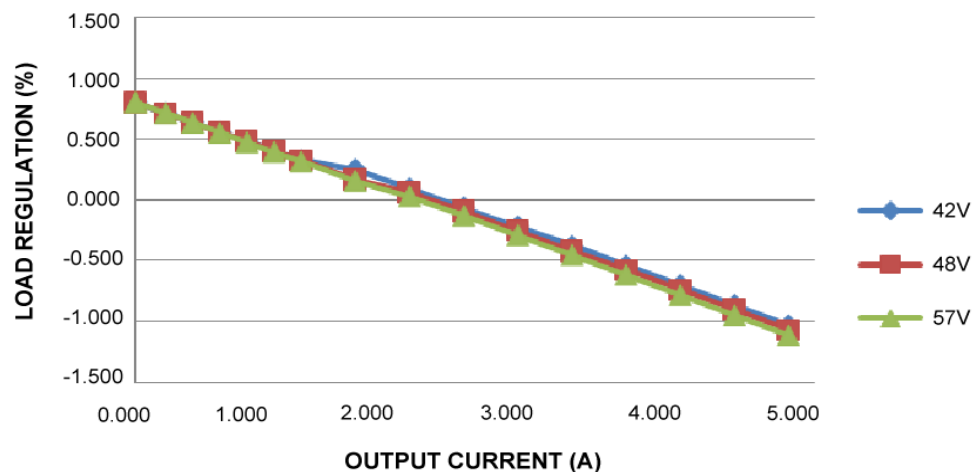


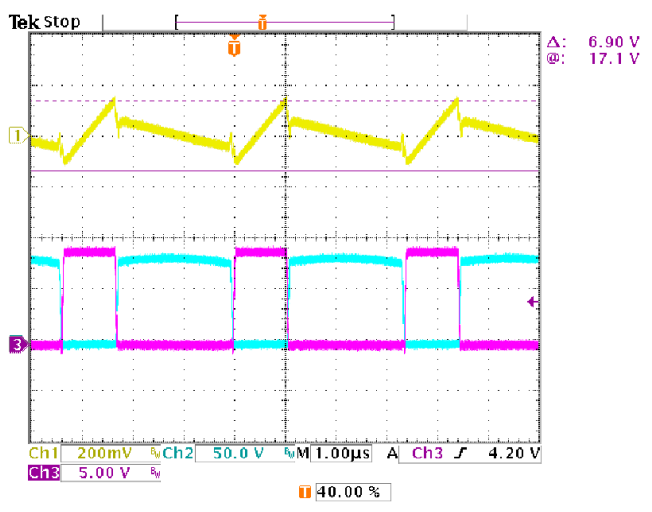
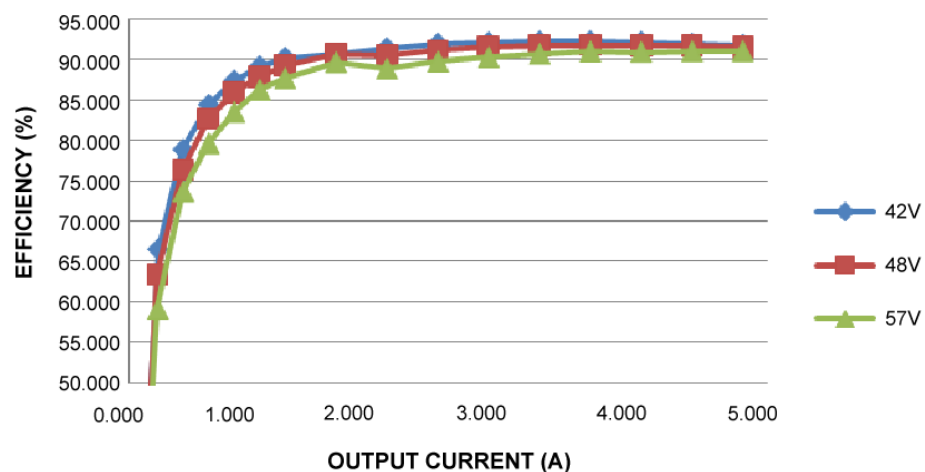
LINE REGULATION vs. INPUT VOLTAGE



LOAD REGULATION vs. OUTPUT CURRENT

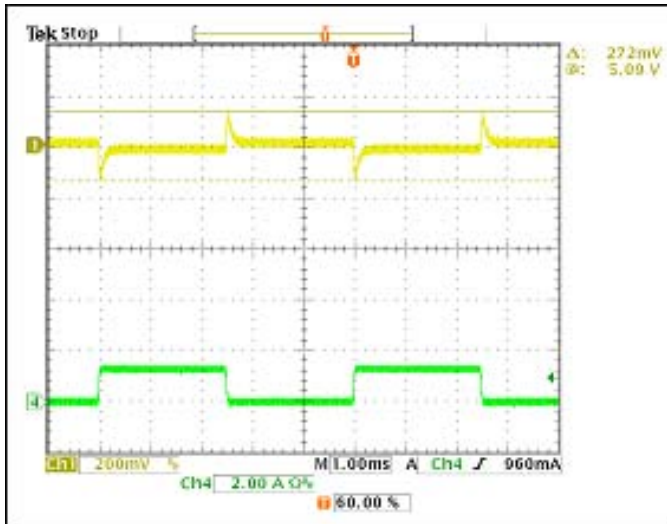


EFFICIENCY vs. OUTPUT CURRENT



Primary Stress Voltage

$V_{IN} = 57V$, $I_{OUT} = 0.1A$
 Ch1: 200mV/div, current sense voltage
 Ch2: 50V/div, primary drain-source voltage
 Ch3: 5V/div, NDRV driving
 Time base: 1µs/div



Transient Response

$V_{IN} = 48V$, $I_{OUT} = 0A-1.2A$
 Ch1: 200mV/div, 5V output voltage
 Ch4: 5A/div, output current
 Time base: 1ms/div

6

5

4

3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

D

C

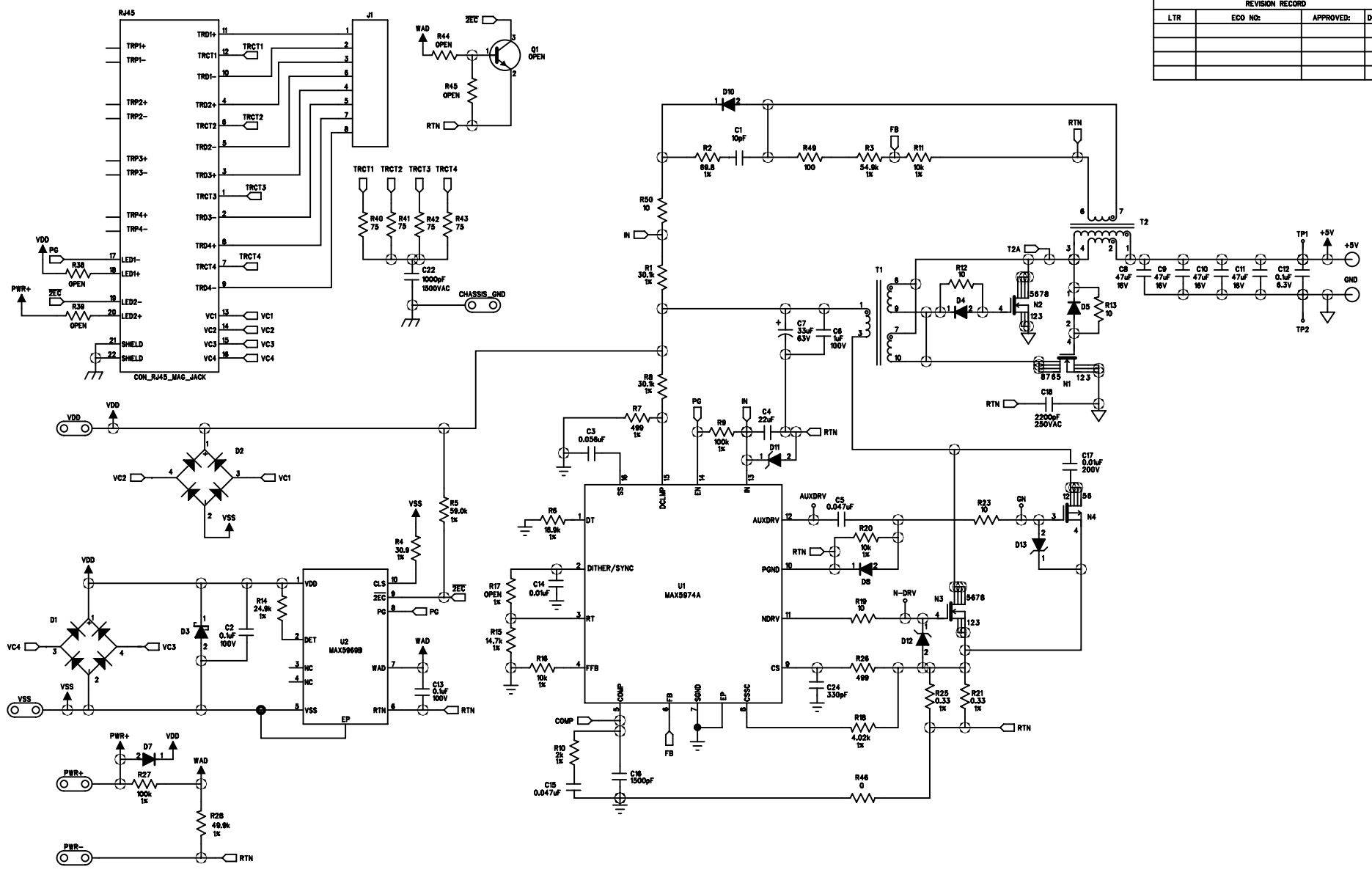
C

B

B

A

A



COMPANY:			
TITLE: MAX9874A EVALUATION KIT+			
DRAWN:	DATED:	CODE:	SIZE:
CHECKED:	DATED:	DRAWING NO:	REV:
QUALITY CONTROL:	DATED:	SCALE:	
RELEASED:	DATED:	SHEET: 1 OF 1	

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Related Parts

MAX5969B	IEEE 802.3af/at-Compliant, Powered Device Interface Controllers with Integrated Power MOSFET	-- Free samples
MAX5974A	Active-Clamped, Spread-Spectrum, Current-Mode PWM Controllers	-- Free samples

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AN5044, AN 5044, APP5044, Appnote5044, Appnote 5044

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