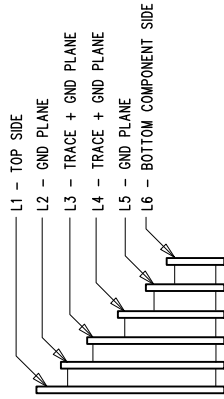


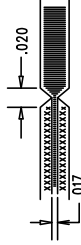
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
1	1	PRODUCTION	Haoran W	2-6-17

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
 - FINISHED THICKNESS TO BE 0.062 +/- .005
 - TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.007 THICK MIN.
- ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
- HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
5. FINISH: -SMBGC USING LPI BOTH SIDES, COLOR GREEN.
 - GOLD IMERSION BOTH SIDES.
 - FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
7. DESIGN HAS SOLDER MASK DEFINED PADS ON U1-U4.
U1,U3,U5,U7 SOLDER MASK SIZE IS 25 MIL AND PAD SIZE IS 29 MIL.
DON'T CHANGE PAD SIZE OR SOLDER MASK SIZE.
8. PCBs ARE TO BE ROHS COMPLIANT.
9. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.067	26	X	YES	+/-0.003
0.205	11	□	YES	+/-0.003
0.012	516	◇	YES	+/-0.003
0.037	26	⊗	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.01	239	⊕	YES	+/-0.003
0.03543	6	⊖	YES	+/-0.003
0.035	16	⊕	YES	+/-0.003
0.008	119	⊖	YES	+/-0.003
0.11811	2	⊕	YES	+/-0.003
0.1378	1	⊖	YES	+/-0.003
0.0315	2	⊕	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED	APPROVALS		 LINEAR TECHNOLOGY	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-9800 WWW.LTC.COM LTC@LTC.COM FOR CUSTOMER USE ONLY
	DIMENSIONS ARE IN INCHES	PCB DES. HZ		
TOLERANCES:	0.010 ± 0.01 0.005 ± 0.005			

APPROVALS

PCB DES.	HZ
APP ENG.	Haoran W.

TITLE: FABRICATION DRAWING

SIZE	IC NO.	REV
N/A	LTM4675EY+LTM4676EY+LTM4677EY LTM46820AEY+LTC2974CUP DEMO CIRCUIT 2204B	1

SCALE = NONE

FILENAME: DC2204B-1.PCB

SHT 1 OF 1