

Vishay High Power Products

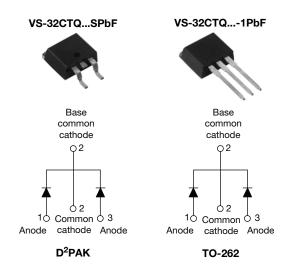
RoHS

COMPLIANT

HALOGEN

FREE

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 15 A			
V _R	25 V/30 V			

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

The VS-32CTQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	30	А		
V _{RRM}		25/30	V		
I _{FSM}	t _p = 5 μs sine	900	А		
V _F	15 Apk, T _J = 125 °C	0.40	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-32CTQ025SPbF VS-32CTQ025-1PbF	VS-32CTQ030SPbF VS-32CTQ030-1PbF	UNITS	
Maximum DC reverse voltage	V _R	25	30	V	
Maximum working peak reverse voltage	V _{RWM}	25	30	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 115 °C, rectangular waveform		30	
Maximum peak one cycle non-repetitive surge current	I _{FSM} -	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	900	А
See fig. 7		10 ms sine or 6 ms rect. pulse		250	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH		13	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 3		А	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1		15 A	T _J = 25 °C	0.49	V
	V _{FM} ⁽¹⁾	30 A		0.58	
	VFM V	15 A	T _J = 125 °C	0.40	
		30 A		0.53	
Maximum reverse leakage current	I _{BM} ⁽¹⁾	$T_J = 25 \ ^{\circ}C$	V _R = Rated V _R	1.75	mA
See fig. 2	IRM (")	T _J = 125 °C		97	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.233	V
Forward slope resistance	r _t			9.09	mΩ
Maximum junction capacitance per leg	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		1300	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

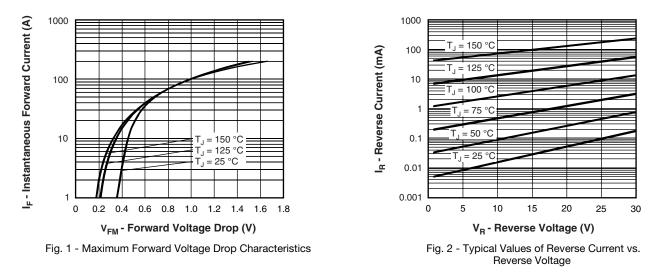
 $^{(1)}\,$ Pulse width < 300 $\mu s,\,duty\,cycle$ < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation See fig. 4	3.25	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	C/ VV	
				2	g	
Approximate weight			0.07	oz.		
Mounting torque	minimum				kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
			0	32CT0	32CTQ025S	
Marking device		Case style D ² PAK	32CT0	32CTQ030S		
		32CTQ025-1				
			Case style TO-262	32CTQ030-1		



VS-32CTQ...SPbF, VS-32CTQ...-1PbF Series

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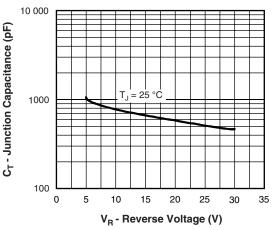


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

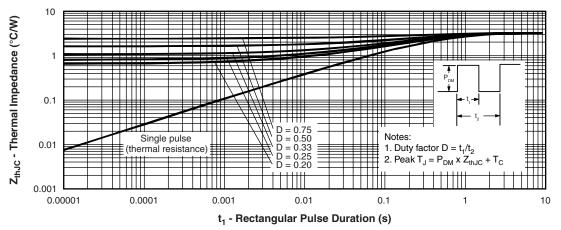
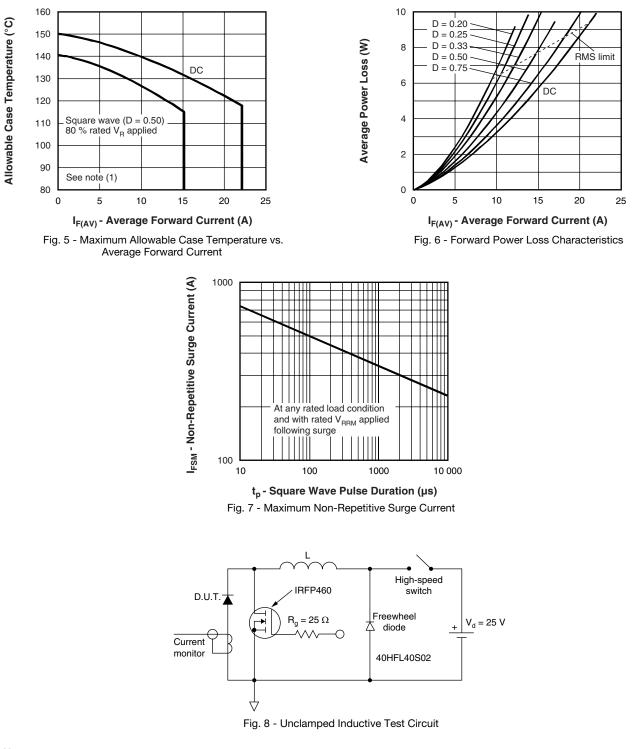


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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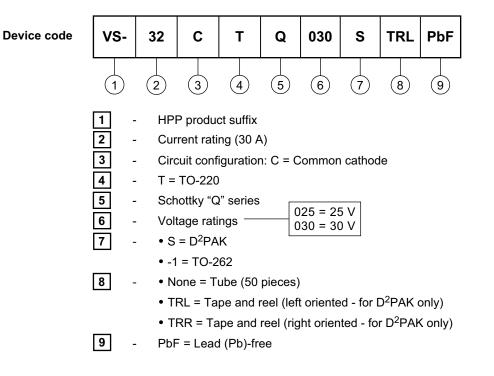
Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95014				
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			



Vishay

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