

## SPL-74H73H-G

**(RoHS Compliant)**

**3.3V / 1.25 Gbps 1490 nm Continuous-Mode TX / 1.25 Gbps 1310 nm Burst-Mode RX**

**IEEE 802.3ah-2004 PX-20+ SC SFP OLT Transceiver (with Reset-Less RX)**

### FEATURES

- | Gigabit Ethernet Optical Line Terminal (OLT) Transceiver for IEEE 802.3ah-2004 PX-20+
- | SFP SC OLT Transceiver
- | 1.25 Gbps / 1490 nm Continuous-Mode Transmitter
- | 1.25 Gbps / 1310 nm Burst-Mode Receiver with 2R Output
- | **Resetless BM Receiver Design**
- | BM RX with Transceiver Settling Time less than 400 ns
- | High Resistance of BM RX to DC Optical Background
- | 0 to 70°C Case Operating Temperature
- | Single +3.3 V Power Supply
- | Transmitter: AC Coupling Input
- | Receiver: DC Coupling Output
- | **RoHS Complaint**
- | Class 1 Laser International Safety Standard IEC 60825 Compliant

### DESCRIPTION

SPL-74H73H series is a small form factor pluggable transceiver for IEEE 802.3ah-2004 PX-20+ optical line terminal (OLT) with 1.25 Gbps/1490 nm in downstream and 1.25 Gbps /1310 nm in upstream. It is with the SFP 20-pin connector to allow hot plug capability. The SPL-74H73H series is high performance module for single fiber communications by using 1490 nm continuous-mode transmitter and 1310 nm burst-mode receiver. The transmitter section uses a multiple quantum well 1490 nm DFB laser and is a class 1 laser compliant according to International Safety Standard IEC 60825. The receiver section uses an integrated 1310 nm burst-mode detector preamplifier (IDP) mounted in an optical header and a burst-mode limiting post-amplifier IC. **Unlike the conventional BM RX, the RX does not requires reset pulse to receive optical data packets with different optical power.**

### APPLICATIONS

- | Gigabit Ethernet PON System

### LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC 60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

### ORDER INFORMATION

P/No	Class	TX				RX				Temp (°C)	Package	RoHS Compliant
		Type	Speed (Gb/s)	λ (nm)	Power (dBm)	Type	Speed (Gb/s)	λ (nm)	Sens. (dBm)			
<b>SPL-74H73H-G</b>	PX20+	CNT	1.25	1490 DFB	7 / 2	BM, 2R	1.25	1310	-8 / -30	0 / 70	<b>SC SFP</b>	Yes

Absolute Maximum Ratings						
Parameter	Symbol	Min	Max	Units	Notes	
Storage Temperature	Tstg	-40	85	°C		
Operating Case Temperature	Topr	0	70	°C		
Power Supply Voltage	Vcc	-0.5	3.6	V		

Recommended Operating Conditions						
Parameter	Symbol	Min	Max.	Unit	Units / Notes	
Power Supply Voltage	Vcc	3.13	3.47	V	V	
Operating Case Temperature	Topr	0	70	°C		
Power Supply Current	I <sub>CC(TX+RX)</sub>		300	mA		

**Transmitter Specifications (0°C < Topr < 70°C, 3.13V < Vcc < 3.47V)**

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
<b>Optical</b>						
Optical Transmit Power	P <sub>o</sub>	2		7	dBm	1
Output Center Wavelength	λ	1480		1500	nm	
Output Spectrum Width	Δλ			1	nm	-20 dB Width
Side Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	E <sub>R</sub>	9			dB	
Data Rate			1.25		Gb/s	
Optical Rise Time				0.26	ns	20% to 80% Values
Optical Fall Time				0.26	ns	20% to 80% Values
Relative Intensity Noise	RIN			-120	dB/Hz	
Total Jitter				0.226	ns	
<b>Electrical</b>						
Data Input Current – Low		-350			μA	
Data Input Current – High				350	μA	
Differential Input Voltage	V <sub>IH</sub> - V <sub>IL</sub>	0.4		2.4	V	Peak-to-Peak
TX Disable Input Voltage – Low	T <sub>DIS,L</sub>	0		0.8	V	2
TX Disable Input Voltage – High	T <sub>DIS,H</sub>	2.0		V <sub>CC</sub>	V	2
TX Disable Assert Time	T <sub>ASSERT</sub>			10	μs	
TX Disable Deassert Time	T <sub>DEASSERT</sub>			1	ms	
TX Fault Output Voltage -- Low	T <sub>FAULTL</sub>	0		0.5	V	3
TX Fault Output Voltage -- High	T <sub>FAULTH</sub>	2.0		V <sub>CC</sub> +0.3	V	3

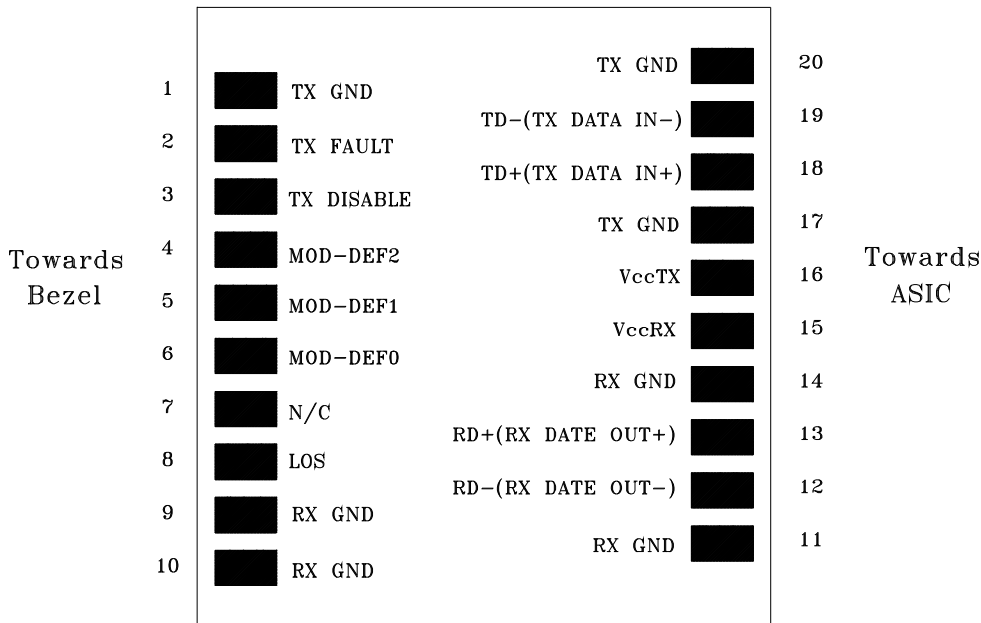
1. Output power is power coupled into a 9/125 μm single mode fiber.
2. There is an internal 4.7K to 10K ohm pull-up resistor to VccTX.
3. Open collector compatible, 4.7K to 10K ohm pull-up to Vcc (Host Supply Voltage).

**Receiver Characteristics (0°C < Topr < 70°C, 3.13V < Vcc < 3.47V)**

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
<b>Optical</b>						
Sensitivity	Sen			-30	dBm	4
Saturation Optical Power	Sat	-8			dBm	4
Wavelength of Operation		1260		1360	nm	5
Data Rate			1.25		Gb/s	
Settling Timing		400			ns	6,7
DC Optical Tolerance		Sen +3			dB	
Optical Return Loss		20			dB	
<b>Electrical</b>						
Data Output Voltage – Low	V <sub>OL</sub> - V <sub>CC</sub>	-2.0		-1.58	V	8
Data Output Voltage – High	V <sub>OH</sub> - V <sub>CC</sub>	-1.1		-0.74	V	8
Signal Detect Output Voltage -- Low	V <sub>OL</sub>			0.5	V	
Signal Detect Output Voltage -- High	V <sub>OH</sub>	2.0			V	
Signal Detect Assert Time	AS <sub>MAX</sub>	10	15	30	ns	Transition: low to high
Signal Detect Deassert Time	ANS <sub>MAX</sub>	70	80	120	ns	Transition: high to low
Rest Between Packet				Resetless		

4. Sensitivity and saturation levels at BER 1E-12 for a PRBS 2<sup>7</sup>-1 @ER=9.
5. At least 30 dB optical isolation for the wavelength 1480 to 1500 nm.
6. No reset is needed.
7. The optical power difference between neighboring packets is 20 dB at least.
8. These inputs are compatible with 10K, 10KH and 100K ECL and LVPECL outputs.

CONNECTION DIAGRAM



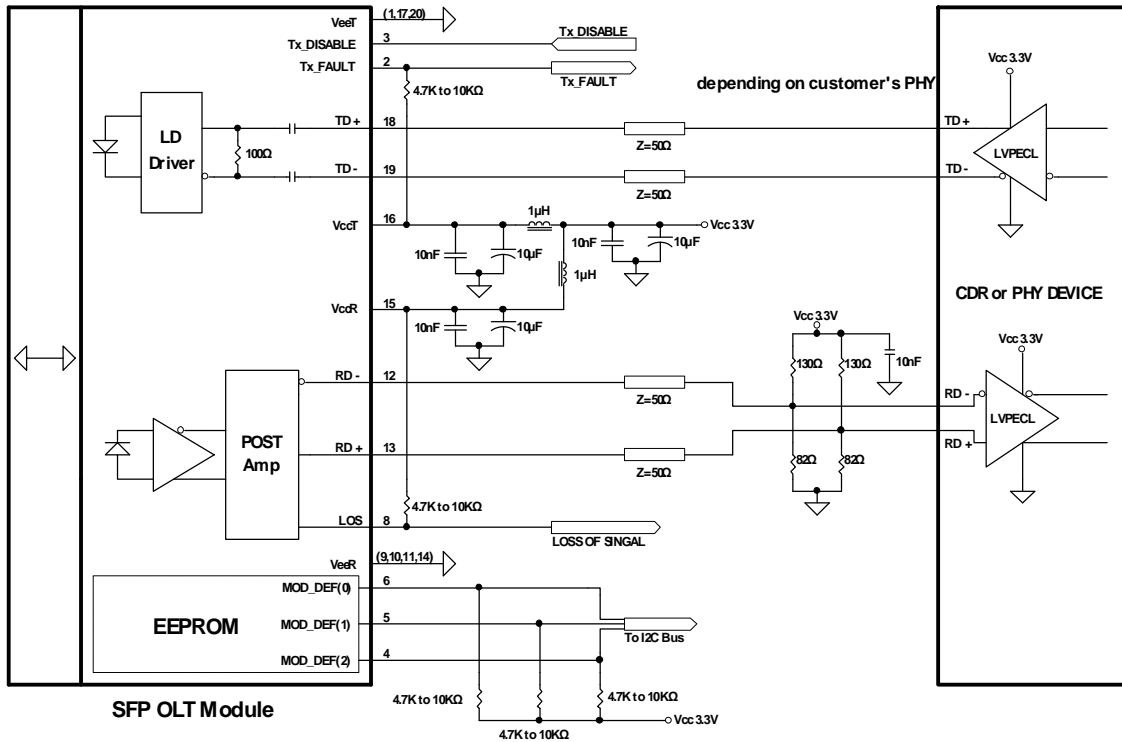
PIN	Signal Name	Description	PIN	Signal Name	Description
1	TX GND	Transmitter Ground	11	RX GND	Receiver Ground
2	TX Fault	Transmitter Fault Indication	12	RX DATA OUT-	Inverse Receiver Data Out
3	TX Disable	Transmitter Disable (Module disables on high or open)	13	RX DATA OUT+	Receiver Data Out
4	MOD-DFE2	Modulation Definition 2 – Two wires serial ID Interface	14	RX GND	Receiver Ground
5	MOD-DEF1	Modulation Definition 1 – Two wires serial ID Interface	15	Vcc RX	Receiver Power – 3.3V±5%
6	MOD-DEF0	Modulation Definition 0 – Ground in Module	16	Vcc TX	Transmitter Power – 3.3V±5%
7	N/C	Not Connected	17	TX GND	Transmitter Ground
8	LOS	Loss of Signal	18	TX DATA IN+	Transmitter Data In
9	RX GND	Receiver Ground	19	TX DATA IN-	Inverse Transmitter Data In
10	RX GND	Receiver Ground	20	TX GND	Transmitter Ground

MODULE DEFINITION

Module Definition	MOD-DEF2 PIN 4	MOD-DEF1 PIN 5	MOD-DEF0 PIN 6	Interpretation by Host
4	SDA	SCL	LV-TTL Low	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, MOD-DEF(1:2) appear as no connector (NC) and MOD-DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E<sup>2</sup>PROM protocol of the ATMEL AT24C01A/02/04 family of components.

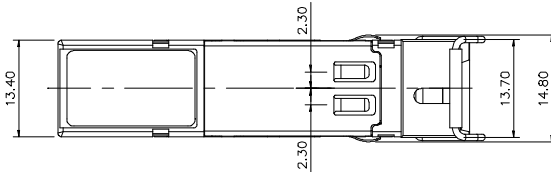
RECOMMENDED CIRCUIT SCHEMATIC



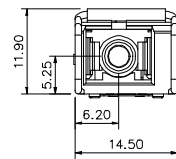
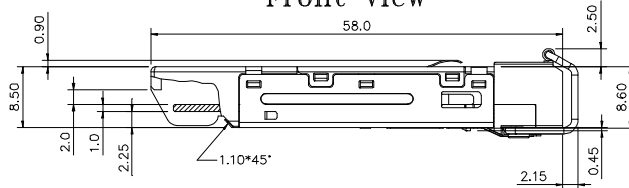
PACKAGE DIAGRAM

Units in mm

Top View

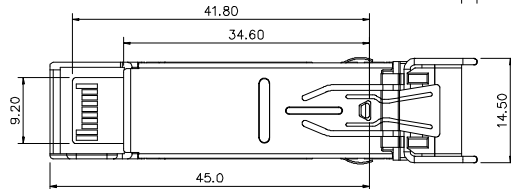


Front View



Side View

Bottom View



**Note:** Specifications subject to change without notice.