1.0MHZ, 2A STEP-DOWN DC-DC BUCK CONVERTER

NEW PRODUCT

Description

The AP3402 is a 2A step-down DC-DC converter. At heavy load, the constant frequency PWM control performs excellent stability and transient response. No external compensation components are required.

The AP3402 supports a range of input voltages from 2.7V to 5.5V, allowing the use of a single Li+/Li- polymer cell, multiple Alkaline/NiMH cell, and other standard power sources. The output voltage is adjustable from 0.6V to the input voltage. The AP3402 employs internal power switch and synchronous rectifier to minimize external part count and realize high efficiency. During shutdown, the input is disconnected from the output and the shutdown current is less than 1μA. Other key features include over-temperature and short circuit protection, and under-voltage lockout to prevent deep battery discharge.

The AP3402 delivers 2A maximum output current while consuming only 90μA of no-load quiescent current. Ultra-low $R_{DS(ON)}$ integrated MOSFETs and 100% duty cycle operation make the AP3402 an ideal choice for high output voltage, high current applications which require a low dropout threshold.

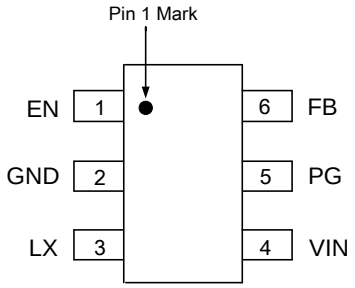
The AP3402 is available in TSOT26 package.

Features

- Output Current: Up to 2A
- Output Voltage: 0.6V to V_{IN}
- Input Voltage: 2.7V to 5.5V
- Peak Efficiency Up to 95%
- 90μA (Typ) No Load Quiescent Current
- Shutdown Current: <1μA
- 100% Duty Cycle Operation
- 1MHz Switching Frequency
- Power Good Indicator Function
- Internal Soft Start
- No External Compensation Required
- Current Limit Protection
- Thermal Shutdown
- TSOT26 Package
- **Totally Lead-free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Pin Assignments

(Top View)



TSOT26

Applications

- 5V or 3.3V Point of Load Conversion
- Telecom/Networking Equipment
- Set Top Boxes
- Storage Equipment
- Video Cards
- DDR Power Supply

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

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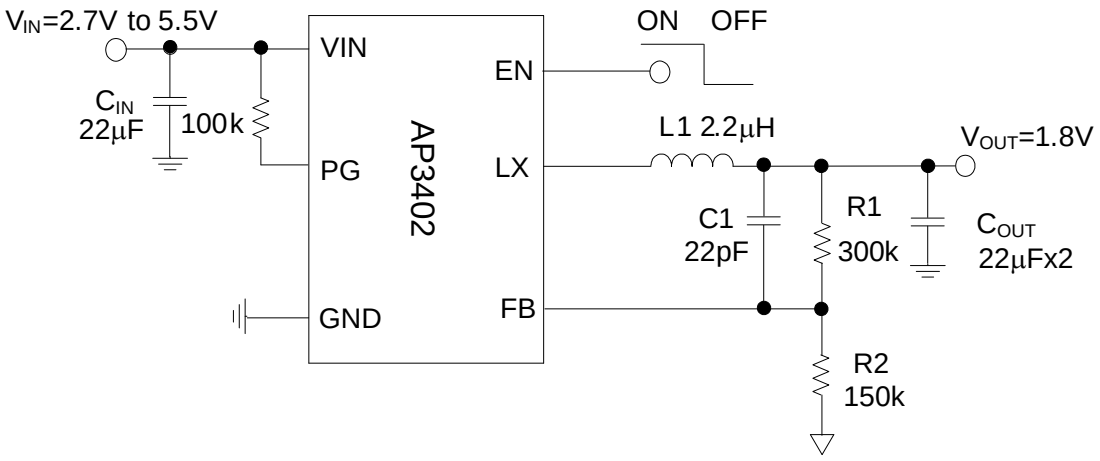
Typical Applications Circuit



Pin Descriptions

Pin Number	Pin Name	Function
1	EN	Enable control input. Force this pin voltage above 1.5V enables the chip, and below 0.4V shuts down the device
2	GND	Ground pin
3	LX	The drains of the internal main and synchronous power MOSFET
4	VIN	Bias supply. Chip main power supply pin
5	PG	Power good indicator, open drain output. PG is pulled up to VIN when the output voltage is within 20% of the regulation level, otherwise it is low
6	FB	Feedback voltage to internal error amplifier, the threshold voltage is 0.6V

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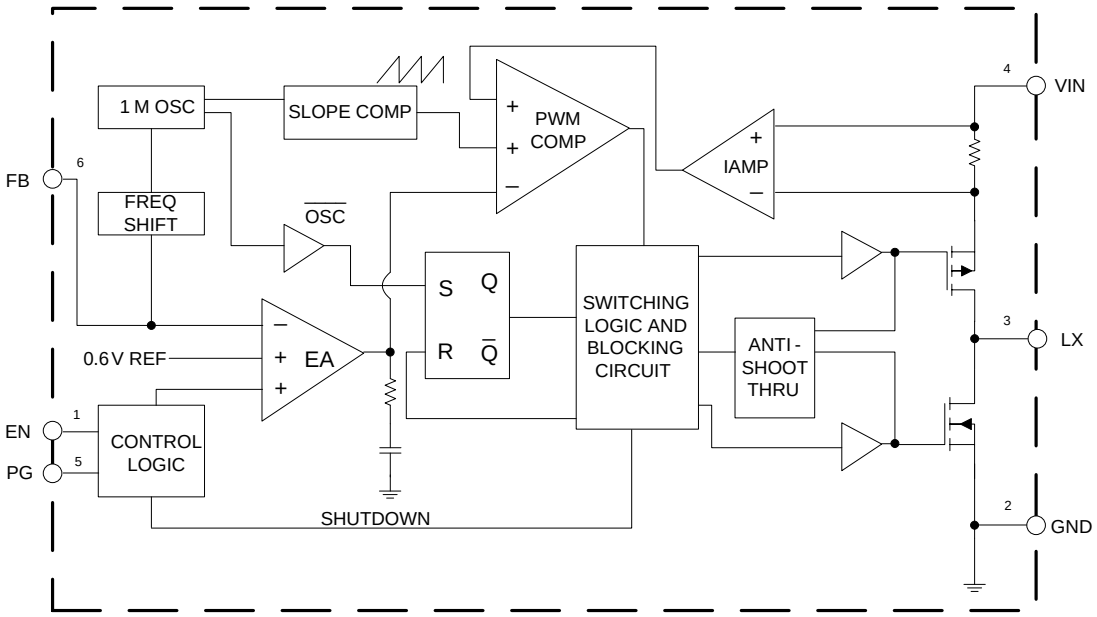
Functional Block Diagram



Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating		Unit
V_{IN}	Input Voltage	-0.3 to 6		V
V_{EN}	EN Pin Voltage	-0.3 to V_{IN} +0.3		V
V_{LX}	LX Pin Voltage	-0.3 to V_{IN} +0.3		V
V_{FB}	Feedback Pin Voltage	-0.3 to V_{IN} +0.3		V
V_{PG}	PG Pin Voltage	-0.3 to V_{IN} +0.3		V
θ_{JA}	Thermal Resistance (Junction to Ambient)	TSOT26	220	°C/W
T_J	Operating Junction Temperature	+150		°C
T_{STG}	Storage Temperature	-65 to +150		°C
T_{LEAD}	Lead Temperature (Soldering, 5 sec)	+300		°C
V_{MM}	ESD (Machine Model)	200		V
V_{HBM}	ESD (Human Body Model)	2000		V

Note 4: Stresses greater than those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “Recommended Operating Conditions” is not implied. Exposure to “Absolute Maximum Ratings” for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{IN}	Input Voltage Range	2.7	5.5	V
T_A	Operating Ambient Temperature	-40	+85	°C
T_J	Junction Temperature Range	-40	+125	°C

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Electrical Characteristics (@V_{IN} = 5V, V_{OUT} = 1.8V, L = 2.2µH, C_{OUT} = 22µF×2, T_A = +25°C, unless otherwise specified.)

Symbol	Parameters	Conditions	Min	Typ	Max	Unit
V_{IN}	Input Voltage Range	—	2.7	—	5.5	V
V_{OUT}	Output Voltage Range	—	0.6	—	V_{IN}	V
V_{FB}	Regulated Feedback Voltage	—	0.588	0.6	0.612	V
I_{FB}	FB Leakage Current	V_{FB} = 1V	—	—	0.2	µA
I_Q	Quiescent Current	V_{FB} = 0.65V	—	90	—	µA
I_{SD}	Shutdown Current	V_{EN} = 0V	—	—	1	µA
I_{LIM}	Peak Inductor Current	—	3	—	—	A
f_{OSC}	Oscillator Frequency	—	—	1	—	MHz
$R_{DS(ON)}$	Drain to Source On-state Resistance	I_{LX} = 100mA, high side	—	110	—	mΩ
		I_{LX} = 100mA, low side	—	80	—	
V_{ENH}	EN High Threshold	—	1.5	—	—	V
V_{ENL}	EN Low Threshold	—	—	—	0.4	V
I_{EN}	EN Leakage Current	V_{IN} = V_{EN} = 5V	-1.0	—	1.0	µA
V_{UVLO}	Input UVLO Threshold	—	—	2.4	2.7	V
V_{HYS}	UVLO Hysteresis	—	—	0.2	—	V
T_{OTP}	Over Temperature Protection	—	—	+160	—	°C
T_{OTH}	OTP Hysteresis	—	—	+30	—	°C
V_{FBTH}	Power Good Threshold (Relative to V_{FB})	—	—	±20	—	%
V_{IOVP}	V_{IN} Over Voltage Protection	—	—	6.25	—	V
V_{IHSY}	IOVP Hysteresis	—	—	0.25	—	V
t_{SS}	Soft-start Time	—	—	1.8	—	ms

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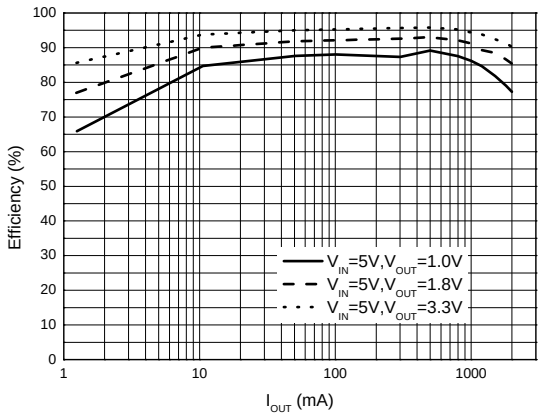
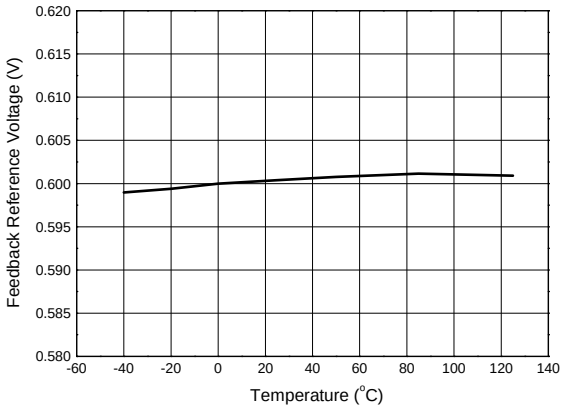
Performance Characteristics (@T_A = +25ºC, V_{IN} = 5.0V, V_{OUT} = 1.8V, unless otherwise specified.)

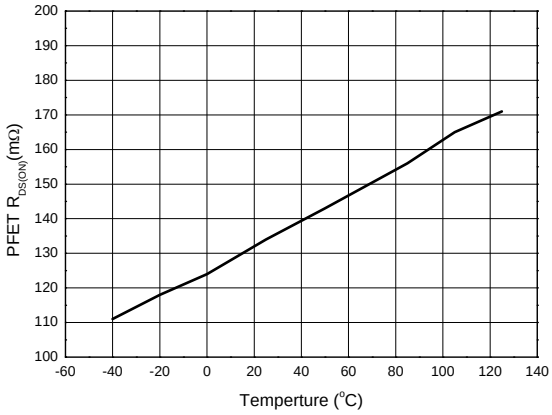
Efficiency vs. Load Current



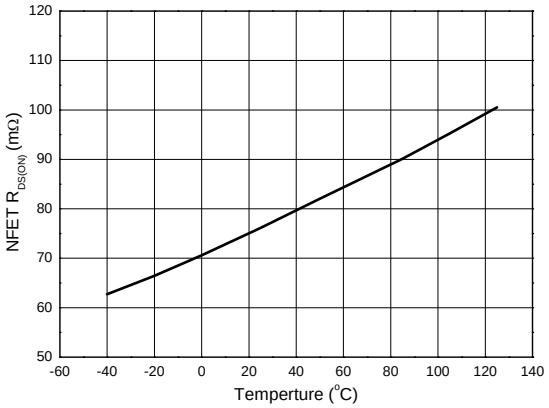
Feedback Reference Voltage vs. Temperature



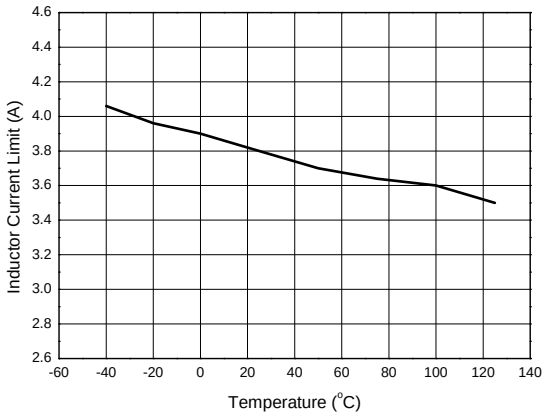
PFET Drain-Source On-State Resistance vs. Temperature



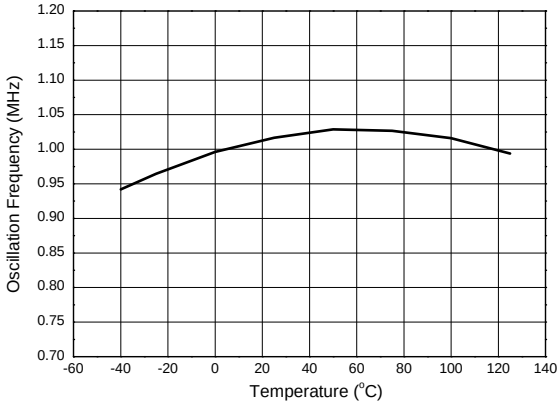
NFET Drain-Source On-State Resistance vs. Temperature



Inductor Current Limit vs. Temperature



Oscillation Frequency vs. Temperature



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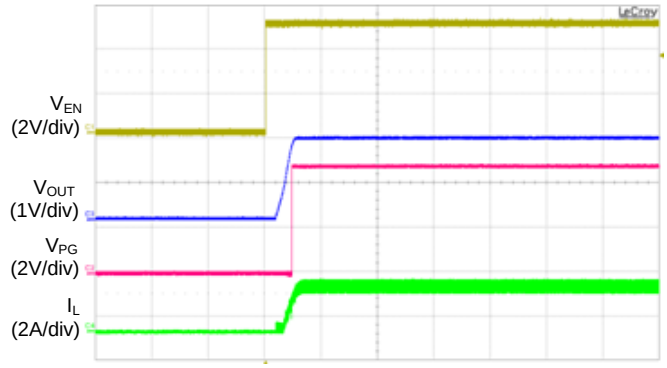
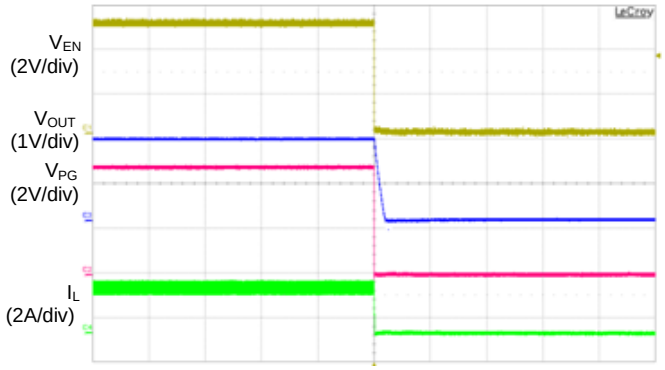
Performance Characteristics (@T_A = +25ºC, V_{IN} = 5.0V, V_{OUT} = 1.8V, unless otherwise specified.)

Enable Turn on Characteristic (I_{OUT}=2A)



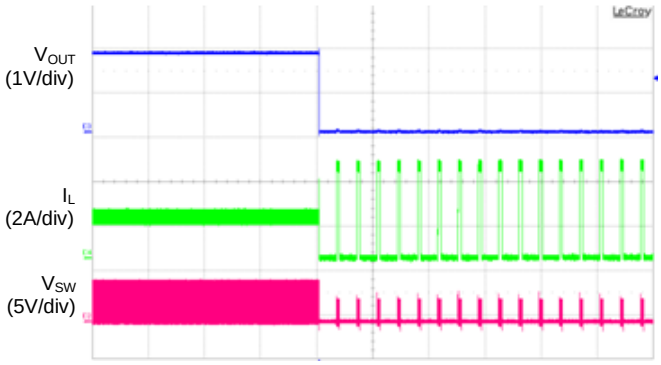
Time (5ms/div)

Enable Turn off Characteristic (I_{OUT}=2A)



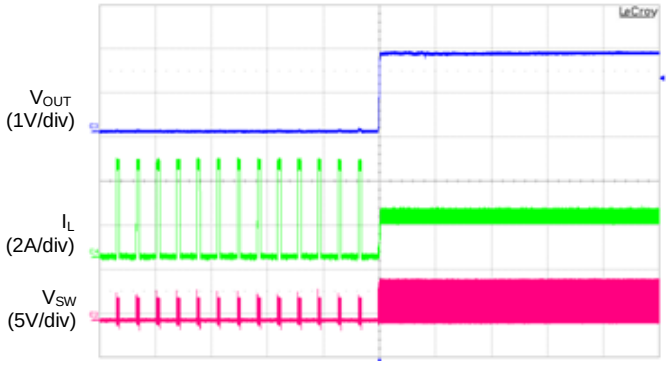
Time (200µs/div)

Short Current Protection (I_{OUT}=2A)



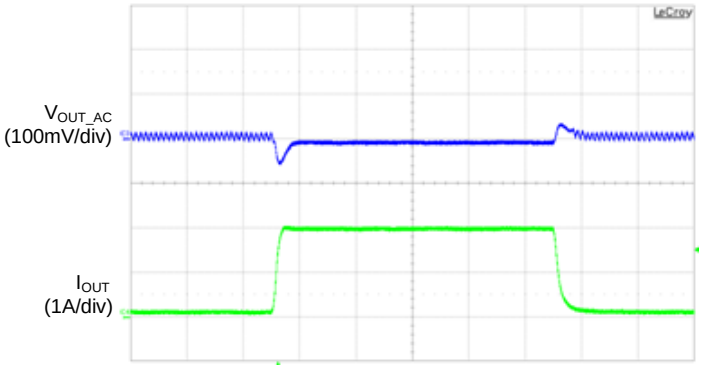
Time (50ms/div)

SCP Recovery (I_{OUT}=2A)



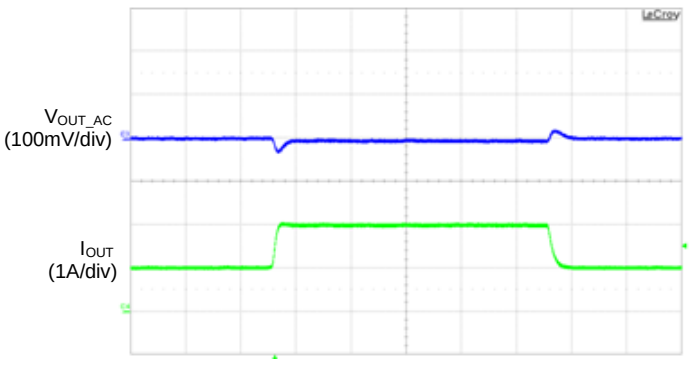
Time (50ms/div)

Load Transient (I_{OUT}=0.1A to 2A)



Time (100µs/div)

Load Transient (I_{OUT}=1A to 2A)



Time (100µs/div)

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Application Information

Typical application circuit is shown in the application circuit and for the circuit parameters setting please refers to the following descriptions.

Under Voltage Lockout (UVLO) Circuit

When the V_{IN} drops lower than the UVLO detector threshold, the UVLO circuit starts to operate, V_{REF} stops, and high-side switch and low-side switch built-in switch transistors turn "OFF". As a result, V_{OUT} drops according to the C_{OUT} capacitance value and the load. When the V_{IN} is rising higher than UVLO released voltage, the IC will restart the operation.

Short Circuit Protection and Recovery

When the AP3402 output node is shorted to GND that V_{FB} drops under 0.3V, AP3402 will enter hiccup mode to protect itself. If short circuit is removed, and V_{FB} rises over 0.3V, the AP3402 recovers to normal operation again. If the AP3402 reaches OCP threshold while short circuit, the AP3402 will enter cycle by cycle current limit mode until the current under OCP threshold.

Power Good

The PG pin output is an open drain MOSFET. The output is pulled low when the FB voltage enters the fault condition by falling below 80% or rising above 120% of the nominal internal reference voltage. When the FB voltage rises to the good condition above 90% or falls below 110% of the internal voltage reference PG output MOSET is turned off. It is recommended using a pull-up to VIN.

Over Temperature Protection

The internal thermal temperature protection circuitry is provided to protect the integrated circuit in the event that the maximum junction temperature is exceeded. When the junction temperature exceeds +160^{O}C, it shuts down the internal control circuit and switching power MOSFET. The AP3402 will restart automatically under the control of soft start circuit when the junction temperature decreases to +130^{O}C.

Input Over Voltage Protection

When input voltage of AP3402 is near 6.25V, the IC will enter Input-Over-Voltage-Protection. It would be shut down and there will be no output voltage in this state. As the input voltage goes down below 6V, it will leave input OVP and recover the output voltage.

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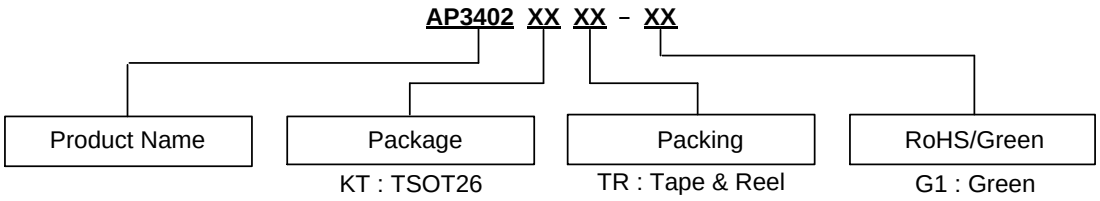
Ordering Information

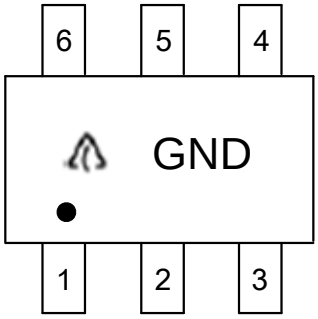


Package	Temperature Range	Part Number	Marking ID	Packing
TSOT26	-40°C to +85°C	AP3402KTTR-G1	GND	3000 / Tape & Reel

Marking Information

(1) TSOT26

(Top View)



First Line: Logo and Marking ID

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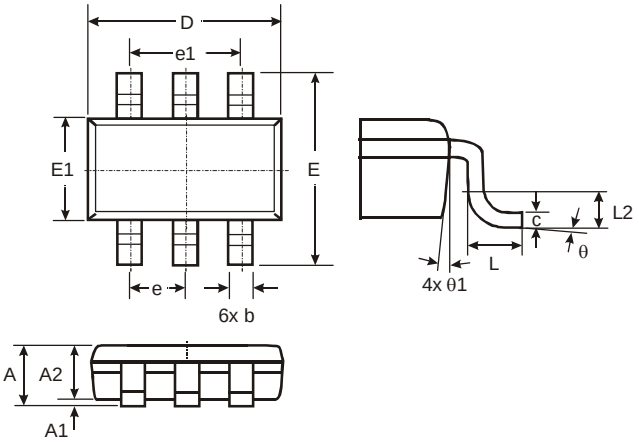
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

(1) Package Type: TSOT26

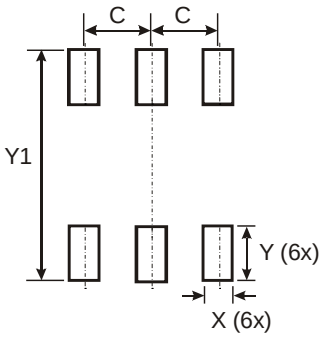


TSOT26			
Dim	**Min**	**Max**	**Typ**
A	–	1.00	–
A1	0.01	0.10	–
A2	0.84	0.90	–
D	–	–	2.90
E	–	–	2.80
E1	–	–	1.60
b	0.30	0.45	–
c	0.12	0.20	–
e	–	–	0.95
e1	–	–	1.90
L	0.30	0.50	
L2	–	–	0.25
θ	0°	8°	4°
θ1	4°	12°	–
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

(1) Package Type: TSOT26



Dimensions	Value (in mm)
C	0.950
X	0.700
Y	1.000
Y1	3.199

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