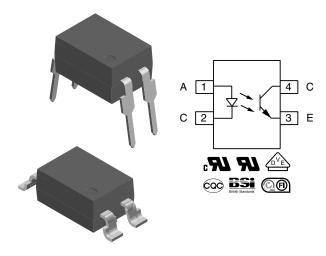
# VO618A



Vishay Semiconductors

# Optocoupler, Phototransistor Output, High Reliability, 5300 V<sub>RMS</sub>



### DESCRIPTION

The 110 °C rated VO618A feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm spacing.

Creepage and clearance distances of > 8.0 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation up to an operation voltage of 400 V<sub>RMS</sub> or DC. Specifications subject to change.

### **FEATURES**

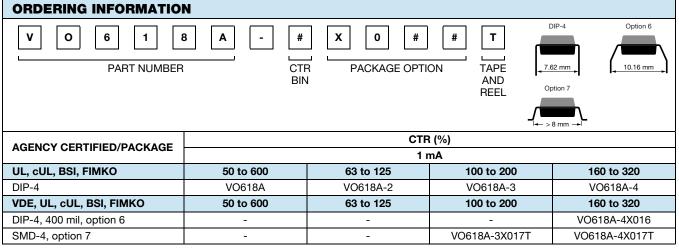
- Operating temperature from 55 °C to + 110 °C
- Good CTR linearity depending on forward current
- Isolation test voltage, 5300 V<sub>RMS</sub>
- High collector emitter voltage, V<sub>CEO</sub> = 80 V
- Low saturation voltage
- · Fast switching times
- Low CTR degradation
- Temperature stable
- · Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- High common mode interference immunity
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

### **APPLICATIONS**

- AC adapters
- SMPS
- PLC
- Factory automation
- · Game consoles

### AGENCY APPROVALS

- UL1577, file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI IEC 60950; IEC 60065
- FIMKO EN 60065, EN 60950-1
- COC GB8898-2001



#### Note

Additional options may be possible, please contact sales office.

1 For technical questions, contact: optocoupleranswers@vishay.com Document Number: 83432



e3 RoHS COMPLIANT HALOGEN FREE

GREEN (5-2008)



<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V <sub>R</sub>	6	V			
Forward current		I <sub>F</sub>	60	mA			
Forward surge current	$t_p \le 10 \ \mu s$	I <sub>FSM</sub>	2.5	А			
LED power dissipation	at 25 °C	P <sub>diss</sub>	70	mW			
OUTPUT							
Collector emitter voltage		V <sub>CEO</sub>	80	V			
Emitter collector voltage		V <sub>ECO</sub>	7	V			
Collector current		Ι <sub>C</sub>	50	mA			
Collector peak current	$t_p/T=0.5,t_p\leq 10\ ms$	I <sub>CM</sub>	100	mA			
Ouput power dissipation	at 25 °C	P <sub>diss</sub>	150	mW			
COUPLER							
Isolation test voltage (RMS)	t = 1 min	V <sub>ISO</sub>	5300	V <sub>RMS</sub>			
Total power dissipation		P <sub>tot</sub>	200	mW			
Operation temperature		T <sub>amb</sub>	- 55 to + 110	°C			
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C			
Soldering temperature <sup>(1)</sup>	2 mm from case, $\leq$ 10 s	T <sub>sld</sub>	260	°C			

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

(1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD), and wave profile for soldering conditions for through hole devices (DIP), please go to "Assembly Instructions" (<u>www.vishay.com/doc?80054</u>).

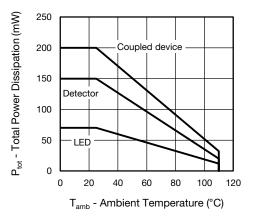


Fig. 1 - Total Power Dissipation vs. Ambient Temperature



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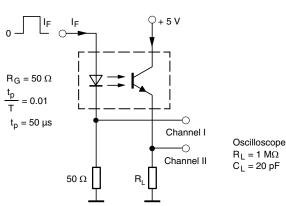
ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I <sub>F</sub> = 5 mA	V <sub>F</sub>	1	1.1	1.65	V
Reverse current	$V_R = 6 V$	I <sub>R</sub>		0.01	10	μA
Junction capacitance	$V_R = 0 V$ , f = 1 MHz	Cj		13		pF
OUTPUT						
Collector emitter leakage current	V <sub>CE</sub> = 10 V	I <sub>CEO</sub>		10	200	nA
Collector emitter capacitance	$V_{CE} = 5 V$ , f = 1 MHz	C <sub>CE</sub>		5.2		pF
Collector emitter breakdown voltage	$I_{\rm C} = 1  \rm{mA}$	BV <sub>CEO</sub>	80			V
Emitter collector breakdown voltage	I <sub>E</sub> = 100 μA	BV <sub>ECO</sub>	7			V
COUPLER						
Collector emitter saturation voltage	I <sub>F</sub> = 1 mA, I <sub>C</sub> = 2.5 mA	V <sub>CEsat</sub>		0.25	0.4	V
Coupling capacitance	f = 1 MHz	C <sub>C</sub>		0.4		pF

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 5 V	VO618A	CTR	50		600	%
		VO618A-2	CTR	63		125	%
		VO618A-3	CTR	100		200	%
		VO618A-4	CTR	160		320	%

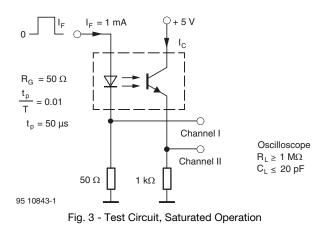
SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	CTR BIN	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED							
Rise and fall time	$I_F$ = 1 mA, $V_{CC}$ = 5 V, $R_L$ = 75 $\Omega$		t <sub>r</sub> , t <sub>f</sub>		2		μs
Turn-on time	$I_{\rm E} = 1 \text{ mA}, V_{\rm CC} = 5 \text{ V}, \text{ R}_{\rm I} = 75 \Omega$		t <sub>on</sub>		3		μs
Turn-off time	$I_F = 1 IIIA, V_{CC} = 5 V, H_L = 75 \Omega_2$		t <sub>off</sub>		2.3		μs
Cut-off frequency	$I_F = 1$ mA, $V_{CC} = 5$ V, $R_L = 75$ $\Omega$		f <sub>ctr</sub>		100		kHz
SATURATED							
Turn-on time	I <sub>F</sub> = 1 mA		t <sub>on</sub>		4.2		μs
Turn-off time	I <sub>F</sub> = 1 mA		t <sub>off</sub>		23		μs
Rise time	I <sub>F</sub> = 1 mA		t <sub>r</sub>		3		μs
Fall time	I <sub>F</sub> = 1 mA		t <sub>f</sub>		14		μs



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Fig. 2 - Test Circuit, Non-Saturated Operation



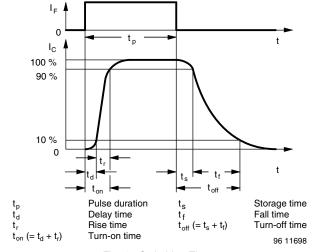


Fig. 4 - Switching Times

SAFETY AND INSULATION R	ATINGS			
PARAMETER	SYMBOL	VALUE	UNIT	
MAXIMUM SAFETY RATINGS				•
Output safety power		P <sub>SO</sub>	265	mW
Input safety current		I <sub>si</sub>	130	mA
Safety temperature		Τ <sub>S</sub>	150	°C
Comparative tracking index	CTI	175		
INSULATION RATED PARAMETERS				
Maximum withstanding isolation voltage	V <sub>ISO</sub>	5300	V <sub>RMS</sub>	
Maximum transient isolation voltage	V <sub>IOTM</sub>	8000	V <sub>peak</sub>	
Maximum repetitive peak isolation voltag	V <sub>IORM</sub>	890	V <sub>peak</sub>	
Insulation resistance	Insulation resistance $T_{amb} = 25 \text{ °C}, V_{DC} = 500 \text{ V}$		10 <sup>12</sup>	Ω
Isolation resistance	$T_{amb} = 100 \ ^{\circ}C, V_{DC} = 500 \ V$	R <sub>IO</sub>	10 <sup>11</sup>	Ω
Climatic classification (according to IEC		55/110/21		
Environment (pollution degree in accorda		2		
Internal and external ereaners	Standard DIP-4		≥ 7	mm
Internal and external creepage	400 mil DIP-4		≥ 8	mm
Clearance	Standard DIP-4		≥ 7	mm
	400 mil DIP-4		≥ 8	mm
Insulation thickness	Insulation thickness			mm

#### Note

• As per DIN EN 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

Rev. 1.4, 18-Apr-13



### **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified)

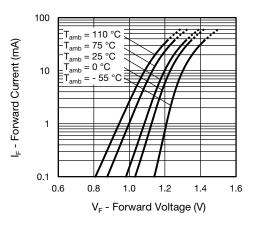


Fig. 5 - Forward Voltage vs. Forward Current

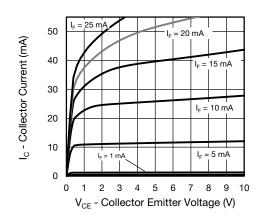


Fig. 6 - Collector Current vs. Collector Emitter Voltage

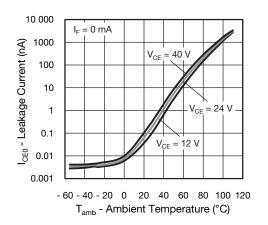


Fig. 7 - Collector Emitter Current vs. Ambient Temperature

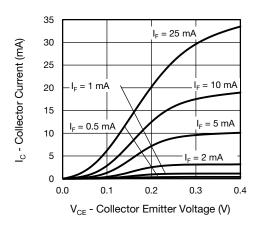


Fig. 8 - Collector Current vs. Collector Emitter Voltage

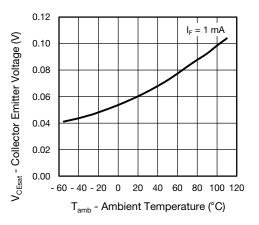
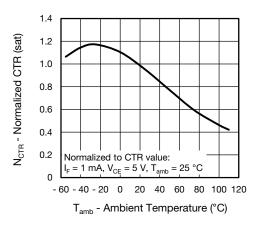
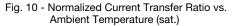
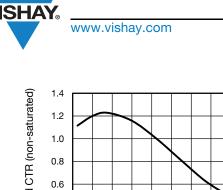


Fig. 9 - Collector Emitter Voltage vs. Ambient Temperature





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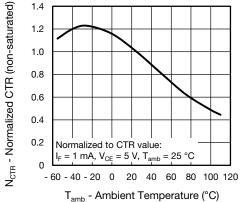


Fig. 11 - Normalized Current Transfer Ratio vs. Ambient Temperature (non-sat.)

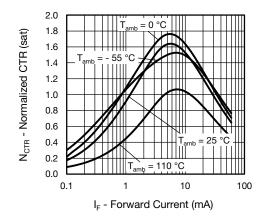


Fig. 12 - Current Transfer Ratio vs. Forward Current (sat.)

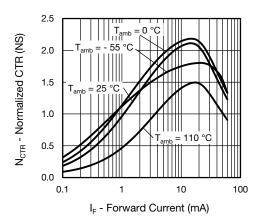


Fig. 13 - Current Transfer Ratio vs. Forward Current (non-sat.)

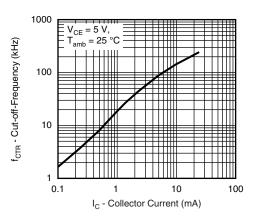


Fig. 14 - Frequency (- 3 dB) vs. Collector Current

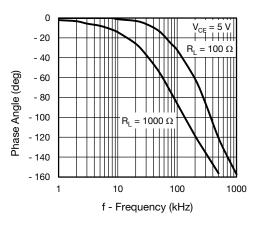


Fig. 15 - F<sub>CTR</sub> vs. Phase Angle

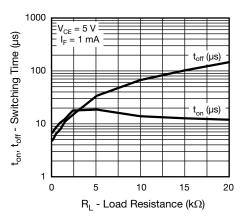


Fig. 16 - Switching Time vs. Load Resistance

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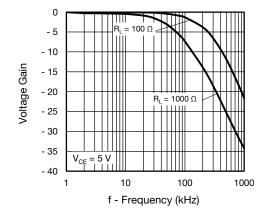
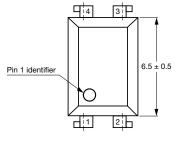
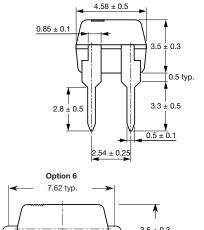
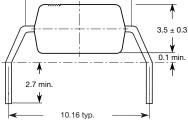


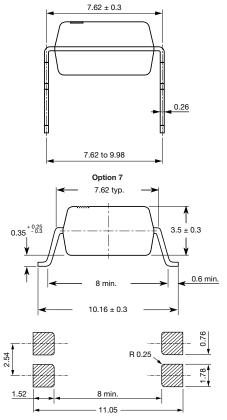
Fig. 17 - Voltage Gain vs. Frequency











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For technical questions, contact: <u>optocoupleranswers@vishay.com</u>

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### PACKAGE MARKING (example of VO618A-3X017T)



#### Notes

- The VDE logo is only marked on option 1 parts. Option information is not marked on the part.
- Tape and reel suffix (T) is not part of the package marking.

### **PACKING INFORMATION**

DEVICE PER TUBE						
ТҮРЕ	UNITS/TUBE	TUBES/BOX	UNITS/BOX			
DIP-4	100	40	4000			

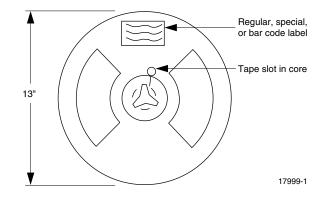


Fig. 18 - Tape and Reel Shipping Medium (1000 units per reel)

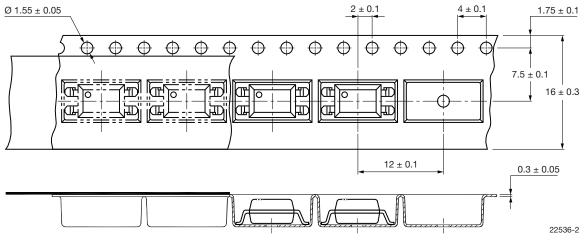


Fig. 19 - Tape and Packing for Option 7



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