UNITRODE

UC1702
UC2702
UC3702

Quad PWM Relay Driver

FEATURES

- Maintains Constant Average Relay Voltage With Varying Supply Voltages
- VBUS Voltages up to 42.5V
- Up to 50mA per Relay
- Integrated Schottky Flyback Diodes
- Individual Relay Control Inputs
- Short Circuit Protection
- User Selectable Operating Center Frequency and Relay Voltage
- Global Reset

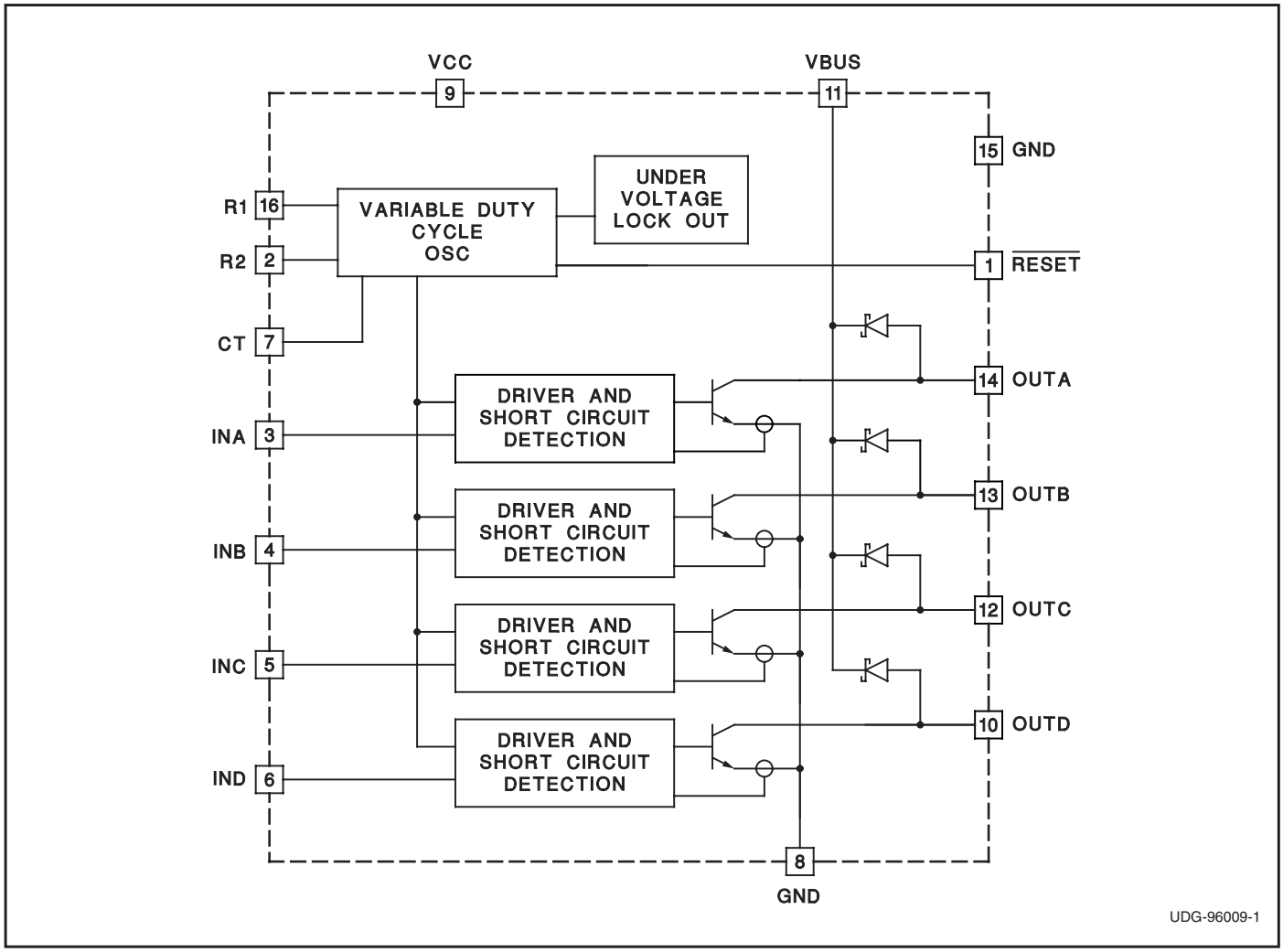
DESCRIPTION

The UC3702 Quad Relay Driver is intended to drive up to four relays from logic inputs using an unregulated relay voltage supply. The relays are driven from the Bus supply in a power efficient PWM converter fashion. The relay coil is used as the inductive element. (See the application's section concerning the relay selection.) VBUS must be higher then the rated relay voltage. Short circuit protection is provided on chip with periodic retry.

The UC3702 requires a +5V logic supply as a reference. Two external resistors program the effective relay voltage and a capacitor sets the nominal operating frequency. Internal Schottky diodes eliminate the need for any external power components.

In typical applications, the UC3702 grants the user the flexibility to choose the most cost effective relay without the added burden of generating a separate relay supply voltage. The UC3702 will even function with a poorly regulated supply VBUS, containing significant 100Hz or 120Hz ripple.

BLOCK DIAGRAM



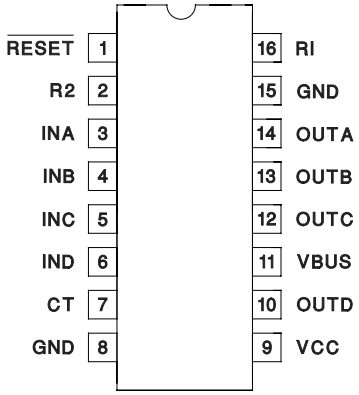
ABSOLUTE MAXIMUM RATINGS

VBUS . . . . . +50V
VCC . . . . . +9V
Input Voltage . . . . . –0.3V to VCC + 0.3V
Average Current per Relay 50mA
Total Output Capacitance. 25pF
Storage Temperature . . . . . –65°C to +150°C
Junction Temperature . . . . . –55°C to +150°C
Lead Temperature (Soldering, 10 sec.) . . . . . +300°C

Currents are positive into, negative out of the specified terminal. Consult Packaging Section of Databook for thermal limitations and considerations of packages.

CONNECTION DIAGRAM

DIL-16, SOIC-16 (Top View)
J or N, D Package



ELECTRICAL CHARACTERISTICS: Unless otherwise stated these specifications apply for T_A = –55°C to +125°C for UC1702; –25°C to +85°C for UC2702; 0°C to +70°C for UC3702; VBUS = 34V, VCC = 5V, $\overline{\text{RESET}}$ = VCC, R2 = 43.2kΩ; R1 = 203kΩ; CT = 220pF, $T_A = T_J$.

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage Section					
VCC		4.5	5	8	V
VCC Supply Current	$\overline{\text{RESET}}$, INA, INB, INC, IND = VCC		4	6	mA
VBUS				42.5	V
Oscillator Section					
NOMINAL Operating Frequency	R1 = 203kΩ, R2 = 43.2kΩ, CT = 220pF	150	175	200	kHz
R1		100	202	500	kΩ
R2		20	42.5	80	kΩ
CT		100			pF
Output Driver VBUS = 20V					
Rated Relay Current				50	mA
Short Circuit Current		100	175		mA
Diode Leakage Current	Control Input Low, V_{OUTX} = VBUS		0.05	5	µA
Diode Forward Voltage			500		mV
Averaged Output Voltage Error	UC2702, UC3702 (Note 1)	–10		10	%
	UC1702	–20		20	%
Control Inputs (INA, INB, INC, IND)					
Logic Low				0.5	V
Logic High		3		VCC	V
TON / TOFF Delay				250	ns
Input Current		–1	0	1	µA
Input Current	V_{IN} = 5V		70	120	µA
$\overline{\text{RESET}}$					
TON / TOFF Delay				2	µs
Input Current	V_{IN} = 0.3V	–8	–5		µA
Input Current	V_{IN} = 5V	–10	0	10	µA

Note 1: Programmed Average Voltage $= \frac{1.06 \bullet R1 \bullet VCC}{2 \bullet R2} - 400mV$

PIN DESCRIPTIONS

CT: A capacitor from the CT pin to ground sets the oscillator center frequency. Note that the oscillator period must be least an order of magnitude less than the relaxation time constant (L/R) of the relay coil. However, a needlessly high operating frequency only increases power dissipation. For best accuracy, CT should be 220pF or greater.

GND: Both ground pins must be connected to a low noise system ground.

INA, INB, INC, IND: Separate digital control inputs for each of the four relay drivers. An active high input (a logic high) turns on the respective relay. Active low disables the common-emitter drive transistor.

OUTA, OUTB, OUTC, OUTD: Each of these output pins can be connected to the low side of one relay coil. The rated relay coil voltage of all relays to be driven by a single UC3702 must be the same. (It is permissible to use coils with different rated series resistances with a single UC3702, so long as the rated coil voltages are the same and care is taken with respect to the relaxation times of the different relay coils). Output transients are slew rate limited to decrease electro-magnetic radiation.

R1: User selected external resistor must be placed from the R1 pin to the VBUS voltage supply. Sensing the VBUS amplitude, the oscillator varies its ON duty cycle such that the average voltage across the driver coil is constant.

R2: User selected external resistor must placed from R2 pin to GND. This resistor, in conjunction with the external timing capacitor, CT, sets the OFF cycle time of the oscillator. For best accuracy, use 42.3kΩ for R2.

$\overline{\text{RESET}}$: Digital reset pin is active low. When $\overline{\text{RESET}}$ is low, the oscillator stops running and all drivers are open collector.

VCC: Nominally 5V. In addition to supplying the voltage for the driver logic and oscillator circuit, the VCC supply is used as reference for the generating the average relay voltage. It is recommended that the VCC be regulated to ±5% of its nominal value or better to insure good regulation. Good decoupling with a minimum of 1μF is necessary.

VBUS: This pin should be tied to a low impedance voltage source at some voltage higher than the rated voltage of the relay coil to be driven. VBUS may even be a poorly filtered rectified sin wave, as the UC3702 will regulate the correct duty cycle. Good decoupling with a minimum of 1μF is necessary.

APPLICATIONS INFORMATION

The UC3702 must be programmed for a specific relay voltage. The table given below has some suggested values of R1, R2, and CT. The VCC supply voltage is assumed to be 5V.

Rated Relay Voltage	R1	R2	CT
9V	153kΩ	43.2kΩ	220pF
12V	203kΩ	43.2kΩ	220pF
24V	398kΩ	43.2kΩ	220pF

More generally, the following equation can be used to determine the equivalent average relay voltage:

$$V_{RELAY} = \frac{1.06 \bullet VCC \bullet R1}{2 \bullet R2} - 400mV$$

Industrial Relays

The UC3702 takes advantage of the inductance of the relay coil to regulate a constant coil current in a manner very similar to a switch-mode power supply. To use the UC3702 correctly, the coil characteristics must be known. The rated relay voltage is what the relay manufacturers specify as voltage to drive the relay coil with if the coil were driven from a dc source. Most manufacturers also specify the series resistance of the coil. This is the resistance of the copper wire and determines the steady state coil ON current. For example, a 12V relay with a 320Ω series coil resistance with a draw of 37.5mA of constant current. The relay inductance is not typically specified in the manufacturer's short-form datasheet, so the engineer will need an LCR bridge to measure it or request the information from the relay manufacturer.

APPLICATIONS INFORMATION (cont.)

Although the inductance may not be specified in the relay datasheet, it is very repeatable since it is determined by the number of turns and geometry of the relay.

The relay's coil inductance divided by its series resistance is the relay coil's relaxation time constant. It is important that the relaxation time constant be at least an order of magnitude longer than the nominal switching period of the UC3702 to obtain a constant current through the relay coil. This means that some relays may be ill-suited for use with the UC3702.

For example, a common 12V industrial relay has a series resistance of 500Ω and a coil inductance of 500mH. Its relaxation time constant is 1ms, an order of magnitude less than the period of a 10kHz oscillator.

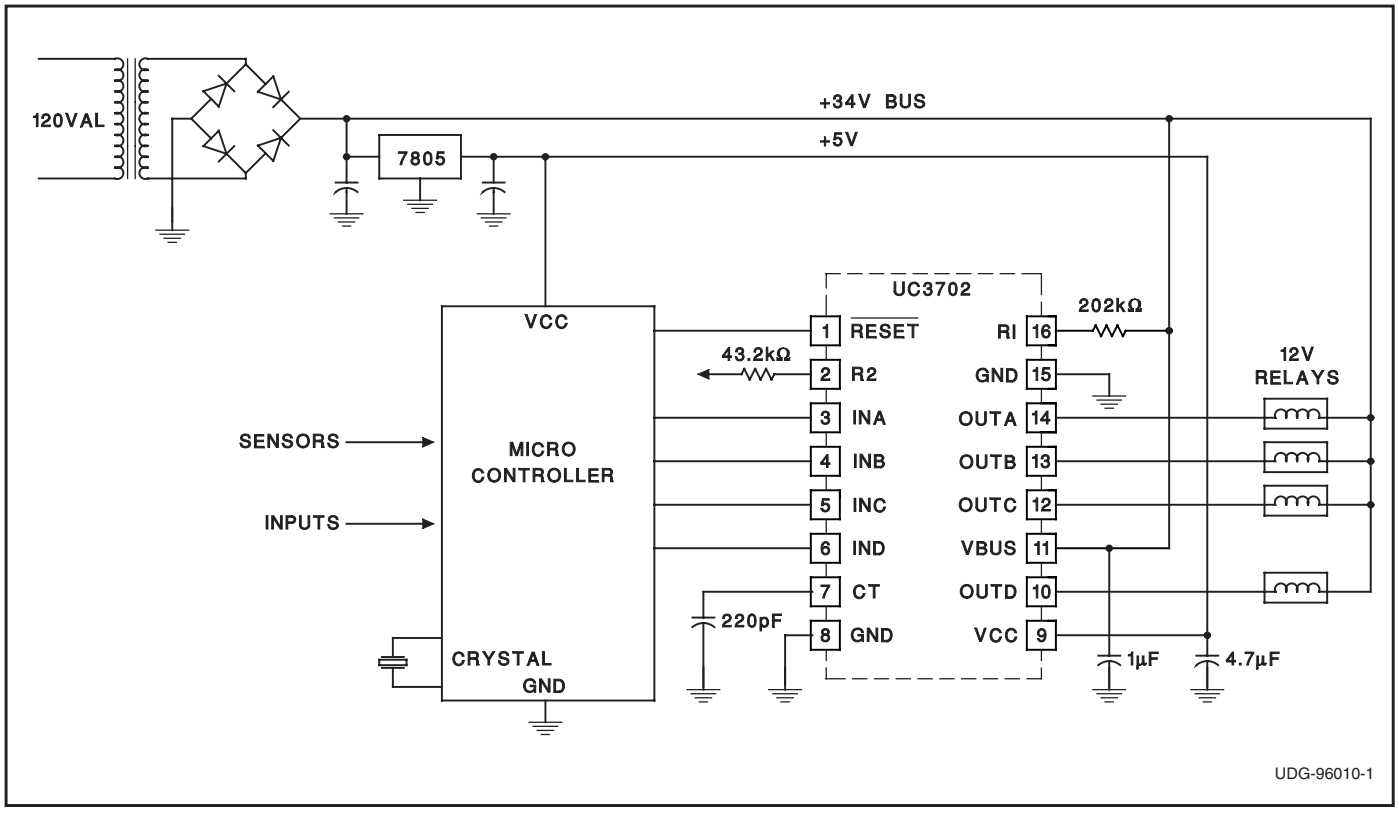
When performing an engineering evaluation of the UC3702 in a system, it is useful to have a current probe. Alternately, a 1Ω resistor can be placed in series with the relay coil in the prototype and differential voltage probes can be used to observe the coil current. (Do not use a large series resistor, as this will impact observed relaxation time constant.)

Layout Considerations

The layout of the printed circuit board and good decoupling of the power supplies is critical to proper operation of the UC3702. For best results, high quality 4.7μF tantalum capacitors should be placed as close as possible to the VCC and the VBUS pins.

In addition to decoupling considerations, the R1 and R2 pins are sensitive to capacitive coupling from any of the driver outputs (OUTA - D) which can slew at speeds of 300V/μs. The external resistors R1 and R2 should be placed as close as possible to their respective pins. Avoid routing the outputs directly past these pins without a low impedance trace (such as GND, VCC, or VBUS) in between to act as a capacitive shield.

Typical Microcontroller Application Driving 12V Industrial Relays



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PACKAGING INFORMATION

Orderable Device	Status [1]	Package Type	Package Drawing	Pins	Package Qty	Eco Plan [2]	Lead/Ball Finish	MSL Peak Temp [3]
UC2702D	NRND	SOIC	D	16		TBD	Call TI	Call TI
UC2702DTR	NRND	SOIC	D	16		TBD	Call TI	Call TI
UC3702D	NRND	SOIC	D	16		TBD	Call TI	Call TI
UC3702N	NRND	PDIP	N	16		TBD	Call TI	Call TI

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
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Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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