

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for active power factor correction and switching mode power supplies.

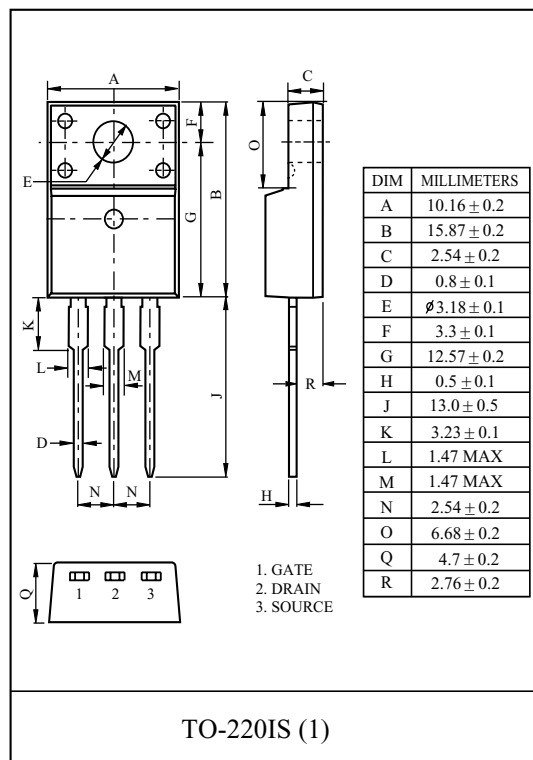
FEATURES

- $V_{DS}=800V$, $I_D=4A$
- Drain-Source ON Resistance :
 $R_{DS(ON)}(Max)=2.6\Omega$ @ $V_{GS}=10V$
- $Q_g(typ.)=17nC$

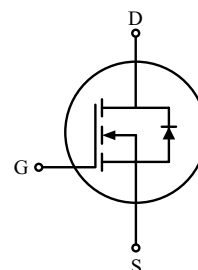
MAXIMUM RATING ($T_c=25^\circ C$)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DSS}	800	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	@ $T_c=25$	I_D	4*	A
	@ $T_c=100$		2.5*	
	Pulsed (Note1)	I_{DP}	11*	
Single Pulsed Avalanche Energy (Note 2)		E_{AS}	250	mJ
Repetitive Avalanche Energy (Note 1)		E_{AR}	4.7	mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5	V/ns
Drain Power Dissipation	$T_c=25$	P_D	43	W
	Derate above 25		0.34	W/
Maximum Junction Temperature		T_j	150	
Storage Temperature Range		T_{stg}	-55 150	
Thermal Characteristics				
Thermal Resistance, Junction-to-Case		R_{thJC}	2.9	/W
Thermal Resistance, Junction-to-Ambient		R_{thJA}	62.5	/W

* : Drain current limited by maximum junction temperature.



PIN CONNECTION



KF4N80F

ELECTRICAL CHARACTERISTICS (Tc=25)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250 μA, V _{GS} =0V	800	-	-	V
Breakdown Voltage Temperature Coefficient	Δ BV _{DSS} / Δ T _j	I _D =250 μA, Referenced to 25	-	0.8	-	V/
Drain Cut-off Current	I _{DSS}	V _{DS} =800V, V _{GS} =0V	-	-	10	μA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =250 μA	2.5	-	4.5	V
Gate Leakage Current	I _{GSS}	V _{GS} = ± 30V, V _{DS} =0V	-	-	± 100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =3.5A	-	2.2	2.6	Ω
Dynamic						
Total Gate Charge	Q _g	V _{DS} =480V, I _D =4A V _{GS} =10V (Note4,5)	-	17	-	nC
Gate-Source Charge	Q _{gs}		-	3	-	
Gate-Drain Charge	Q _{gd}		-	8	-	
Turn-on Delay time	t _{d(on)}	V _{DD} =400V I _D =4A R _G =25 Ω (Note4,5)	-	25	-	ns
Turn-on Rise time	t _r		-	25	-	
Turn-off Delay time	t _{d(off)}		-	65	-	
Turn-off Fall time	t _f		-	30	-	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	750	-	pF
Output Capacitance	C _{oss}		-	90	-	
Reverse Transfer Capacitance	C _{rss}		-	13	-	
Source-Drain Diode Ratings						
Continuous Source Current	I _S	V _{GS} <V _{th}	-	-	4	A
Pulsed Source Current	I _{SP}		-	-	16	
Diode Forward Voltage	V _{SD}	I _S =4.0A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	I _S =4.0A, V _{GS} =0V,	-	460	-	ns
Reverse Recovery Charge	Q _{rr}	dI _S /dt=100A/ μs	-	4.0	-	μC

Note 1) Repetivity rating : Pulse width limited by junction temperature.

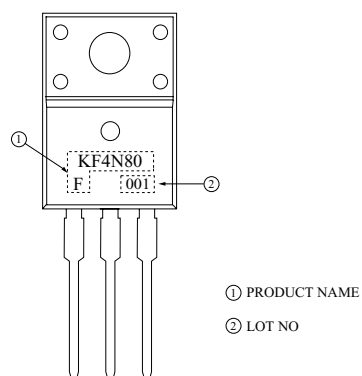
Note 2) $L=30mH, I_S=4A, V_{DD}=50V, R_G=25\Omega$, Starting $T_j=25$.

Note 3) $I_S \leq 4.0A, dI/dt \leq 100A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_j=25$.

Note 4) Pulse Test : Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

Note 5) Essentially independent of operating temperature.

Marking



KF4N80F

Fig1. $I_D - V_{DS}$

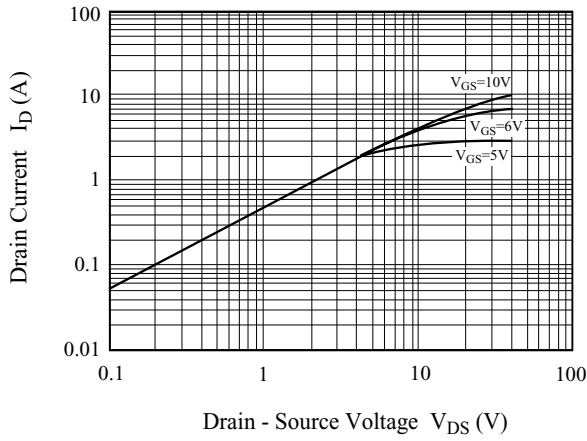


Fig2. $I_D - V_{GS}$

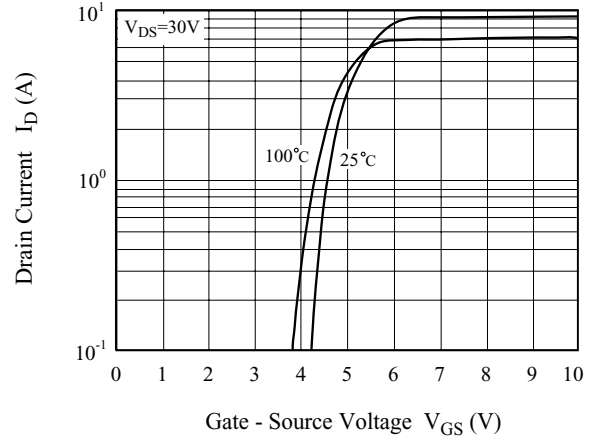


Fig3. $BV_{DSS} - T_j$

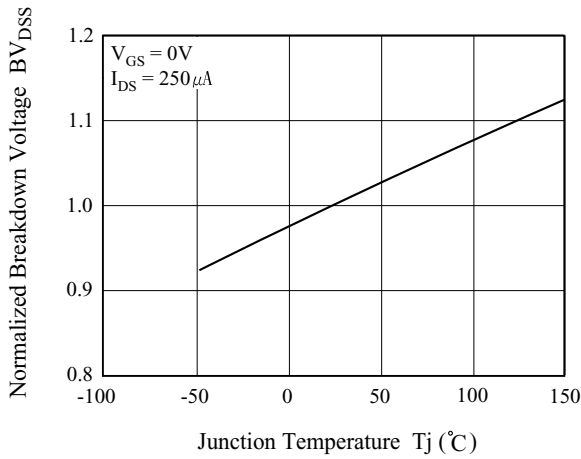


Fig4. $R_{DS(ON)} - I_D$

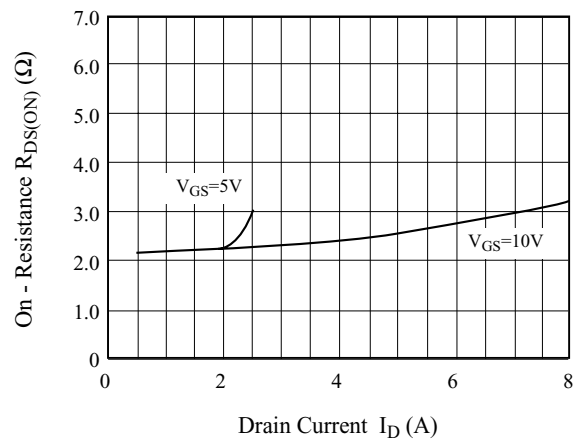


Fig5. $I_S - V_{SD}$

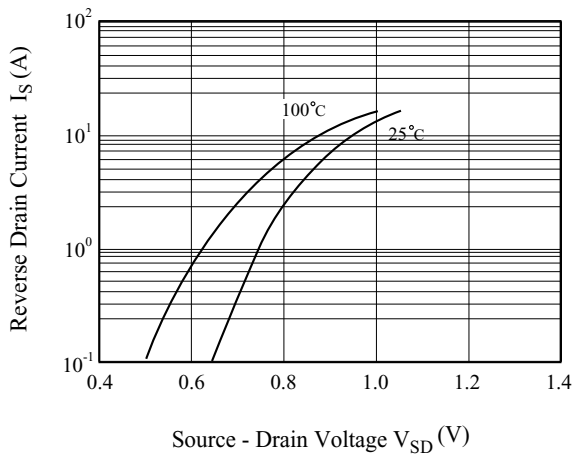


Fig6. $R_{DS(ON)} - T_j$

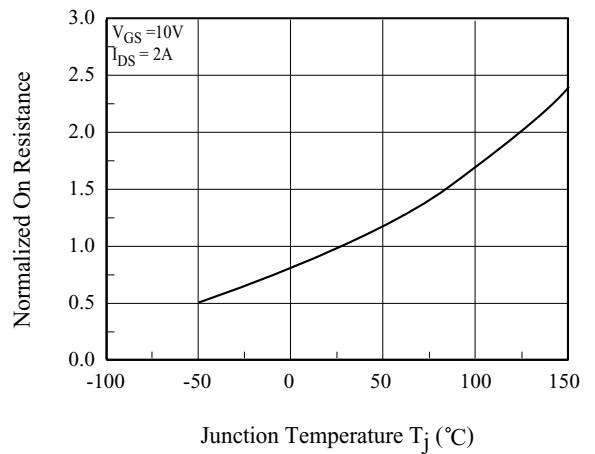


Fig 7. C - V_{DS}

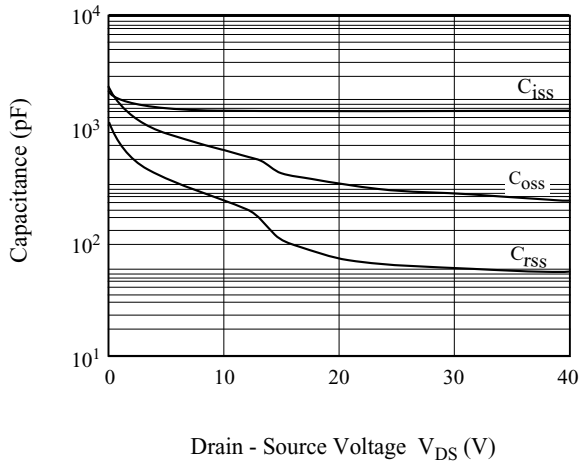


Fig8. Q_g - V_{GS}

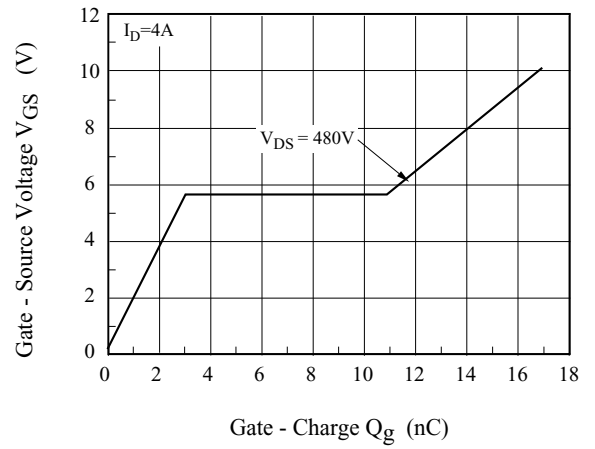


Fig9. Safe Operation Area

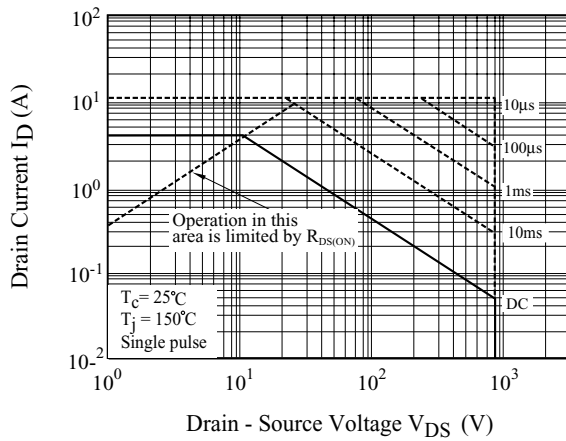


Fig10. Transient Thermal Response Curve

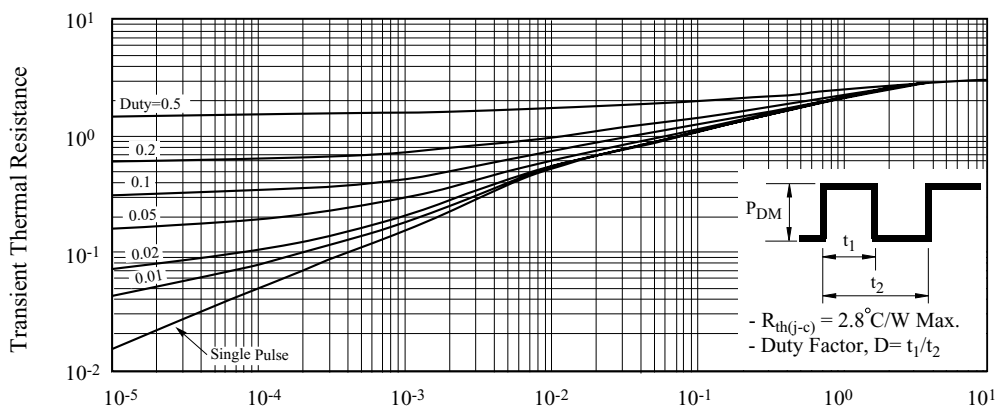


Fig11. Gate Charge

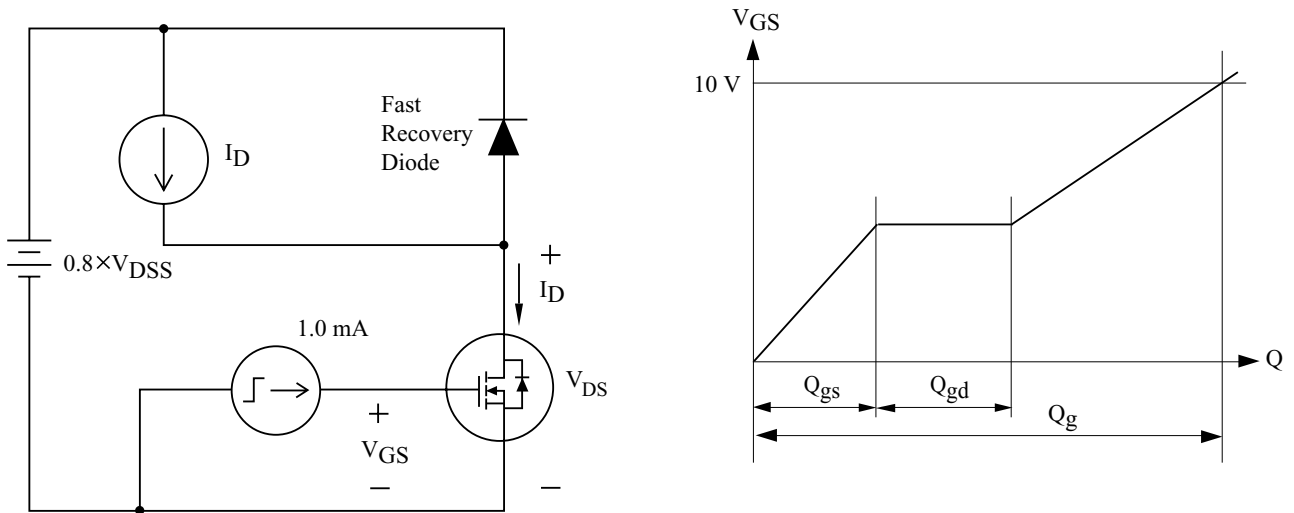


Fig12. Single Pulsed Avalanche Energy

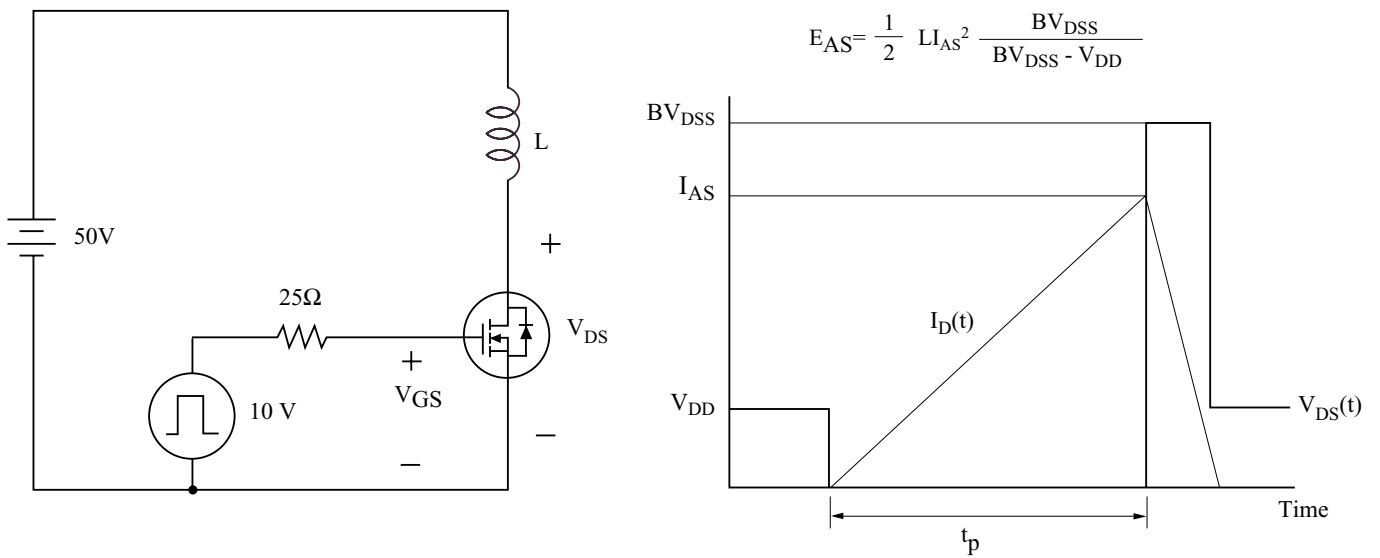


Fig13. Resistive Load Switching

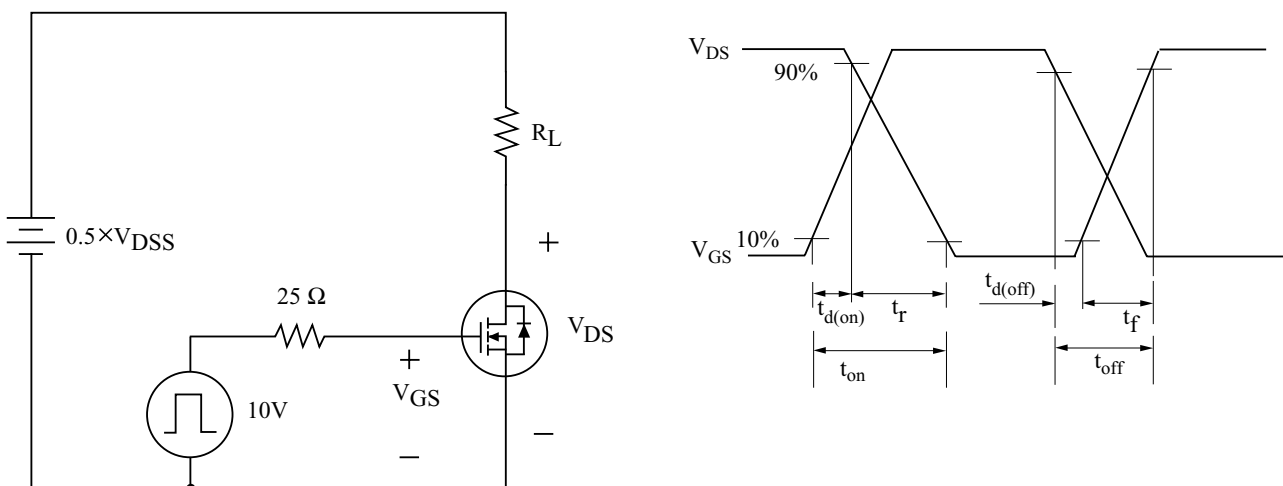


Fig14. Source - Drain Diode Reverse Recovery and dv/dt

