



AL3066

FOUR-CHANNEL CURRENT SOURCE BOOST LED DRIVER

Description

The AL3066 is a high efficiency 4-channel boost controller for WLED backlight applications. It operates over a wide input voltage ranged from 4.5V to 33V.

The current of the 4 channels is simply programmed from 20mA to 400mA with an external resistor. The current match between any channel is $\pm 1.5\%$ (typical). Its operating frequency can be adjusted from 0.1MHz to 1MHz.

The AL3066 can support two dimming modes: direct PWM dimming, PWM to analog dimming.

Robust protection features include cycle by cycle current limit, soft-start, UVLO, programmable OVP, OTP, open/short LED protection, Schottky Diode Short and Open Protection, Inductor Short-Circuit Protection and $V_{\rm OUT}$ Short protection.

The IC is available in SO-16 package.

Features

Notes:

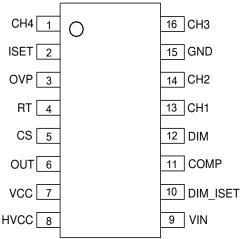
- Input Voltage Range: 4.5V to 33V
- Drivers Up to 4 Strings in Parallel, 250mA Per String, 400mA Pulse Current
- ±3% Current Precision
- Low Ripple for Low BOM Cost
- 4kV HBM ESD Class
- High Voltage Pins CS and OVP for Safety Test
- Support Direct PWM Dimming, PWM to Analog Dimming
- Minimum PWM Dimming Duty Cycle can be 1/10,000 at 100Hz Dimming Frequency
- LED Open/Short Protection
- Schottky Diode/Inductor Short-Circuit Protection
- Built-in OCP, OVP, OTP, UVLO, V_{OUT} Short/Schottky Diode Open Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Assignments



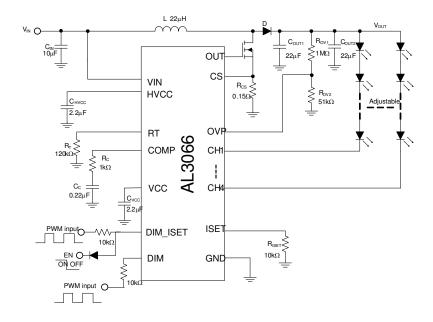


Applications

- LCD Monitor
- LCD Display Module
- LCD TV



Typical Applications Circuit

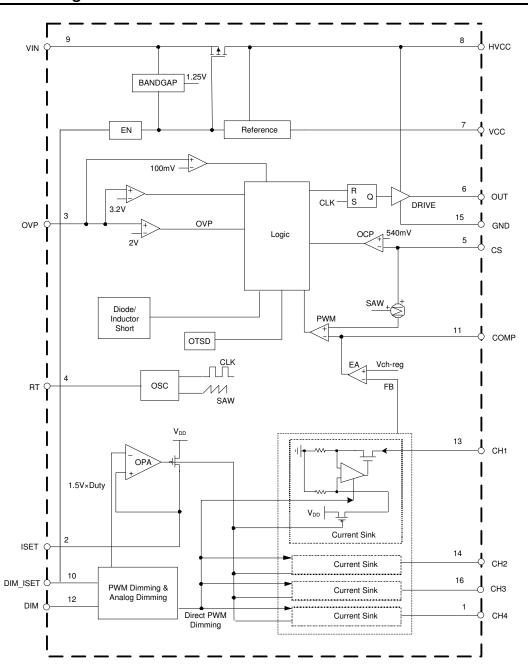


Pin Descriptions

Pin Number	Pin Name	Function
1	CH4	LED current sink 4. Leave the pin open directly if not used.
2	ISET	LED current set pin. The corresponding maximum current of all 4 strings is set through connecting a resister from this pin to GND.
3	OVP	Overvoltage protection pin. When the OVP pin voltage exceeds 2.0V, the OVP is triggered and the power switch is turned off. When the OVP pin voltage drops below Hysteresis voltage, the OVP is released and the power switch will resume normal operation.
4	RT	Frequency control pin.
5	CS	Power switch current sense input.
6	OUT	Boost converter power switch gate output. This pin outputs high voltage (10V) to drive the external N-MOSFET.
7	VCC	5V linear regulator output pin. This pin should be bypassed to GND with a ceramic capacitor.
8	HVCC	10V linear regulator output pin. This pin should be bypassed to GND with a ceramic capacitor.
9	VIN	Supply input pin. A capacitor (typical 10μF) should be connected between the VIN and GND to keep the DC input voltage constant.
10	DIM_ISET	PWM to Analog dimming pin and Enable pin. Apply a PWM signal to this pin to adjust ISET pin reference to regulate the output current.
11	COMP	Soft-start and control loop compensation.
12	DIM	PWM control dimming MOSFET pin. Apply a PWM signal to this pin to control LED dimming.
13	CH1	LED current sink 1. Leave the pin open directly if not used.
14	CH2	LED current sink 2. Leave the pin open directly if not used.
15	GND	Ground
16	CH3	LED current sink 3. Leave the pin open directly if not used.



Functional Block Diagram





Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified. Note 4, 5)

Symbol	Parameter	Rating	Unit
V _{IN}	Input Voltage	-0.3 to 40	V
V _{EN}	EN Pin Voltage	-0.3 to 7	V
Vcc	VCC Pin Voltage	-0.3 to 7	V
HVcc	HVCC Pin Voltage	-0.3 to 20	V
V _{CH}	CH1 to CH4 Pins Voltage	-0.3 to 60	V
Vcs	CS Pin Voltage	-0.3 to 42	V
V _{COMP}	COMP Pin Voltage	-0.3 to 7	V
V _{ISET}	ISET Pin Voltage	-0.3 to 7	V
Vout	OUT Pin Voltage	-0.3 to 20	V
V _{OVP}	OVP Pin Voltage	-0.3 to 42	V
V _{RT}	RT Pin Voltage	-0.3 to 7	V
V _{DIM_ISET}	DIM_ISET Pin Voltage	-0.3 to 7	V
V _{DIM}	DIM Pin Voltage	-0.3 to 7	V
V_{GND}	GND Pin Voltage	-0.3 to 0.3	V
θ _{JA}	Thermal Resistance (Junction to Ambient) (Note 6)	79	°C /W
θ_{JC}	Thermal Resistance (Junction to Case) (Note 6)	10	°C /W
TJ	Operating Junction Temperature	+150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10sec)	+260	°C
-	ESD (Machine Model)	200	V
-	ESD (Human Body Model)	4000	V

- Note: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" are not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.
 - 5. For better performance, the AL3066 should have high voltage pins CS and OVP. If CS or OVP pin is added to 16V, the IC will not smoke or burn.
 - 6. Device mounted on 2"x2" FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IN}	Input Voltage	4.5	33	٧
f _O	Operating Frequency	0.1	1	MHz
Існ	LED Channel Current	20	400	mA
f _{PWM1}	DIM pin PWM Dimming Frequency	0.1	25	kHz
f _{PWM2}	DIM_ISET Pin PWM Dimming Frequency	10	100	kHz
T _A	Operating Ambient Temperature	-40	+85	°C



Electrical Characteristics (@T_{A=+25°C}, V_{IN=12V}, V_{DIM_ISET=5V}, unless otherwise specified.)

Symbol	Parameter	Condition	Min	Тур	Max	Unit
Input Supply	,		1	1		•
V _{IN}	Input Voltage	-	4.5	-	33	V
	O de constant	V _{DIM} =0V, V _{DIM_ISET} =5V	-	3	-	mA
ΙQ	Quiescent Current	V _{DIM} =V _{DIM} _ISET=5V	-	5	-	mA
I _{SHDN}	Shutdown Supply Current	V _{DIM_ISET} =0V	-	1	-	μΑ
V _{UVLO}	Under-Voltage Lockout Voltage	V _{IN} Rising	3.7	4.0	4.3	V
V_{HYS}	UVLO Hysteresis	-	-	200	-	mV
V _{CC} Regulator						
HV _{CC}	HV _{CC} Voltage	V _{IN} ≥10.5V	-	10	-	V
11400	Tivee voitage	V _{IN} <10.5V	-	V _{IN} -0.5	-	V
V _{CC}	V _{CC} Voltage	V _{IN} ≥5.5V	-	5	-	V
V CC	VCC Vollage	V _{IN} <5.5V	-	V _{IN} -0.5	-	V
trise	OUT Pin Rise Time	OUT Pin Load =1nF	-	30	-	ns
t _{FALL}	OUT Pin Fall Time	OUT Pin Load =1nF	-	30	-	ns
-	Load Regulation	Load=0 to 30mA	-	5	-	mV/mA
-	Line Regulation	V _{IN} =12V to 33V	-	0.3	-	mV/V
High Frequency Oscillator						
fosc ₁	Switch Frequency	R _T =100kΩ	450	500	550	kHz
-	Switch Frequency Range	-	0.1	1	1	MHz
D _{MAX}	Max. Duty Cycle	R _T =100kΩ	80	90	-	%
Enable Logic and Dimming	g Logic					
V _{DIM_ISET_H}	DIM_ISET Logic for External	Logic High	2.5	-	-	V
V _{DIM_ISET_L}	Dimming	Logic Low	-	-	0.3	V
V_{DIM_H}	DIM Logic for External	Logic High	2.5	1	1	V
V_{DIM_L}	Dimming	Logic Low	-	-	0.3	V
Power Switch Drive						
V _{LIMIT1}	Current Limit Threshold Voltage	-	480	540	600	mV
V_{LIMIT2}	D/L Short Threshold Voltage	-	720	800	880	mV
t _{LEB}	Current Sense LEB Time (Note 7)	-	80	100	150	ns
Compensation and Soft St	/	1	1			
l _{О_Н}	Sourcing Current	V _{COMP} =0.5V	-	120	-	μΑ
I _{O_L}	Sinking Current	V _{COMP} =2V	-	120	-	μΑ
	•	<u> </u>		<u> </u>		



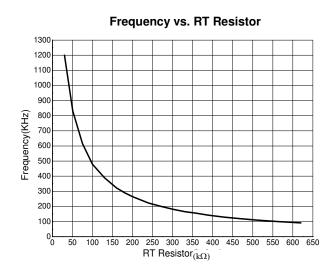
Electrical Characteristics (Cont. @T_{A=+25°C}, V_{IN}=12V, V_{DIM_ISET}=5V, unless otherwise specified.)

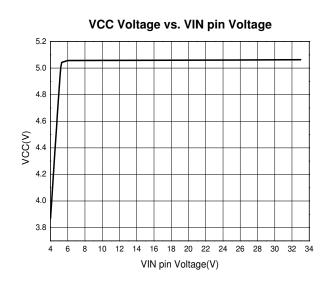
Symbol	Parameter	Condition	Min	Тур	Max	Unit	
Over Voltage Protection	Over Voltage Protection						
V _{OVP}	OVP Threshold Voltage	V _{OUT} Rising	1.9	2.0	2.1	V	
Vovp_HYS	OVP Hysteresis	-	-	200	-	mV	
V _{OVP-SH}	Shutdown Under Abnormal Condition	-	3.0	3.2	3.4	V	
Current Source	Current Source						
I _{CH_MATCH}	LED Current Matching between Each String (Note 8)	I _{CH} =100mA	-	1.5	2.7	%	
Існ	Regulation Current per Channel	R _{ISET} =12kΩ	97	100	103	mA	
V _{LED_REG}	Minimum LED Regulation Voltage	I _{CH} =120mA	-	500	-	mV	
I _{LED_LEAK}	CH1 to CH4 Leakage Current	V _{EN} =0V, V _{LED} =37V	-	0.1	1	μΑ	
V _{LED-S}	LED Short Protection Threshold	-	4.9	5.4	5.9	V	
Over Temperature Protection							
T _{OTSD}	Thermal Shutdown Temperature (Note 7)	-	+155	+160	+165	°C	
T _{HYS}	Thermal Shutdown Temperature Hysteresis (Note 7)	-	-	+30	-	°C	

Notes: 7. Guaranteed by Design.
 8.
$$I_{CH_MATCH} = \frac{I_{MAX} - I_{MIN}}{2 \times I_{AVG}} \times 100\%$$

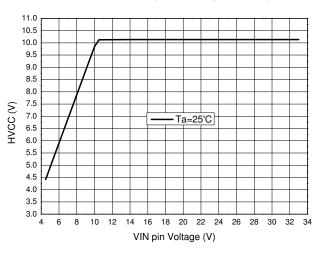


Typical Performance Characteristic (@TA = +25°C, VIN=24V, VDIM_ISET=VDIM=5V, unless otherwise specified.)

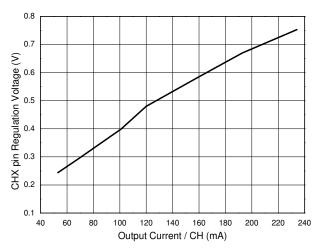




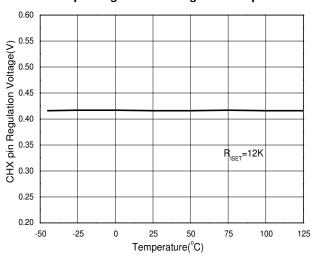
HVCC Voltage vs. VIN pin Voltage



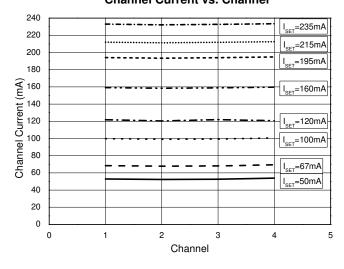
CHX pin Regulation Voltage vs. Output Current /CH



CHX pin Regulation Voltage vs. Temperature



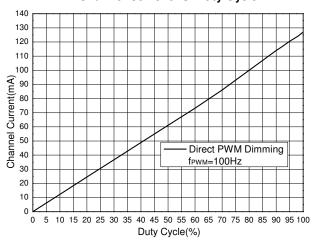
Channel Current vs. Channel



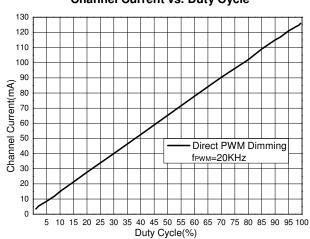


Typical Performance Characteristic (@T_A = +25°C, V_{IN}=24V, V_{DIM_ISET}=V_{DIM}=5V, unless otherwise specified.)

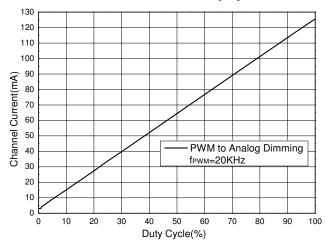
Direct PWM Dimming @ fPWM=100Hz Channel Current vs. Duty Cycle



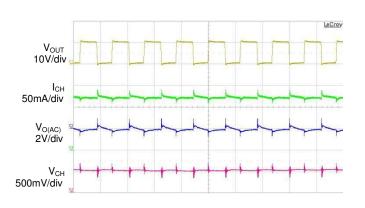
Direct PWM Dimming @ fPWM=20kHz Channel Current vs. Duty Cycle



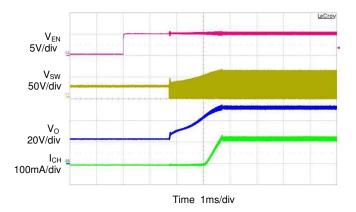
PWM to Analog Dimming @ fPWM=20kHz Channel Current vs. Duty Cycle



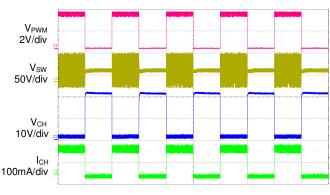
Steady State



System Startup



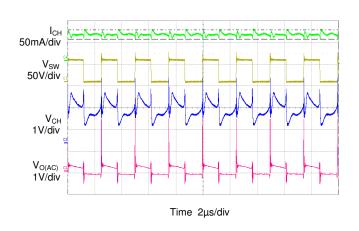
Direct PWM Dimming



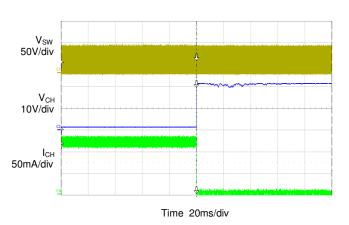


$\textbf{Typical Performance Characteristic} \ (@T_A = +25^{\circ}C,\ V_{IN} = 24V,\ V_{DIM_ISET} = V_{DIM} = 5V,\ unless\ otherwise\ specified.)$

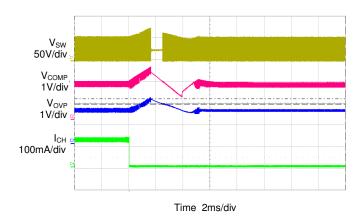
PWM to Analog Dimming



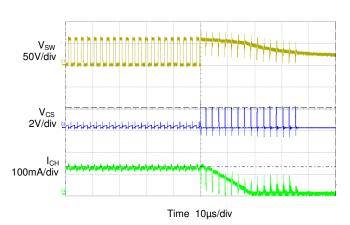
LED Short Protection



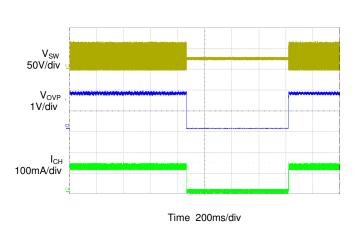
LED Open Protection



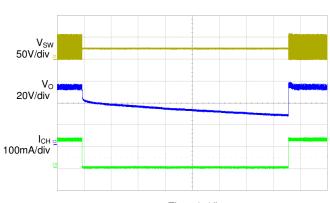
Schottky/Inductor Short Protection



VOUT Short/Diode Open Protection



Over Temperature Protection





Application Information

Enable

The AL3066 is enabled when the voltage at EN pin is greater than approximately 2.4V, and disabled when lower than 0.5V.

Frequency Selection

An external resistor R_T , placed between RT pin and GND, can be used to set the operating frequency. The operating frequency ranges from 100kHz to 1MHz. The high frequency operation optimizes the regulator for the smallest-sized component application, while low frequency operation can help to reduce switch loss. The approximate operating frequency can be expressed as below:

$$f_{OSC}[MHz] = \frac{52}{R_T[K\Omega]}$$

LED Current Setting

The maximum LED current per channel can be adjusted up to 400mA via ISET pin. When \geq 400mA current is needed in application, two or more channels can be paralleled to provide larger drive current. A resistor R_{ISET} is connected between ISET pin and GND to set the reference current I_{SET}. The LED current can be expressed as below:

$$I_{LED}[mA] = \frac{1200}{R_{ISET}[K\Omega]}$$

Dimming Control

1.) Direct PWM Dimming Control

Compared to Analog dimming, PWM dimming offers superior dimming resolution and reduced LED color shift. Apply a PWM signal to the DIM pin to achieve direct PWM dimming. The LED current of all enabled channels can be adjusted at the same time and the LED brightness can be adjusted from 1%×I_{CH MAX} to 100%×I_{CH MAX}.

During the "high level" period of PWM signal, the LED is turned on and 100% of the current flows through the LED, while during the "low level" period of the PWM signal, the LED is turned off and almost no current flows through the LED. Changing the average current through the LED can adjust the LED brightness.

The external PWM signal frequency applied to DIM pin can be 100Hz or higher and the minimum PWM duty can be 1/10,000 at 100Hz dimming frequency.

2.) PWM to Analog Dimming Control

Apply a PWM signal to DIM_ISET pin to modulate ISET pin reference voltage to achieve PWM to analog dimming. The AL3066 chops up the internal reference voltage at the duty cycle of the PWM signal. The pulse signal is then filtered by an internal low pass filter. The output of this filter contains an average DC component as ISET pin reference voltage thereby modulating the amplitude of the LED current.

3-Channel Operation

For applications where only three LED strings are used, the AL3066 provides design flexibility by disabling the unused channel in two methods.

1.) Disable the unused channel by floating

Leave any of the 4 channels (CH1~CH4) floating to disable the unused channel. The AL3066 integrates an LED Open-Circuit Protection circuit. At each start up, VOUT will boost up until the voltage at OVP pin reaches an approximate threshold of 2.0V and then disable the floating channel, the remaining channels will continue normal operation.

2.) Disable the third channel CH3 by pulling high

Disable the third channel CH3 by pulling CH3 high to a DC voltage higher than LED short protection threshold (5.4V typically), usually connected to HVCC pin. The AL3066 integrates an LED Short-Circuit Protection circuit. If the voltage at CH3 pin exceeds a threshold of approximately 5.4V during normal operation, CH3 is disabled. At each start up, CH3 will be disabled and the remaining channels will continue normal operation.



Application Information (Cont.)

Protection

1.) Over Voltage Protection

The AL3066 integrates an OVP circuit. The OVP pin is connected to the center tap of voltage-divider (ROV1 and ROV2) connected between high voltage output and GND.

If the voltage at OVP pin exceeds 2.0V, which may result from open loop or excessive output voltage, all the functions of the AL3066 will be disabled with output voltage falling. The OVP hysteresis is 200mV.

2.) Over-Current Protection

The AL3066 integrates an OCP circuit. The CS pin is connected to the voltage-sensor (RCS) placed between the source of the MOSFET and GND. If the voltage at CS pin exceeds 0.54V, the MOSFET is turned off immediately and will not turn on until the next cycle begins.

3.) LED Short-Circuit Protection

The AL3066 integrates an LED Short-Circuit Protection circuit. If the voltage at any of the CH1 to CH4 pins exceeds a threshold of approximately 5.4V during normal operation, the corresponding channel is latched off. Toggle V_{IN} or EN to reset the latch. LED short detecting logic priority is lower than open LED and OVP logic. The LED short detecting is triggered when 0.1V<V_{LED_MIN} under dimming on mode, and disabled when LED open occurs until output voltage resumes to the regulated voltage.

4.) LED Open-Circuit Protection

The AL3066 integrates an LED Open-Circuit Protection circuit. When any LED string is open, V_{OUT} will boost up until the voltage at OVP pin reaches an approximate threshold of 2.0V. The IC will automatically ignore the open string whose corresponding pin voltage is less than 100mV and the remaining string will continue operation. If all the strings are open and the voltage at OVP pin reaches a threshold of 2.0V, the MOSFET drive gate will turn off and the IC will shut down and latch.

5.) V_{OUT} Short/Open Schottky Diode Protection

The AL3066 monitors the OVP pin, if the OVP pin voltage is less than 0.1V, MOSFET drive output will turn off. This protects the converter if the output Schottky diode is open or V_{OUT} is shorted to ground.

6.) Under-Voltage Lockout

The AL3066 provides an under voltage lockout circuit to prevent it from undefined status when it starts up. The UVLO circuit shuts down the device when V_{CC} drops below 3.8V. The UVLO circuit has 200mV hysteresis, which means the device starts up again when V_{CC} rises to 4.0V.

7.) Over-Temperature Protection

The AL3066 features Over-Temperature Protection. If the junction temperature exceeds approximately +160°C, the IC will shut down until the junction temperature is less than approximately +140°C. When the IC is released from over temperature shutdown, it will start a soft-start process.

8.) Schottky Diode/Inductor Short-Circuit Protection

The AL3066 features Schottky Diode/Inductor Short-Circuit protection circuit. When CS pin voltage exceeds 0.8V for greater than 16 switching clocks, the IC will latch off. The voltage of CS pin is monitored after a short delay of t_{LEB}.

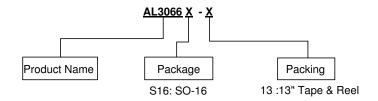
9.) Shut Down under Abnormal Condition

The AL3066 features Shutdown under Abnormal Condition Protection circuit. When the OVP pin voltage exceeds 3.2V, the IC will latch off. Toggle EN pin to restart the IC. This feature can be used to shut down the IC under any defined abnormal condition.

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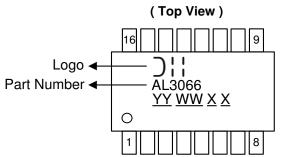


Ordering Information



Part Number	Poekogo Codo	Dookogo	13"Tape & Reel		
Part Number	Package Code	Package	Quantity	Part Number Suffix	
AL3066S16-13	S16	SO-16	2500	-13	

Marking Information



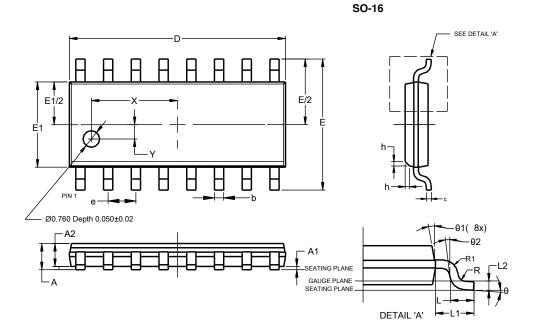
YY: Year: 13, 14,15~ WW: Week: 01~52; 52 represents 52 and 53 week

 $\underline{X} \underline{X}$: Internal Code



Package Outline Dimensions

 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

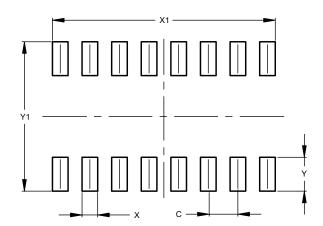


SO-16					
Dim	Min	Max	Тур		
Α		1.260			
A 1	0.10	0.23			
A2	1.02				
b	0.31	0.51			
С	0.10	0.25			
D	9.80	10.00			
E	5.90	6.10			
E1	3.80	4.00			
е		.27 BS	0		
h	0.15	0.25	0.20		
L	0.40	1.27			
L1	1	.04 RE	F		
L2	C).25 BS(
R	0.07	-			
R1	0.07				
X	3.945 REF				
Υ	0.661 REF				
θ	0°	8°			
θ1	5°	15°			
θ2	0°				
All Dimensions in mm					

Suggested Pad Layout

 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

SO-16



Dimensions	Value (in mm)		
С	1.270		
X	0.670		
X1	9.560		
Y	1.450		
Y1	6.400		

February 2017



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 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

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