

RKP300KK

Silicon Epitaxial Planar Pin Diode for Wireless LAN

REJ03G1616-0100

Rev.1.00

Jan 28, 2008

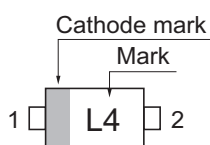
Features

- Suitable for an antenna switches of wireless LAN and a cordless telephone.
- Super -Low capacitance. ($C = 0.25 \text{ pF max}$)
- Low forward resistance. ($r_f = 3.7 \Omega \text{ max}$)
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code
RKP300KK	L4	SFP	PUSF0002ZB-A

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	30	V
Forward current	I_F	50	mA
Power dissipation	P_d	100	mW
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_R	—	—	100	nA	$V_R = 30\text{ V}$
Forward voltage	V_F	—	—	1.0	V	$I_F = 10\text{ mA}$
Capacitance	C	—	—	0.25	pF	$V_R = 1\text{ V}$, $f = 1\text{ MHz}$
Forward resistance	r_f	—	—	3.7	Ω	$I_F = 10\text{ mA}$, $f = 100\text{ MHz}$

Note: For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristics

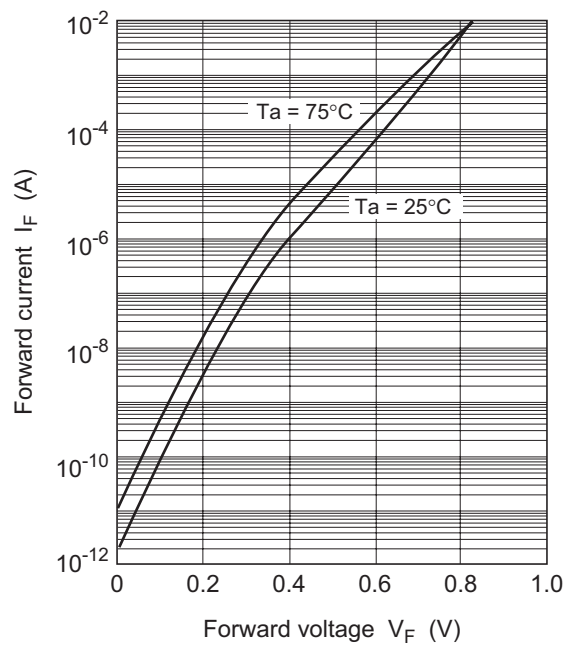


Fig.1 Forward current vs. Forward voltage

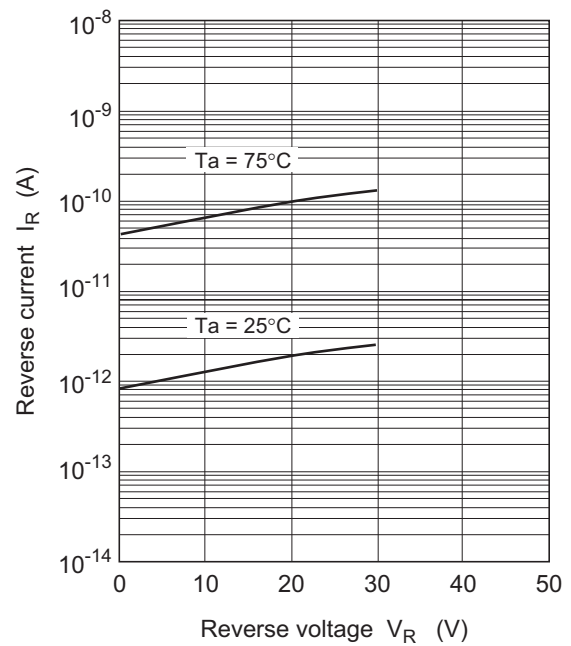


Fig.2 Reverse current vs. Reverse voltage

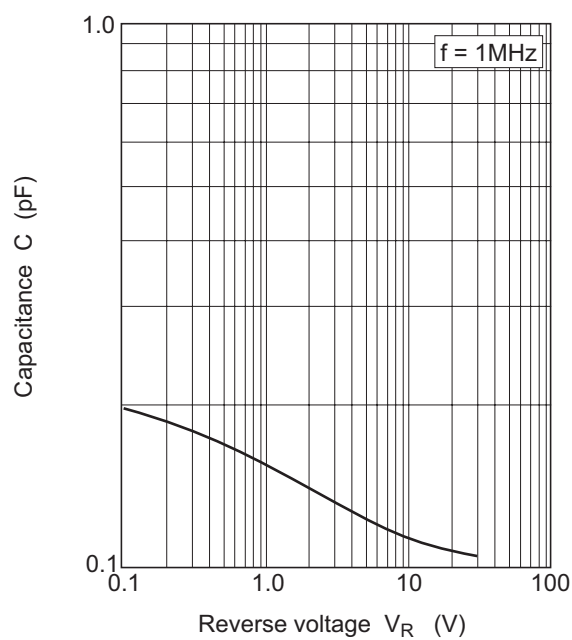


Fig.3 Capacitance vs. Reverse voltage

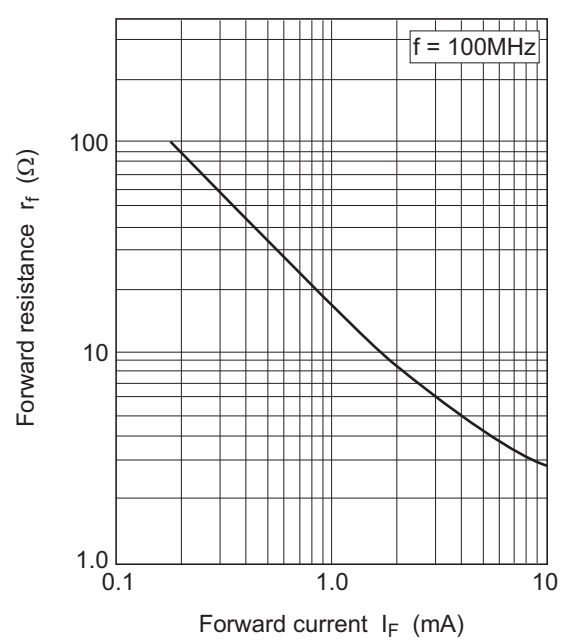
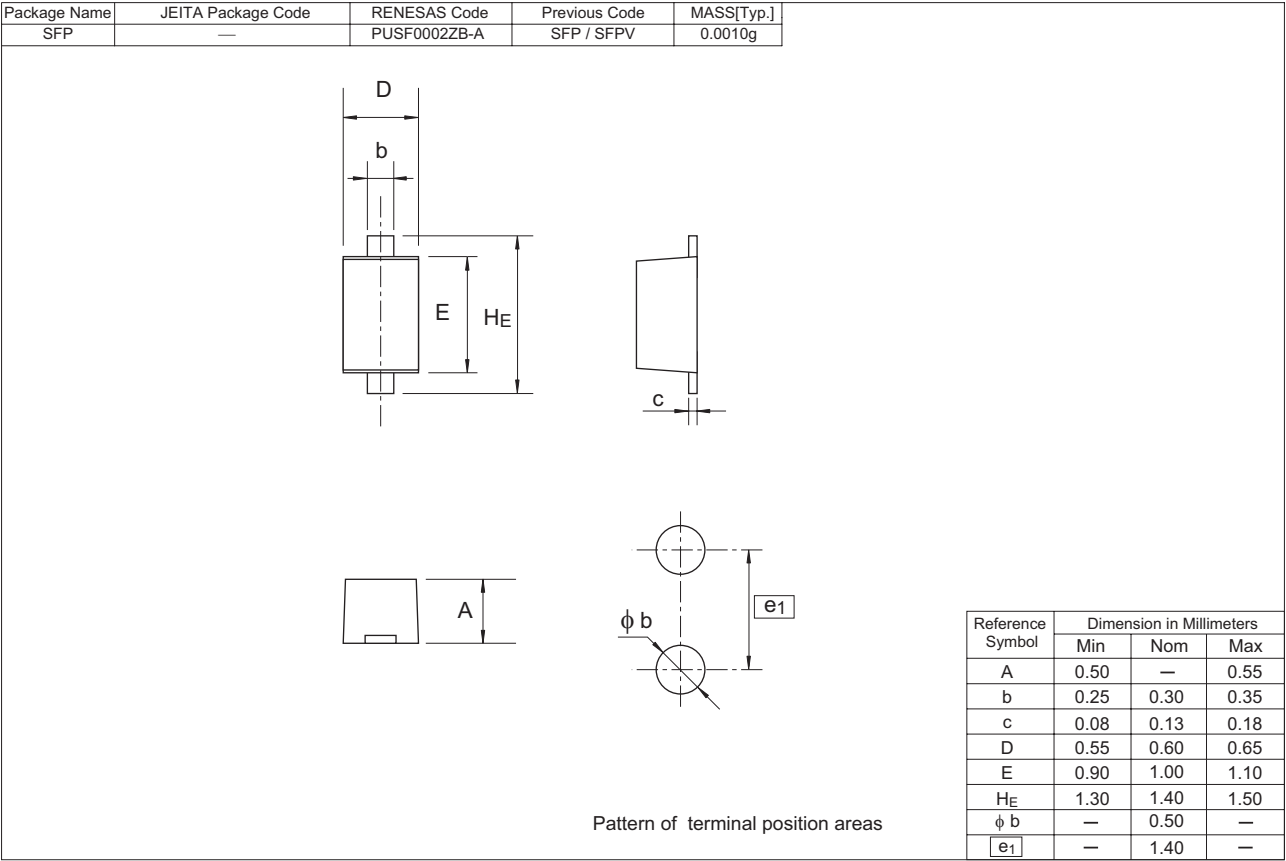


Fig.4 Forward resistance vs. Forward current

Package Dimensions



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