

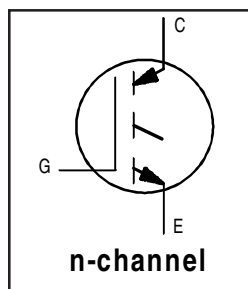
IRGMC30F

INSULATED GATE BIPOLAR TRANSISTOR

Fast Speed IGBT

Features

- Electrically Isolated and Hermetically Sealed
- Simple Drive Requirements
- Latch-proof
- Fast Speed operation 3 kHz - 8 kHz
- High operating frequency
- Switching-loss rating includes all "tail" losses



$$V_{CES} = 600V$$

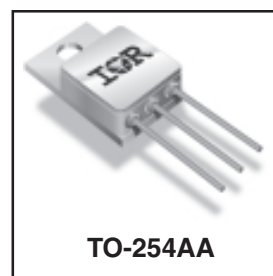
$$V_{CE(on) \max} = 2.1V$$

$$@ V_{GE} = 15V, I_C = 12A$$

Description

Insulated Gate Bipolar Transistors (IGBTs) from International Rectifier have higher usable current densities than comparable bipolar transistors, while at the same time having simpler gate-drive requirements of the familiar power MOSFET. They provide substantial benefits to a host of high-voltage, high-current applications.

The performance of various IGBTs varies greatly with frequency. Note that IR now provides the designer with a speed benchmark ($f_{IC/2}$, or the "half-current frequency"), as well as an indication of the current handling capability of the device.



TO-254AA

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Breakdown Voltage	600	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	23	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	12	
I_{CM}	Pulsed Collector Current ①	92	
I_{LM}	Clamped Inductive Load Current ②	92	
V_{GE}	Gate-to-Emitter Voltage	± 20	V
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	75	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	30	
T_J	Operating Junction and	-55 to + 150	$^\circ C$
T_{STG}	Storage Temperature Range		
	Lead Temperature	300 (0.063in./1.6mm from case for 10s)	
	Weight	9.3 (typical)	g

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJC}	Junction-to-Case	—	—	1.67	$^\circ C/W$	
R_{thCS}	Case-to-Sink	—	0.21	—		
R_{thJA}	Junction-to-Ambient	—	—	48		

For footnotes refer to the last page
www.irf.com

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	600	—	—	V	$V_{GE} = 0V, I_C = 1.0\text{ mA}$
$V_{(BR)ECS}$	Emitter-to-Collector Breakdown Voltage ③	24	—	—	V	$V_{GE} = 0V, I_C = 1.0\text{ A}$
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	0.69	—	V/ $^\circ\text{C}$	$V_{GE} = 0V, I_C = 1.0\text{ mA}$
$V_{CE(ON)}$	Collector-to-Emitter Saturation Voltage	—	—	2.1	V	$I_C = 12A$ $V_{GE} = 15V$
		—	2.4	—		$I_C = 23A$ See Fig.5
		—	2.2	—		$I_C = 12A, T_J = 125^\circ\text{C}$
$V_{GE(th)}$	Gate Threshold Voltage	3.0	—	5.5		$V_{CE} = V_{GE}, I_C = 250\text{ }\mu\text{A}$
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	—	-11	—	mV/ $^\circ\text{C}$	$V_{CE} = V_{GE}, I_C = 250\text{ }\mu\text{A}$
g_{fe}	Forward Transconductance ④	6.1	—	—	S	$V_{CE} \geq 15V, I_C = 12A$
I_{CES}	Zero Gate Voltage Collector Current	—	—	50	μA	$V_{GE} = 0V, V_{CE} = 480V$
		—	—	1000		$V_{GE} = 0V, V_{CE} = 480V, T_J = 125^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{GE} = \pm 20V$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	27	54	nC	$I_C = 12A$
Q_{ge}	Gate - Emitter Charge (turn-on)	—	4	8		$V_{CC} = 300V$ See Fig. 8
Q_{gc}	Gate - Collector Charge (turn-on)	—	12	24		$V_{GE} = 15V$
$t_{d(on)}$	Turn-On Delay Time	—	—	50	ns	$I_C = 12A, V_{CC} = 480V$ Energy losses include "tail" See Fig. 9, 10, 13
t_r	Rise Time	—	—	42		
$t_{d(off)}$	Turn-Off Delay Time	—	—	320		
t_f	Fall Time	—	—	500		
E_{on}	Turn-On Switching Loss	—	0.4	—	mJ	$T_J = 125^\circ\text{C}$ $I_C = 12A, V_{CC} = 480V$ $V_{GE} = 15V, R_G = 7.5\Omega$ Energy losses include "tail" See Fig. 11, 13
E_{off}	Turn-off Switching Loss	—	2.1	—		
E_{ts}	Total Switching Loss	—	2.5	3.5		
$t_{d(on)}$	Turn-On Delay Time	—	25	—	ns	$T_J = 125^\circ\text{C}$ $I_C = 12A, V_{CC} = 480V$ $V_{GE} = 15V, R_G = 7.5\Omega$ Energy losses include "tail" See Fig. 11, 13
t_r	Rise Time	—	21	—		
$t_{d(off)}$	Turn-Off Delay Time	—	290	—		
t_f	Fall Time	—	590	—		
E_{ts}	Total Switching Loss	—	4.0	—	mJ	
$L_C + L_E$	Total Inductance	—	6.8	—	nH	Measured from Collector lead (6mm / 0.25in. from package) to Emitter lead (6mm / 0.25in. from package)
C_{ies}	Input Capacitance	—	670	—	pF	$V_{GE} = 0V$
C_{oes}	Output Capacitance	—	100	—		$V_{CC} = 30V$ See Fig. 7
C_{res}	Reverse Transfer Capacitance	—	10	—		$f = 1.0\text{MHz}$

Note: Corresponding Spice and Saber models are available on the Website.

For footnotes refer to the last page

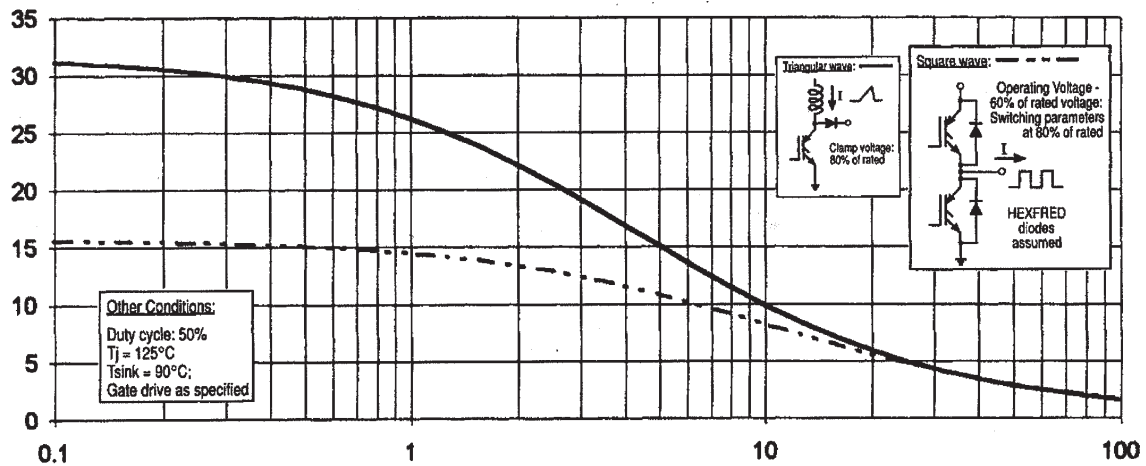


Fig. 1 - Typical Load Current vs. Frequency
(For square wave, $I = I_{RMS}$ of fundamental; for triangular wave, $I = I_{PK}$)

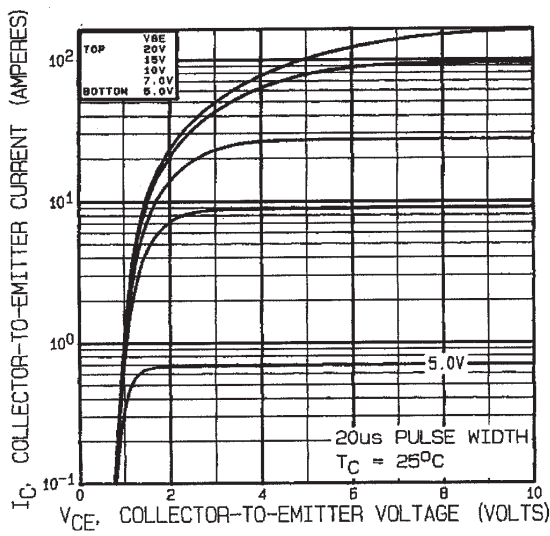


Fig. 2 - Typical Output Characteristics

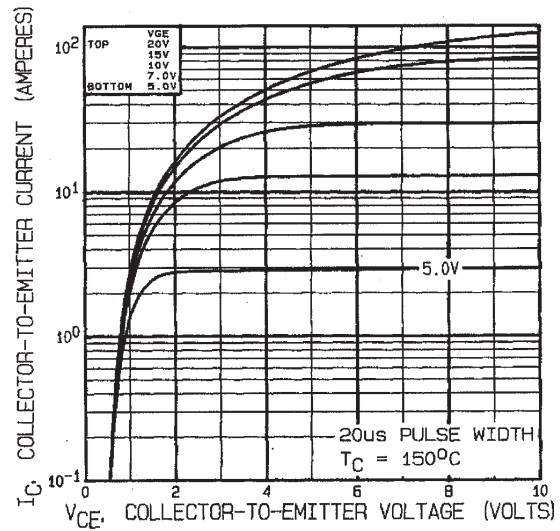


Fig. 3 - Typical Transfer Characteristics

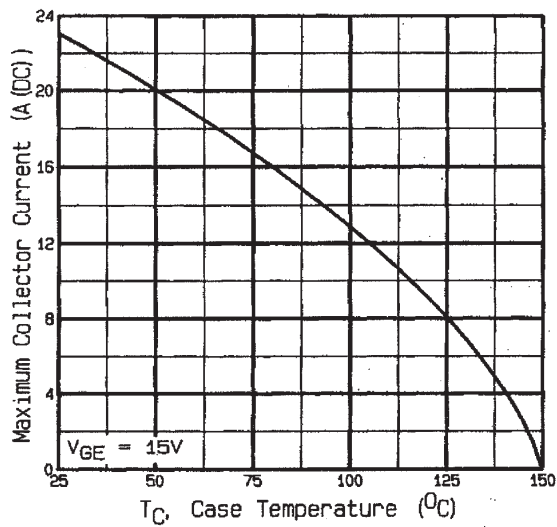


Fig. 4 - Maximum Collector Current vs. Case Temperature

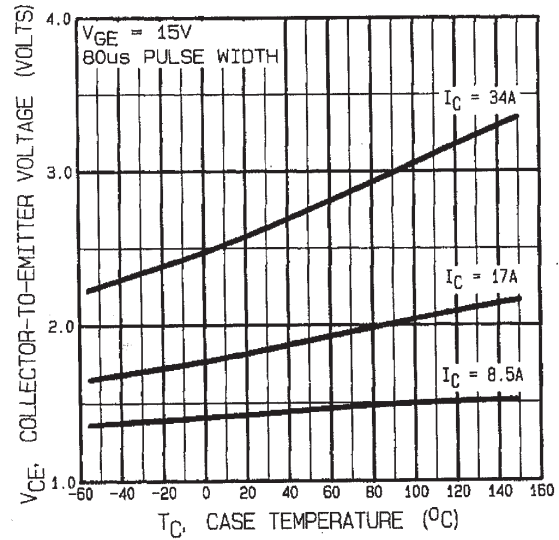


Fig. 5 - Collector-to-Emitter Voltage vs. Junction Temperature

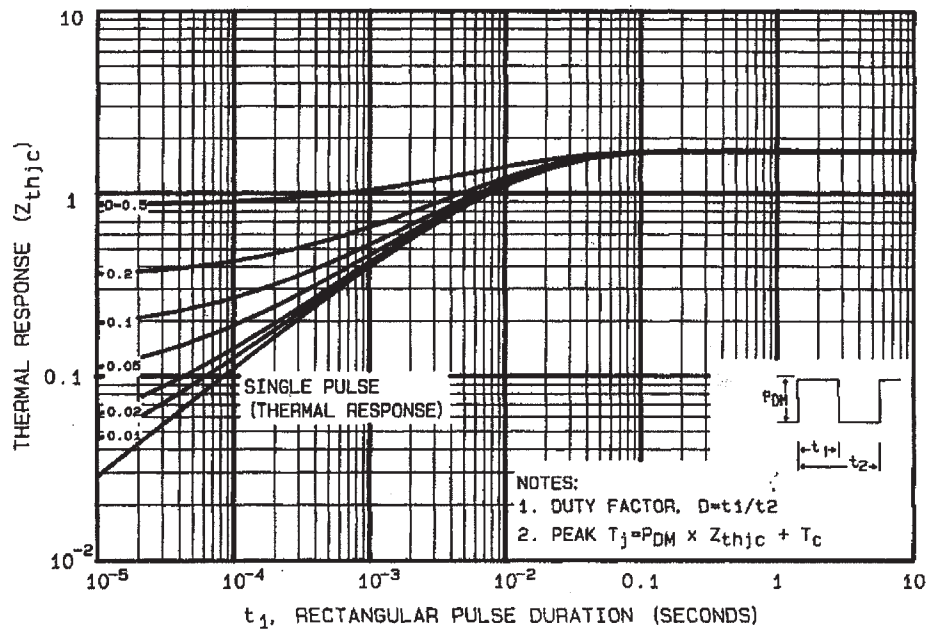


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

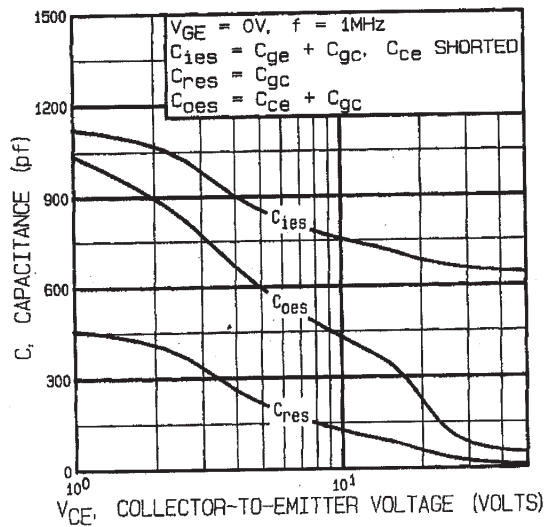


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

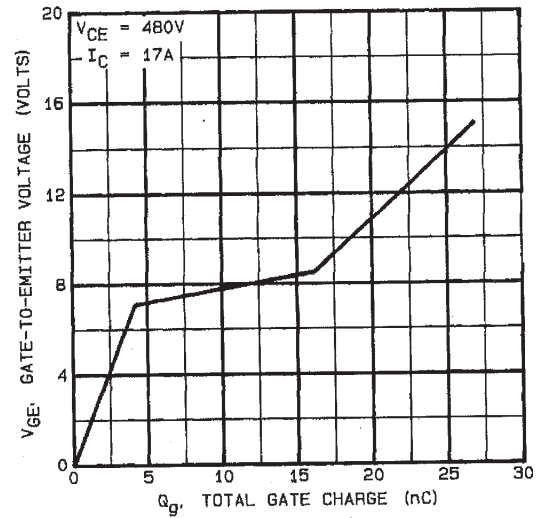


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

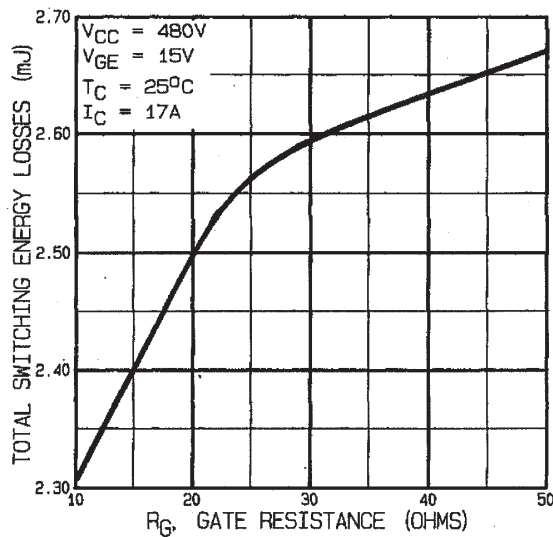


Fig. 9 - Typical Switching Losses vs. Gate Resistance

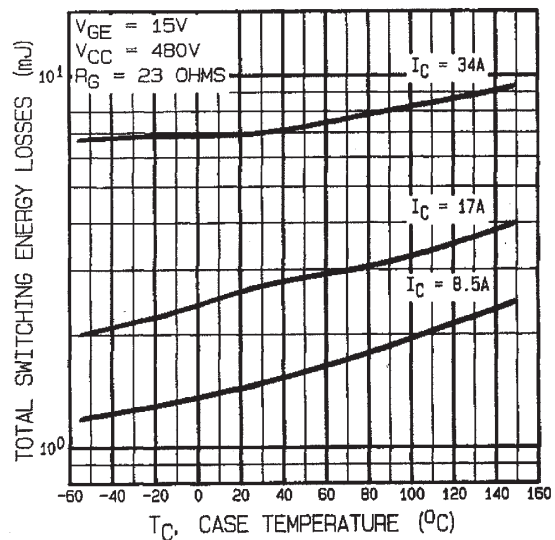


Fig. 10 - Typical Switching Losses vs. Junction Temperature

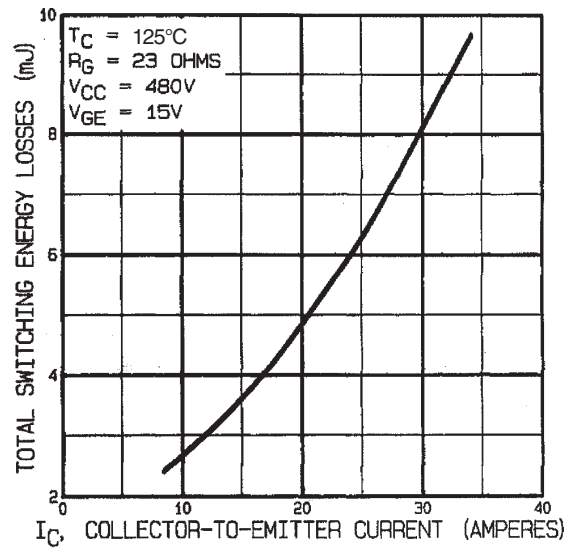


Fig. 11 - Typical Switching Losses vs.
Collector-to-Emitter Current

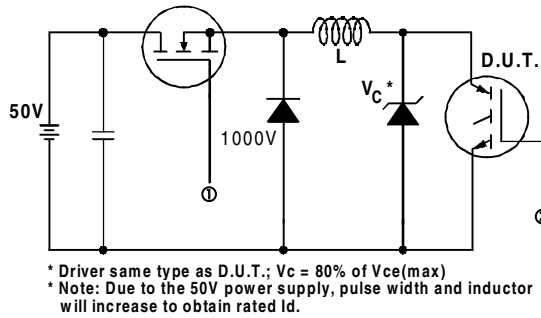


Fig. 12a - Clamped Inductive Load Test Circuit

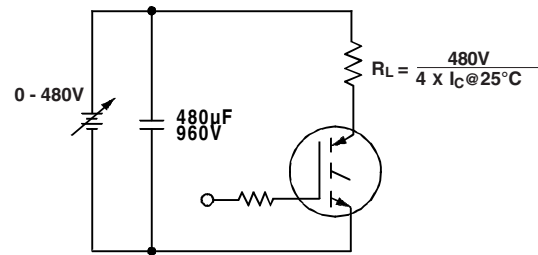


Fig. 12b - Pulsed Collector Current Test Circuit

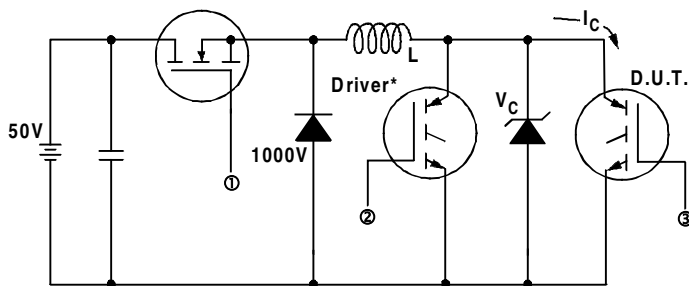


Fig. 13a - Switching Loss Test Circuit

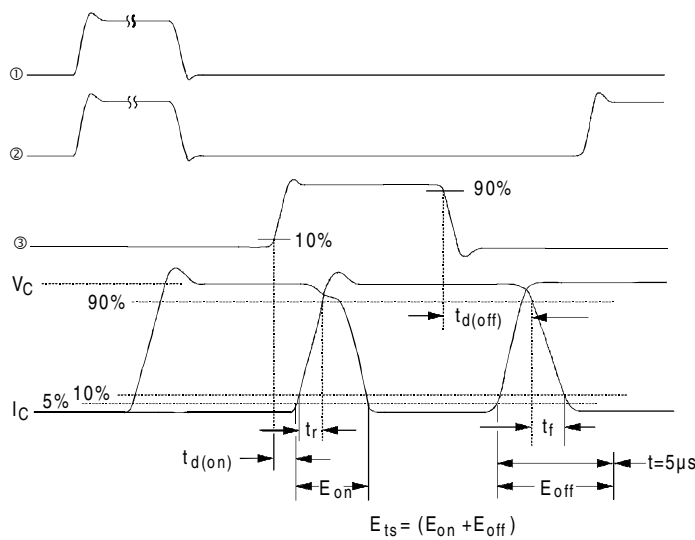


Fig. 13b - Switching Loss Waveforms

① Repetitive rating; $V_{GE} = 20V$, pulse width limited by max. junction temperature.

② $V_{CC} = 80\%(V_{CES})$, $V_{GE} = 20V$, $L = 10\mu H$, $R_G = 10\Omega$

③ Pulse width $\leq 80\mu s$; duty factor $\leq 0.1\%$.

④ Pulse width $5.0\mu s$, single shot.

2K

Dimensions (mm) (inches):

- Top View:
 - Overall Width: 13.84 (545) / 13.59 (535)
 - Overall Height: 20.32 (800) / 20.07 (790)
 - Internal Width: 17.40 (685) / 16.89 (665)
 - Internal Height: 17.40 (685) / 16.89 (665)
 - Bottom Width: 3.81 (150)
 - Bottom Height: 1.14 (0.45) / 0.89 (0.35)
 - Internal Width (1): 1.14 (0.45) / 0.89 (0.35)
 - Internal Width (2): 1.14 (0.45) / 0.89 (0.35)
 - Internal Width (3): 1.14 (0.45) / 0.89 (0.35)
- Side View:
 - Overall Width: 6.60 (260) / 6.32 (249)
 - Overall Height: 13.84 (545) / 13.59 (535)
 - Internal Width: 0.84 (0.33) MAX.
 - Bottom Width: 3.81 (150)

3K

Dimensions (mm) (inches):

- Top View:
 - Overall Width: 13.84 (545) / 13.59 (535)
 - Overall Height: 20.32 (800) / 20.07 (790)
 - Internal Width: 17.40 (685) / 16.89 (665)
 - Internal Height: 17.40 (685) / 16.89 (665)
 - Bottom Width: 3.81 (150)
 - Bottom Height: 1.14 (0.45) / 0.89 (0.35)
 - Internal Width (1): 1.14 (0.45) / 0.89 (0.35)
 - Internal Width (2): 1.14 (0.45) / 0.89 (0.35)
 - Internal Width (3): 1.14 (0.45) / 0.89 (0.35)
- Side View:
 - Overall Width: 6.60 (260) / 6.32 (249)
 - Overall Height: 13.84 (545) / 13.59 (535)
 - Internal Width: 0.84 (0.33) MAX.
 - Bottom Width: 3.81 (150)

Tolerances

Feature	Symbol	Value
Top View	ϕ	0.36 (0.014)
Side View	ϕ	0.36 (0.014)
Bottom View	ϕ	0.36 (0.014)
Internal Width (1)	ϕ	0.36 (0.014)
Internal Width (2)	ϕ	0.36 (0.014)
Internal Width (3)	ϕ	0.36 (0.014)

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
3. CONTROLLING DIMENSION: INCH.
4. CONFORMS TO JEDEC OUTLINE TO-254AA.

1 = COLLECTOR
2 = EMITTER
3 = GATE

BERYLLIA WARNING PER MIL-PRF-19500

Packages containing beryllia shall not be ground, sandblasted, machined or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxide packages shall not be placed in acids that will produce fumes containing beryllium.

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7903

Visit us at www.irf.com for sales contact information.

Data and specifications subject to change without notice. 02/02