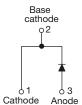


COMPLIANT

HALOGEN FREE

High Performance Schottky Rectifier, 20 A





2L TO-220AC

Single

TO-220AC 2L

Package

Circuit configuration

PRIMARY CHARACTERISTICS								
I _{F(AV)} 20 A								
V_{R}	15 V							
V _F at I _F	See Electrical table							
I _{RM} max.	600 mA at 100 °C							
T _J max.	125 °C							
F.o.	10 m l							

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- · Optimized for OR-ing applications
- Ultra low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified according to JEDEC®-JESD47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES UN								
I _{F(AV)}	Rectangular waveform	20	Α					
V_{RRM}		15	V					
I _{FSM}	t _p = 5 μs sine	700	Α					
V _F	19 A _{pk} , T _J = 125 °C (typical)	0.25	V					
TJ	Range	-55 to +125	°C					

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-STPS20L15D-M3	UNITS					
Maximum DC reverse voltage	V_R	15	V					
Maximum working peak reverse voltage	V_{RWM}	19	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS					
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle, T _C = 85 °C, r	20	А				
Maximum peak one cycle non-repetitive surge current	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	700	Α			
See fig. 7	10 ms sine or 6 ms rect. pulse Condition and with rated V _{RRM} applied			330				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 6 \text{mHz}$	10	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxin	2	Α				



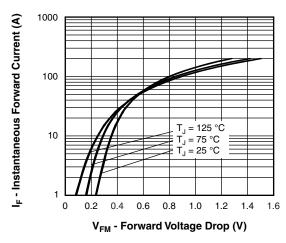
ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS				
		19 A	T _{.1} = 25 °C	-	0.41	V			
Forward voltage drop	V _{FM} ⁽¹⁾	40 A	1J=25 C	-	0.52				
See fig. 1	V FM (1)	19 A	T _{.1} = 125 °C	0.25	0.33				
		40 A	1J = 125 C	0.37	0.50				
Reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	-	10	mA			
See fig. 2		T _J = 100 °C	V _R = nateu V _R	-	600				
Threshold voltage	V _{F(TO)}	$T_1 = T_1 \text{ maximum}$							
Forward slope resistance	r _t	ij=ijmaximum	7.	.6	mΩ				
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	-	2000	pF				
Typical series inductance	L _S	Measured lead to lead 5 n	8	-	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10	000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature range	TJ		-55 to +125	°C				
Maximum storage temperature range	T _{Stg}		-55 to +150	30				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	1.5					
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased (for TO-220)	0.50	°C/W				
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation (for D ² PAK)	40					
Approximate weight			2	g				
Approximate weight			0.07	OZ.				
Mounting torque		Non-lubricated threads	6 (5)	kgf · cm				
Mounting torque maximum		Non-iubricated threads	12 (10)	(lbf \cdot in)				
Marking device		Case style 2L TO-220AC	STPS2	0L15D				





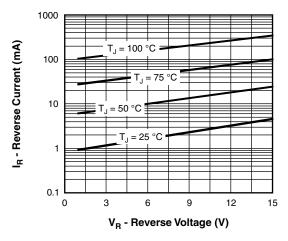


Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

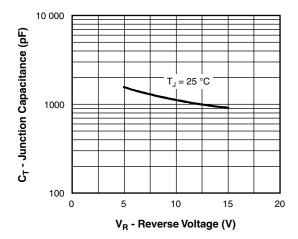


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

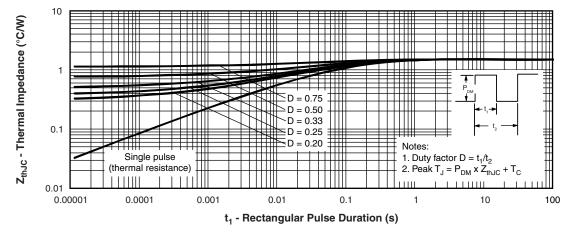


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



Allowable Case Temperature (°C)

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Vishay Semiconductors

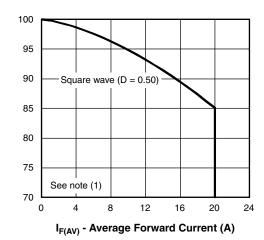


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

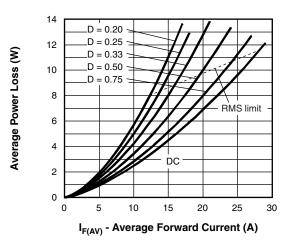


Fig. 6 - Forward Power Loss Characteristics

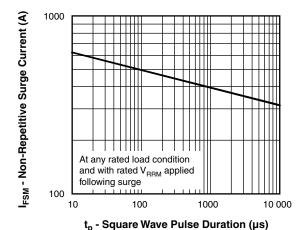


Fig. 7 - Maximum Non-Repetitive Surge Current

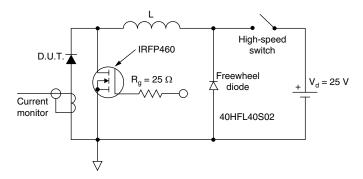


Fig. 8 - Unclamped Inductive Test Circuit

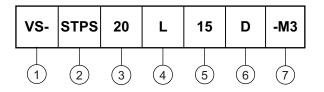
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Schottky STPS series

Current rating (20 = 20 A)

L = Low voltage drop

5 - Voltage rating (15 = 15 V)

6 - D = Essential part number

7 - Environmental digit

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

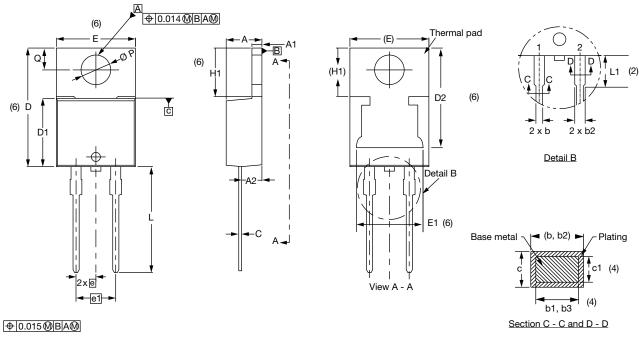
ORDERING INFORMATION (Example)								
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION								
VS-STPS20L15D-M3	50	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?96156</u>						
Part marking information	www.vishay.com/doc?95391					
SPICE model	www.vishay.com/doc?95305					



TO-220AC 2L

DIMENSIONS in millimeters and inches



Lead tip

Conforms to JEDEC® outline TO-220AC

SYMBOL MI	MILLIN	IETERS	INC	HES	NOTES		SYMBOL	MILLIMETERS		INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	JIES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355							•	

Notes

- $^{(1)}$ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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Vishay

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