CHENMKO ENTERPRISE CO.,LTD

CHDTC115EUPT

Lead free devices

SURFACE MOUNT
NPN Digital Silicon Transistor

VOLTAGE 50 Volts CURRENT 20 mAmpere

APPLICATION

* Switching circuit, Inverter, Interface circuit, Driver circuit.

FEATURE

* Small surface mounting type. (SC-70/SOT-323)
* High current gain.
* Suitable for high packing density.
* Low colloector-emitter saturation.
* High saturation current capability.
* Internal isolated NPN transistors in one package.
* Built in bias resistor(R1=100kΩ, Typ.)

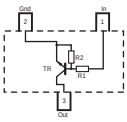
CONSTRUCTION

* One NPN transistors and bias of thin-film resistors in one package.

MARKING

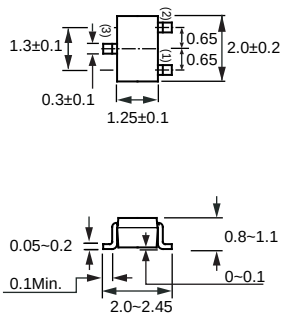
EUF

CIRCUIT





SC-70/SOT-323



Dimensions in millimeters

SC-70/SOT-323

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CC}	Supply voltage		–	50	V
V_{IN}	Input voltage		-10	+40	V
I_O	DC Output current		–	20	mA
$I_{C(Max.)}$			–	100	
P_{TOT}	Total power dissipation	$T_{amb} \leq 25\ ^{\circ}C$, Note 1	–	200	mW
T_{STG}	Storage temperature		–55	+150	°C
T_J	Junction temperature		–	150	°C
$R\theta_{J\text{-}S}$	Thermal resistance	junction - soldering point	–	150	°C/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC (CHDTC115EUPT)

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise speciÞed.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{I(off)}$	Input off voltage	I_O=100uA; V_{CC}=5.0V	0.5	–	–	V
$V_{I(on)}$	Input on voltage	I_O=1mA; V_O=0.3V	–	–	3.0	V
$V_{O(on)}$	Output voltage	I_O=5mA; I_I=0.25mA	–	0.1	0.3	V
I_I	Input current	V_I=5V	–	–	0.15	mA
$I_{O(off)}$	Output current	V_I=0V; V_{CC}=50V	–	–	0.5	uA
h_{FE}	DC current gain	I_O=5mA; V_O=5.0V	82	–	–	
R_1	Input resistor		70	100	130	KΩ
R_2/R_1	Resistor ratio		0.8	1.0	1.2	
f_T	Transition frequency	I_C=5mA, V_{CE}=10.0V f=100MHz	–	250	–	MHz

Note

1.Pulse test: tp≤300uS; δ≤0.02.

RATING CHARACTERISTIC CURVES (CHDTC115EUPT)

Typical Electrical Characteristics

Fig.1 Input voltage vs. output current (ON characteristics)

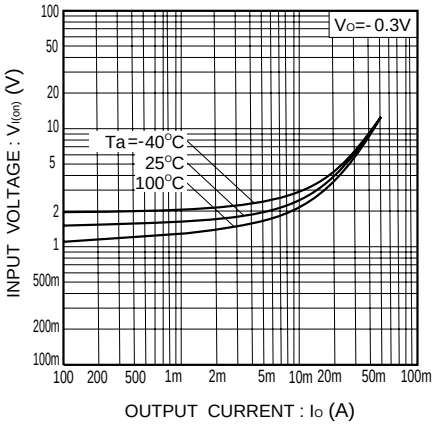


Fig.2 Output current vs. input voltage (OFF characteristics)

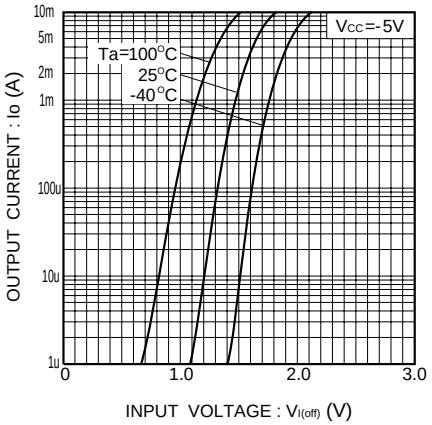


Fig.3 DC current gain vs. output current

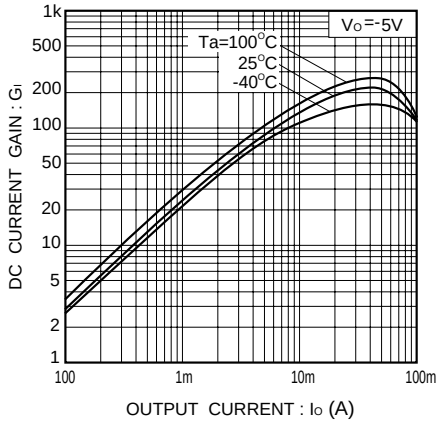


Fig.4 Output voltage vs. output current

