



## 256K x 32 SRAM MODULE

### SYS32256ZK/LK - 12/15/17

Issue 1.1 : January 1999

#### Description

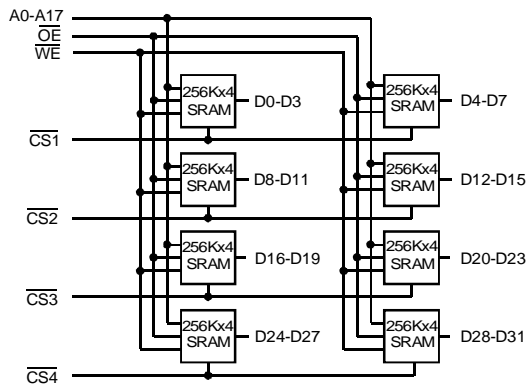
The SYS32256 is a plastic 8Mbit Static RAM Module offered in Industry Standard 64 pin ZIP and 64 lead SIMM package, organised as 256K x 32. The module utilises eight fast 256K x 4 SRAMs housed in SOJ packages, and uses double sided surface mount techniques, to achieve a very high density module.

Four chip selects are used to independently enable the four bytes. Reading or Writing is executed on individual or any combination of multiple bytes. Two pins PD0 & PD1 are used to identify module memory density where alternative versions of the JEDEC standard modules can be interchanged.

#### Features

- Access Times of 12/15/17 ns.
- 64 Pin ZIP & SIMM pinouts.
- 5 Volt Supply  $\pm 10\%$ .
- Power Dissipation :  
Operating (32bit mode) 9.0 W (maximum).  
Standby (CMOS) -L 1.3 W (maximum).
- Completely Static Operation.
- Equal Access and Cycle Times.
- All Inputs and Outputs Directly TTL Compatible.
- On-board Supply Decoupling Capacitors.
- 72 pin SIMM (SYS32256LKX) available.

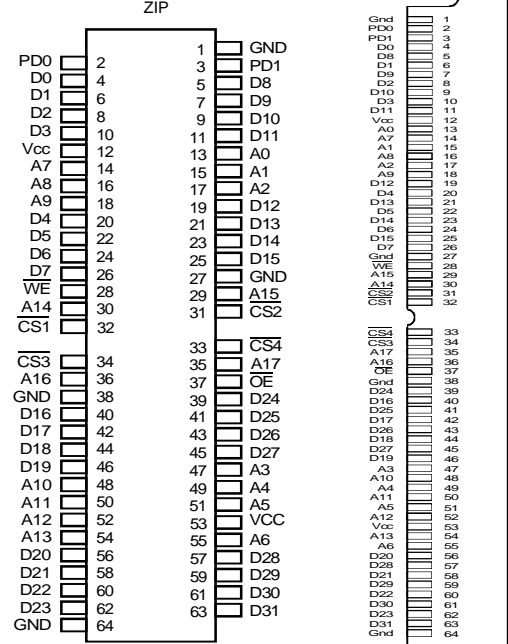
#### Block Diagram



#### Pin Functions

Address Inputs	<b>A0 ~ A17</b>
Data Input/Output	<b>D0 ~ D31</b>
Chip Select	<b>CS1~4</b>
Write Enable	<b>WE</b>
Output Enable	<b>OE</b>
Presence Detect	<b>PD0~1</b>
Power (+5V)	<b>V<sub>CC</sub></b>
Ground	<b>GND</b>

#### Pin Definition

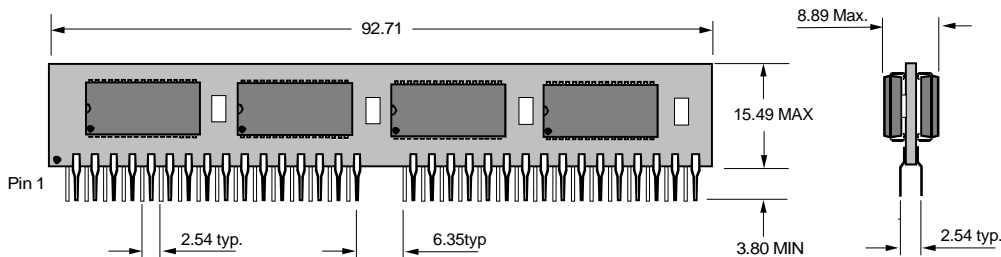


#### Package Details

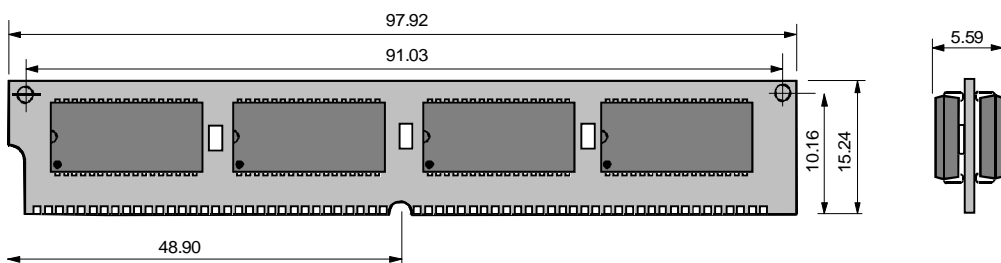
Plastic 64 Pin ZIP  
Plastic 64 Pin SIMM

**Package Information**      Dimensions in mm

**Plastic 64 Pin Zig-Zag-In-line Package (ZIP)**

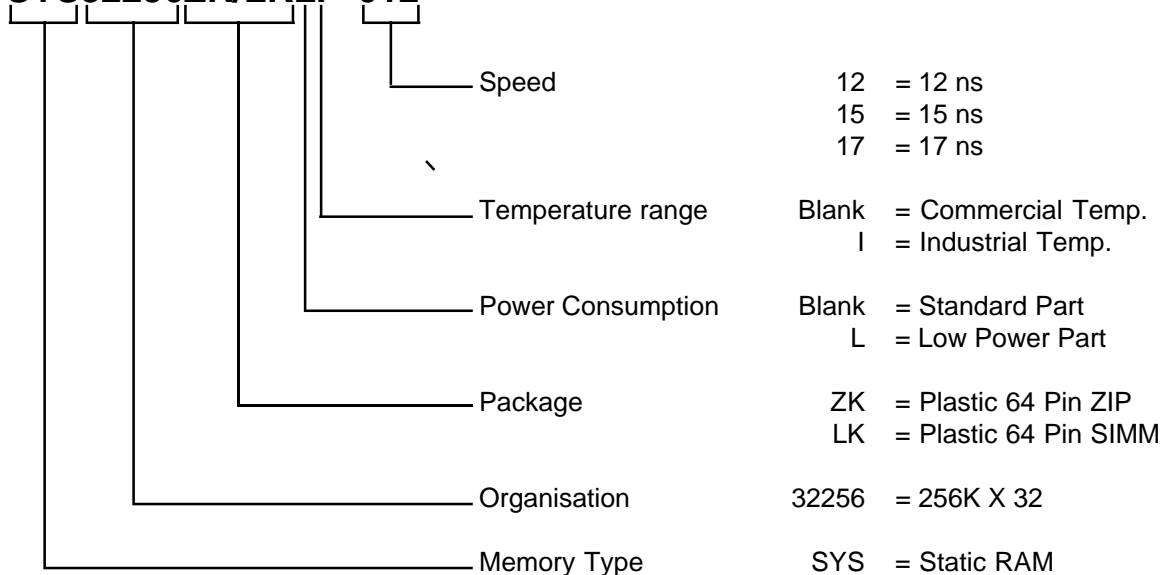


**Plastic 64 Pin Single In-line Memory Module (SIMM)**



**Ordering Information**

**SYS32256ZK/LKLI - 012**



**Note :**

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